





AVX Surface Mount Tantalum Capacitors

156 5V

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Introduction

AVX Tantalum



AVX Paignton is the Divisional Headquarters for the Tantalum division which has manufacturing locations in Paignton in the UK, Biddeford in Maine, USA, Juarez in Mexico, Lanskroun in the Czech Republic and El Salvador.

The Division takes its name from the raw material used to make its main products, Tantalum Capacitors. Tantalum is

an element extracted from ores found alongside tin and niobium deposits; the major sources of supply are Canada, Brazil and Australasia.

So for high volume tantalum capacitors with leading edge technology call us first - **AVX your global partner**.

TECHNOLOGY TRENDS

The amount of capacitance possible in a tantalum capacitor is directly related to the type of tantalum powder used to manufacture the anode.

The graph following shows how the CV/g has steadily increased over time, thus allowing the production of larger and larger capacitances with the same physical volume. CV/g is the measure used to define the volumetric efficiency of a powder, a high CV/g means a higher capacitance from the same volume.

These improvements in the powder have been achieved through close development with the material suppliers.

AVX Tantalum is committed to driving the available technology forwards as is clearly identified by the new TACmicrochip technology and the standard codes under development.

If you have any specific requirements, please contact your local AVX sales office for details on how AVX Tantalum can assist you in addressing your future requirements.

WORKING WITH THE CUSTOMER - ONE STOP SHOPPING

In line with our desire to become the number one supplier in the world for passive and interconnection components, AVX constantly feels the need to look forward and innovate.

It is not good enough to market the best products, the customer must have access to a service system which suits their needs and benefits their business.

The AVX 'one stop shopping' concept is already beneficial in meeting the needs of major OEMs while worldwide partnerships with only the premier division of distributors aids the smaller user.

Helping to market the breadth and depth of our electronic component line card and support our customers are a dedicated team of commercial sales people, applications engineers and product marketing managers. Their qualifica-



Tantalum Powder CV/qm

tions are hopefully always appropriate to your commercial need, but as higher levels of technical expertise are required, access directly to the appropriate department is seamless and transparent.

Total quality starts and finishes with our customer service, and where cost and quality are perceived as given quantities the AVX service invariably has us selected as the preferred supplier.

Facilities are equipped with instant worldwide computer and telecommunication links connected to every sales and production site worldwide. That ensures that our customers delivery requirements are consistently met wherever in the world they may be.



Introduction

AVX Tantalum



APPLICATIONS



QUALITY STATEMENTS

AVX's focus is CUSTOMER satisfaction - customer satisfaction in the broadest sense: product quality, technical support, product availability and all at a competitive price.

In pursuance of the ethos and established goals of our corporate wide QV2000 program, it is the stated objective of AVX Tantalum to supply our customers with a world class service in the manufacturing and supplying of electronic components which will result in an adequate return on investment.

This world class service shall be defined as consistently supplying product and services of the highest quality and reliability.

This should encompass, but not be restricted to all aspects of the customer supply chain.

In addition any new or changed products, processes or services will be qualified to established standards of quality and reliability.

The objectives and guidelines listed above shall be achieved by the following codes of practice:

1. Continual objective evaluation of customer needs and expectations for the future and the leverage of all AVX resources to meet this challenge.

2. By continually fostering and promoting culture of continuous improvement through ongoing training and empowered participation of employees at all levels of the company.

3. By Continuous Process Improvement using sound engineering principles to enhance existing equipment, material and processes. This will involve the application of the science of S.P.C. focused on improving the Process Capability Index, Cpk.

All AVX Tantalum manufacturing locations are ISO9000 approved and Paignton is approved to QS9000 - Automotive Quality System Requirements.

TAJ Series





The TAJ standard series encompasses the five key sizes recognized by major OEMs throughout the world. The V case size has been added to the TAJ range to allow high CVs to be offered. The operational temperature is -55°C to +85°C at rated voltage and up to +125°C with voltage derating in applications utilizing recommended series resistance. TAJ is available in standard and extended ranges.

3216=1206 a型 3528=1210 b型

CASE DIMENSIONS: millimeters (inches)



Code	EIA Code	W+0.2 (0.008) -0.1 (0.004)	L±0.2 (0.008)	H+0.2 (0.008) -0.1 (0.004)	W ₁ ±0.2 (0.008)	A+0.3 (0.012) -0.2 (0.008)	S Min.				
Α	3216	1.6 (0.063)	3.2 (0.126)	1.6 (0.063)	1.2 (0.047)	0.8 (0.031)	1.1 (0.043)				
В	3528	2.8 (0.110)	3.5 (0.138)	1.9 (0.075)	2.2 (0.087)	0.8 (0.031)	1.4 (0.055)				
С	6032	3.2 (0.126)	6.0 (0.236)	2.6 (0.102)	2.2 (0.087)	1.3 (0.051)	2.9 (0.114)				
D	7343	4.3 (0.169)	7.3 (0.287)	2.9 (0.114)	2.4 (0.094)	1.3 (0.051)	4.4 (0.173)				
E	7343H	4.3 (0.169)	7.3 (0.287)	4.1 (0.162)	2.4 (0.094)	1.3 (0.051)	4.4 (0.173)				
V 6.1 (0.240) 7.3 (0.287) 3.45±0.3 (0.136±0.012) 3.1 (0.120) 1.4 (0.055) 3.4 (0.13)											
	W1 dimension applies to the termination width for A dimensional area only.										

HOW TO ORDER





See table above

106 **Capacitance Code**

pF code: 1st two digits represent significant figures 3rd digit represents multiplier (number of zeros to follow)



Tolerance K=±10% M=±20%



R



Packaging Consult page 42 for details



TECHNICAL SPECIFICATIONS

Technical Data:	All technical data relate to an ambient temperature of +25°C									
Capacitance Range:		0.1µ	F to 470µ	١F						
Capacitance Tolerance:		±209	%; ±10%							
Rated Voltage (V _R)	≦ +85°C:	2	4	6.3	10	16	20	25	35	50
Category Voltage (V _C)	≦ +125°C:	1.3	2.7	4	7	10	13	17	23	33
Surge Voltage (V _S)	≦ +85°C:	2.7	5.2	8	13	20	26	32	46	65
Surge Voltage (V _S)	≦ +125°C:	1.7	3.2	5	8	12	16	20	28	40
Temperature Range:		-55°(C to +125	5°C						
Environmental Classification:		55/1	25/56 (IE	C 68-2)						
Reliability	1% per 1000h at 85°C with a 0.1 Ω /V series impedance, 60% confidence level									
Qualification		CEC	C 30801	- 005 iss	sue 1					
		EIA 5	535BAAC	;						





CAPACITANCE AND VOLTAGE RANGE (LETTER DENOTES CASE CODE)

Capad	citance				Rated	voltage (V _R)	at 85°C			
μF	Code	2V	4V	6.3V	10V	16V	20V	25V	35V	50V
0.10 0.15 0.22	104 154 224								A A A	A A/B A/B
0.33 0.47 0.68	334 474 684						A	A A	A A/B A/B	B C C
1.0 1.5 2.2	105 155 225			A	AA	A A A/B	A A A/B	A A/B B A	B A B/C A B/C	C D C D
3.3 4.7 6.8	335 475 685		A A	A A A/B	A A/B A/B	A/B B A B/C A	A/B B/C A B/C	B/C C B C B	C B C/D B D C	D D D
10 15 22	106 156 226		A B A A	A/B B A B/C A	B/C A B/C A C B A	B/C A C B C/D B	CB C/DB DCB	C/D D D C	D C D C E D	E
33 47 68	336 476 686	А	A/B B <mark>A</mark> B/C	C B A C/D B C/D B	C/D B D C B D C	DCB DC DC	D C D C E D	E D D E	DE	
100 150 220	107 157 227	В	B/C B C/D	D C B C/D C/D	D C E D E D	E D D/V V D/E	V D/E E			
330 470 680	337 477 687	С	E	E/VD	D/E/ V E V	E V				
1000 1500	108 158	D E	E							

= Standard Range

= Extended Range

= Development Range



RATINGS & PART NUMBER REFERENCE

	AVX Part No.	Case Size	Capacitance µF	DCL (µA) Max.	DF % Max.	ESR max. (Ω) @ 100 kHz	AVX Part No.	Case Size	Capacitance µF	DCL (µA) Max.	DF % Max.	ESR max. (Ω) @ 100 kHz
	2 v	olt @	85°C (1.2	volt @ 12	25°C)	1	10 \	volt @	85°C (6.3	volt @ 1	25°C)	
	TAJA476*002	Α	47	0.9	6	3.0	TAJA155*010	A	1.5	0.5	6	10.0
	4 v	olt @	85°C (2.5	volt @ 12	25°C)		TAJA225*010 TAJA335*010	A A	2.2 3.3	0.5 0.5	6 6	7.0 5.5
	TAJA475*004 TAJA685*004 TAJA106*004 TAJA156*004 TAJB156*004 TAJB226*004 TAJA226*004 TAJA236*004 TAJB336*004 TAJB476*004 TAJB686*004 TAJC686*004 TAJC686*004 TAJC107*004 TAJC107*004 TAJC227*004	A A A A B A A B B C C C C C	4.7 6.8 10 15 15 22 33 33 47 68 68 100 100 220 220	0.5 0.5 0.5 0.6 0.9 1.3 1.4 1.9 2.7 2.7 4.0 4.0 8.8	6 6 6 6 6 6 6 6 6 6 6 8 8 8 8 8 8	7.5 6.5 6.0 4.0 3.0 3.5 3.0 2.8 2.4 1.8 1.6 1.6 1.3 1.2	TAJA475*010 TAJB475*010 TAJB685*010 TAJB685*010 TAJA106*010 TAJB106*010 TAJC106*010 TAJC106*010 TAJC156*010 TAJC156*010 TAJC226*010 TAJB236*010 TAJC336*010 TAJD336*010	A B A B A B C A B C B C B C D	4.7 4.7 6.8 6.8 10 10 10 15 15 15 22 22 33 33 33	0.5 0.5 0.7 1.0 1.0 1.5 1.5 1.5 2.2 2.2 3.3 3.3 3.3	6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6	5.0 4.0 4.0 3.0 2.5 2.5 3.2 2.8 2.0 2.4 1.8 2.0 1.6 1.1
	TAJE337*004 6.3	E E Volt (220 330 @ 85°C (4	13.2 volt @ 12	8 25°C)	0.9	TAJC476*010 TAJD476*010 TAJC686*010	C D C	47 47 68	4.7 4.7 6.8	6 6 6	1.2 0.9 1.3
	TAJA225*006 TAJA335*006 TAJA685*006 TAJB685*006 TAJB685*006 TAJA106*006 TAJB106*006 TAJB156*006 TAJB156*006 TAJB226*006	A A A B A B A B A B A B A	2.2 3.3 4.7 6.8 6.8 10 10 15 15 15 22 22	0.5 0.5 0.5 0.5 0.6 0.6 1.0 1.0 1.4	6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6	9.0 7.0 6.0 5.0 4.0 4.0 3.0 3.5 2.5 3.0 2.5	TAJC107*010 TAJC107*010 TAJD157*010 TAJE157*010 TAJE227*010 TAJE227*010 TAJE227*010 TAJE337*010 TAJE337*010 TAJE337*010 TAJE477*010	осолеоносо мотернось мотерности мотерно мотерно мотерно моте мотерно мотерно мотерно моте мотерно мотерно моте мотерно	100 100 150 220 220 330 330 330 470	0.8 10.0 15.0 15.0 22.0 22.0 33.0 33.0 33.0 47.0	6 6 8 8 8 8 8 8 8 8 8 8 10	0.9 1.2 0.9 0.9 0.9 0.9 0.9 0.9 0.9 0.9
	TAJC226*006	C	22	1.4	6	2.0	16 י	volt @	85°C (10	volt @ 12	25°C)	-
	TAJC336*006 TAJC476*006 TAJC476*006 TAJC476*006 TAJD476*006 TAJC686*006 TAJC686*006 TAJC107*006 TAJC107*006 TAJC157*006 TAJC157*006 TAJC227*006 TAJC227*006 TAJC237*006 TAJC337*006 TAJC477*006 TAJV477*006	B C B C D B C D C D C D C D E E V	33 47 47 47 68 68 68 100 100 150 150 220 220 330 470 470	2.1 2.1 3.0 3.0 4.3 4.3 4.3 4.3 6.3 9.5 9.5 13.9 13.9 20.8 29.6 29.6	6 6 6 6 6 6 6 6 6 6 10 8 8 10 8	2.2 1.8 2.0 1.6 1.1 1.8 1.6 0.9 1.4 0.9 1.3 0.9 1.2 0.9 0.9 0.9 0.9 0.9 0.9 0.9	TAJA105*016 TAJA225*016 TAJB225*016 TAJB335*016 TAJB335*016 TAJB335*016 TAJB475*016 TAJB475*016 TAJB685*016 TAJC685*016 TAJC685*016 TAJC106*016 TAJC106*016 TAJC156*016 TAJC156*016 TAJC226*016	A A B A B A B C B C B C B C B C	1.0 1.5 2.2 3.3 3.3 4.7 4.7 6.8 6.8 6.8 6.8 10 10 15 15 15 22 22	0.5 0.5 0.5 0.5 0.5 0.8 0.8 1.1 1.1 1.1 1.6 1.6 2.4 2.4 2.5	4 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6	11.0 8.0 6.5 5.5 5.0 4.5 4.0 3.5 2.5 2.5 2.5 2.5 2.8 2.0 2.5 1.8 2.3
l	All technical data relat	les to ar	ambient terr		-25°C measu	ured at	TAJU226*016	D	22	3.5	6	1.0
		les io ar	annoient tem	iperature of 4	rzo u meas	uieu al	TAJC336*016	C	33	5.3	6	1.5

120 Hz, 0.5V RMS unless otherwise stated.

*Insert K for $\pm 10\%$ and M for $\pm 20\%$.

NOTE: We reserve the right to supply higher specification parts in the same case size, to the same reliability standards.

For parametric information on development codes, please contact your local AVX sales office.

33

47

47

68

100

100

150

150

220

5.3

7.5

7.5

10.8

16.0

16.0

24.0

24.0

35.2

6

6

6

6

6

6

6

8

8

0.9

1.4

0.9

0.9

0.9

0.9

0.9

0.9

0.9



TAJD336*016

TAJC476*016

TAJD476*016

TAJD686*016

TAJD107*016

TAJE107*016

TAJD157*016

TAJV157*016

TAJV227*016

D

С

D

D

D

Е

D

V

V



ESR max. (Ω) @ 100 kHz

> 24.0 21.0 18.0 15.0 12.0 10.0 8.0 8.0 7.5 6.5 7.5 5.2 4.5 4.2 3.5 3.5 2.5 3.1 2.2 1.5 1.8 1.3 1.6 1.0 1.4 0.9 0.9 0.9 0.9

> 22.0 15.0 17.0 18.0 14.0 12.0 8.0 7.0 5.5 4.5 4.0 2.5 2.0 1.4 1.0 0.9

RATINGS & PART NUMBER REFERENCE

AVX Part No	Case	Capacitance	DCL (IIA)	DF %	ESR		AVX Part No	Case	Capacitance	DCL	DF %
Tart NO.	0126	μι	Max.	Max.	@ 100 kHz		Tart No.	0120	μι	Max.	Max.
20 י	volt @	85°C (13	volt @ 12	25°C)			35	volt @	85°C (23	volt @ 12	25°C)
TAJA684*020	A	0.68	0.5	4	12.0		TAJA104*035	A	0.1	0.5	4
TAJA105*020	A	1.0	0.5	4	9.0		TAJA154*035	A	0.15	0.5	4
TAJA155*020	A	1.5	0.5	6	6.5		TAJA224*035	A	0.22	0.5	4
TAJA225*020	A	2.2	0.5	6	5.3		TAJA334*035	A	0.33	0.5	4
TAJB225*020	В	2.2	0.5	6	3.5		TAJA474*035	A	0.47	0.5	4
TAJA335*020	A	3.3	0.7	6	4.5		TAJB474*035	В	0.47	0.5	4
TAJB335*020	В	3.3	0.7	6	3.0		TAJA684*035	A	0.68	0.5	4
TAJA475*020	A	4.7	1.0	6	4.0		TAJB684*035	B	0.68	0.5	4
TAJB475*020	B	4.7	1.0	6	3.0		TAJA105*035	A	1.0	0.5	4
TAJC475*020	C	4.7	1.0	6	2.8		TAJB105*035	B	1.0	0.5	4
TAJB685*020	B	6.8	1.4	6	2.5		TAJA155*035	A	1.5	0.5	6
TAJC685*020	С	6.8	1.4	6	2.0		TAJB155*035	B	1.5	0.5	6
TAJB106*020	B	10	2.0	6	2.1		TAJC155*035	C	1.5	0.5	6
IAJC106*020	C	10	2.0	6	1.9		TAJB225*035	B	2.2	0.8	6
TAJB156*020	B	15	3.0	6	2.0		TAJC225*035	C	2.2	0.8	6
TAJC156*020	C	15	3.0	6	2.0		TAJB335*035	B	3.3	1.2	6
TAJD156^020		15	3.0	6	1.1		TAJC335^035		3.3	1.2	6
TAJC226*020		22	4.4	6	1.6		TAJB475^035	B	4.7	1.6	6
TAJD226°020		22	4.4	6	0.9		TAJC475"035		4.7	1.0	6
		33	0.0	6	1.5		TAJD475"035		4.7	1.0	6
TAJD330 020		33	0.0	6	0.9		TAJU000 030		0.0	2.4	6
		47	9.4	6	0.9		TAJD000 000		10.0	2.4	6
TAJD000 020		68	13.0	6	0.9		TAJC100 033		10.0	3.5	6
TA IV107*020		100	20.0	8	0.9		TA IC156*035		15.0	53	6
	v	100	20.0	0	0.0		TAJD156*035		15.0	5.3	6
25 י	volt@	285°C (16	volt @ 12	25°C)			TAJD226*035	D	22.0	7.7	6
TAJA474*025	Α	0.47	0.5	4	14.0		TAJE226*035	E	22.0	7.7	6
TAJA684*025	A	0.68	0.5	4	10.0		TAJD336*035	D	33.0	11.6	6
TAJA105*025	A	1.0	0.5	4	8.0		50 5		95°C (22		25°C)
TAJA155*025	A	1.5	0.5	6	7.5		50		05 0 (55		23 0)
TAJB155*025	В	1.5	0.5	6	5.0		TAJA104*050	A	0.1	0.5	4
TAJA225*025	A	2.2	0.6	6	7.0		TAJA154*050	A	0.15	0.5	4
TAJB225*025	B	2.2	0.6	6	4.5		TAJB154*050	B	0.15	0.5	4
TAJB335*025	B	3.3	0.8	6	3.5		TAJA224*050	A	0.22	0.5	4
TAJC335*025	C	3.3	0.8	6	2.8		TAJB224*050	B	0.22	0.5	4
TAJB475*025	B	4.7	1.2	6	2.8		TAJB334*050	B	0.33	0.5	4
TAJC475^025		4.7	1.2	6	2.4		TAJC474^050	C	0.47	0.5	4
TAJB685^025	B	6.8	1./	6	2.8		TAJC684^050	C	0.68	0.5	4
TAJC685^025		6.8	1./	6	2.0		TAJC105^050	C	1.0	0.5	4
TAJU106*005		10	2.5	0	1.8		TAJU 155"050		1.5	0.8	0
TAJD100 025		10	2.0	6	1.2		TAJD 100 000		1.5	0.0	6
TAJD 100 020		22	5.0	6	1.0		TAJD220 000		2.2	1.1	6
TA ID226*025		22	5.5	6	0.9		TA ID/75*050		4.7	2.4	6
TA.ID336*025		33	8.3	6	0.9		TA.ID685*050		6.8	3.4	6
TAJE336*025	F	33	8.3	6	0.9		TAJE106*050	F	10.0	5.0	8
TAJD476*025	D	47	11.8	6	0.9			mation	on dovelor-	ont ocdor	
							Darametric mitor	man (nn i			

All technical data relates to an ambient temperature of $+25^{\circ}$ C measured at 120 Hz, 0.5V RMS unless otherwise stated.

*Insert K for $\pm 10\%$ and M for $\pm 20\%$.

NOTE: We reserve the right to supply higher specification parts in the same case size, to the same reliability standards.

For parametric information on development codes, please contact your local AVX sales office.

TAJ Series

Low Profile





Three additional case sizes are available in the TAJ range offering low profile solid tantalum chip capacitors. Designed for applications where maximum height of components above or below board are of prime consideration, this height of 1.2mm equates to that of a standard integrated circuit package after mounting. The S&T footprints are identical to the A&B case size parts.



CAS	E DIN	NENSION	S: millimet	ers (inches)	

Code	EIA	W+0.2 (0.008)	L±0.2 (0.008)	H Max.	W ₁ ±0.1 (0.004)	A+0.3 (0.012)	S Min.
	Code	-0.1 (0.004)				-0.1 (0.004)	
R*	2012	1.3 (0.051)	2.05 (0.081)	1.2 (0.047)	1.2 (0.047)	0.5 (0.020)	0.85 (0.033)
S**	3216L	1.6 (0.063)	3.2 (0.126)	1.2 (0.047)	1.2 (0.047)	0.8 (0.031)	1.1 (0.043)
T**	3528L	2.8 (0.110)	3.5 (0.138)	1.2 (0.047)	2.2 (0.087)	0.8 (0.031)	1.4 (0.055)
W	—	3.2 (0.126)	6.0 (0.236)	1.5 (0.059)	2.2 (0.087)	1.3 (0.051)	2.9 (0.114)
Y	_	4.3 (0.169)	7.3 (0.287)	2.0 (0.079)	2.4 (0.094)	1.3 (0.051)	4.4 (0.173)
* 080	5 Equiva	lont					

** Low Profile Versions of A & B Case

 $W_{\rm 1}$ dimension applies to the termination width for A dimensional area only. Pad Stand-off is 0.1±0.1.

CAPACITANCE AND VOLTAGE RANGE (LETTER DENOTES CASE CODE)

Capac	itance	Rated voltage (V _R) at 85°C									
μF	Code	2V	4V	6.3V	10V	16V	20V	25V			
0.10 0.15 0.22	104 154 224						R/S R/S R/S				
0.33 0.47 0.68	334 474 684					R/S	R/S R/S R/S/T				
1.0 1.5 2.2	105 155 225		R/S	R/S R/S	R/S R/S R/S	R/S/T S T	R/S/T T T				
3.3 4.7 6.8	335 475 685	R R	R/S R/S S/T	R/S S/T T	S/T T <mark>R</mark> T	Т		W			
10 15 22	106 156 226	S	R/T	R T	Т	W	W	X Y			
33 47 68	336 476 686			W	ΥW	W X	X Y				
100 150 220	107 157 227	W	W	W X	Y X Y	Y					
330 470 680	337 477 687	W X X/Y	X Y	Y							
1000	108	Y									

= Standard Range

= Development Range

X = 1.5mm height in a D case footprint



Low Profile

RATINGS & PART NUMBER REFERENCE

AVX Part No.	Case Size	Capacitance µF	DCL (µA) Max.	DF % Max.	ESR max. (Ω) @ 100 kHz						
2 volt											
TAJR475*002 TAJR685*002 TAJS106*002	R R S	4.7 6.8 10.0	0.5 0.5 0.5	6 6 6	20.0 20.0 20.0						
		4 vol	t								
TAJR225*004 TAJS225*004 TAJR335*004 TAJS335*004 TAJR475*004 TAJS475*004 TAJS685*004 TAJT685*004 TAJT685*004 TAJT106*004	R S R S R S S T R T	2.2 2.2 3.3 4.7 4.7 6.8 6.8 10.0 10.0	0.5 0.5 0.5 0.5 0.5 0.5 0.5 0.5 0.5 0.5	6 6 6 6 6 6 6	25.0 25.0 20.0 18.0 12.0 10.0 8.0 6.0 10.0 5.0						
		6.3 vc	olt								
TAJR155*006 TAJS155*006 TAJR225*006 TAJR335*006 TAJR335*006 TAJS335*006 TAJS475*006 TAJT475*006 TAJT685*006 TAJT685*006 TAJW336*006	R S R S S T T T W	1.5 1.5 2.2 2.2 3.3 3.3 4.7 4.7 6.8 15.0 33.0	0.5 0.5 0.5 0.5 0.5 0.5 0.5 0.5 0.5 1.0 2.1	6 6 6 6 6 6 6 6 6	25.0 25.0 20.0 18.0 12.0 9.0 7.5 6.0 5.0 4.5 2.0						
		10 vo	lt								
TAJR105*010 TAJS105*010 TAJR155*010 TAJR225*010 TAJS225*010 TAJS335*010 TAJT335*010 TAJT335*010 TAJT475*010 TAJT685*010 TAJT106*010 TAJY686*010 TAJY07*010	R S R S S T T T T Y Y	1.0 1.0 1.5 2.2 2.2 3.3 3.3 4.7 6.8 10.0 68 100	0.5 0.5 0.5 0.5 0.5 0.5 0.5 0.5 1.0 1.0 6.8 10	4 6 6 6 6 6 6 6 6 6 6 6	25.0 25.0 20.0 15.0 12.0 8.0 6.0 5.0 4.0 3.0 0.9 0.9						

All technical data relates to an ambient temperature of +25°C measured at 120 Hz, 0.5V RMS unless otherwise stated.

*Insert K for $\pm 10\%$ and M for $\pm 20\%$.

NOTE: We reserve the right to supply higher specification parts in the same case size, to the same reliability standards.

AVX	Case	Capacitance	DCL	DF	ESR							
Part No.	Size	μF	(µA)	%	max. (Ω)							
			Max.	Max.	@ 100 kHz							
	16 volt											
TAJR684*016	R	0.68	0.5	4	25.0							
TAJS684*016	S	0.68	0.5	4	25.0							
TAJR105*016	R	1.0	0.5	4	20.0							
TAJS105*016	S	1.0	0.5	4	15.0							
TAJT105*016	Т	1.0	0.5	4	15.0							
TAJS155*016	S	1.5	0.5	6	12.0							
TAJT225*016	Т	2.2	0.5	6	6.5							
TAJT335*016	Т	3.3	0.5	6	5.0							
TAJW106*016	W	10.0	1.6	6	2.0							
	I	20 vc	lt									
	R	0.1	0.5	Λ	25.0							
TA IS10//*020	S	0.1	0.5	4	25.0							
TA.IR154*020	B	0.15	0.5	4	25.0							
TA.IS154*020	S	0.10	0.5	4	25.0							
TA.IR224*020	B	0.10	0.5	4	25.0							
TAJS224*020	S	0.22	0.5	4	25.0							
TAJB334*020	R	0.33	0.5	4	25.0							
TAJS334*020	S	0.33	0.5	4	25.0							
TAJR474*020	R	0.47	0.5	4	25.0							
TAJS474*020	S	0.47	0.5	4	25.0							
TAJR684*020	R	0.68	0.5	4	25.0							
TAJS684*020	S	0.68	0.5	4	25.0							
TAJT684*020	Т	0.68	0.5	4	15.0							
TAJR105*020	R	1.0	0.5	4	20.0							
TAJS105*020	S	1.0	0.5	4	12.0							
TAJT105*020	Т	1.0	0.5	4	9.0							
TAJT155*020	Т	1.5	0.5	6	6.5							
TAJT225*020	Т	2.2	0.5	6	6.0							

For parametric information on development codes, please contact your local AVX sales office.

TPS Series

Low ESR





The TPS surface mount products have inherently low ESR (equivalent series resistance) and are capable of higher ripple current handling, producing lower ripple voltages, less power and heat dissipation than standard product for the most efficient use of circuit power. TPS has been designed, manufactured, and

preconditioned for optimum performance in typical power supply applications. By combining the latest improvements in tantalum powder technology, improved manufacturing processes, and application specific preconditioning tests, AVX is able to provide a technologically superior alternative to the standard range.



CASE DIMENSIONS: millimeters (inches)

Μ

Tolerance

K=±10%

M=±20%

Code	EIA Code	W+0.2 (0.008) -0.1 (0.004)	L±0.2 (0.008)	H+0.2 (0.008) -0.1 (0.004)	W ₁ ±0.2 (0.008)	A+0.3 (0.012) -0.2 (0.008)	S Min.
Α	3216	1.6 (0.063)	3.2 (0.126)	1.6 (0.063)	1.2 (0.047)	0.8 (0.031)	1.1 (0.043)
В	B 3528 2.8 (0.110) 3.5 (0.1			1.9 (0.075)	2.2 (0.087)	0.8 (0.031)	1.4 (0.055)
С	6032	3.2 (0.126)	6.0 (0.236)	2.6 (0.102)	2.2 (0.087)	1.3 (0.051)	2.9 (0.114)
D	7343	4.3 (0.169)	7.3 (0.287)	2.9 (0.114)	2.4 (0.094)	1.3 (0.051)	4.4 (0.173)
E	7343H	4.3 (0.169)	7.3 (0.287)	4.1 (0.162)	2.4 (0.094)	1.3 (0.051)	4.4 (0.173)
v		6.1 (0.240)	7.3 (0.287)	3.45 ±0.3 (0.136±0.012)	3.1 (0.120)	1.4 (0.055)	3.4 (0.133)
		W1 dimension	applies to the te	rmination width fo	r A dimensional ar	ea only	

HOW TO ORDER



Case Size See table above

D



107

multiplier (number of zeros to follow)



Packaging Consult page 42 for details

R



*See note below

NOTE: The EIA & CECC standards for low ESR Solid Tantalum Capacitors allow an ESR movement to 1.25 times catalog limit post mounting

TECHNICAL SPECIFICATIONS

Technical Data:	All technical data relate to an ambient temperature of +25°C							
Capacitance Range:		1.5µl	F to 470µ	F				
Capacitance Tolerance:		±20%	%; ±10%					
Rated Voltage (V _R)	≦ +85°C:	6.3	10	16	20	25	35	
Category Voltage (V _C)	≦ +125°C:	4	7	10	13	17	23	
Surge Voltage (V _S)	≦ +85°C:	8	13	20	26	32	46	
Surge Voltage (V _S)	≦ +125°C:	5	8	12	16	20	28	
Temperature Range:		-55°(C to +125	5°C				•
Environmental Classification:	nvironmental Classification: 55/125/56 (IEC 68-2)							
Reliability:		1% per 1000h at 85°C with 0.1 Ω /V series impedance, 60% confidence level						





Low ESR

CAPACITANCE AND VOLTAGE RANGE (LETTER DENOTES CASE CODE)

Capac	itance			Rated voltag	e (V _R) at 85°C		
μF	Code	6.3V	10V	16V	20V	25V	35V
1.5	155					A(3000)	
3.3	335			A(3500)			
4.7	475				A(1800)	B(1500)	C(600)
6.8	685						
10	106		A(1800)		B(1000)	C(500)	D(300) E(200)
15	156	A(1500)	A(1000)	B(800)	C(450)		C(450) D(300)
22	226		B(700)	C(375)		D(200)	D(400) E(200-300)
33	336	B(600)	C(375-500)	C(300)	D(200)	E(175-300)	D(300)
47	476		C(350)	C(350) D(150-200)	E(150)	D(250)	
68	686			D(150)	E(125-150)	V(95-300)	
100	107	C(150)	C(200) D(65-140) E(125)	D(125-150) E(100-150)	V(85-200)		
150	157	D(125)	D(100)	D(150) V(75)			
220	227	D(100)	D (150) E(60-150) V(60)	V(75-150)			
330	337	E(100-150) V(60-100)	D(150) E(60-100) V(60-100)				
470	477	E(50-200) V(55-100)	E(50-200)				

ESR limits quoted in brackets are in milliohms

TPS Series



Low ESR

RATINGS & PART NUMBER REFERENCE

AVX Port No	Case	Capacitance	Rated		DF	ESR Mox (mO)	100kHz Ripple Current (mA) Ratings		A) Ratings
Part NO.	Size	με	voltage	Max.	Max.	@100kHz	25°C	85°C	125°C
TPSA156*006R1500	A	15	6.3	0.9	6	1500	224	200	89
TPSB336*006R0600	В	33	6.3	2.1	6	600	376	337	151
TPSC107*006R0150	С	100	6.3	6.3	6	150	856	766	343
TPSD157*006R0125	D	150	6.3	9.5	6	125	1095	980	438
TPSD227*006R0100	D	220	6.3	13.9	6	100	1225	1095	490
TPSE337*006R0100	E	330	6.3	20.8	8	100	1285	1149	514
TPSE337*006R0125	E	330	6.3	20.8	8	125	1149	1028	460
TPSE337*006R0150	E	330	6.3	20.8	8	150	1049	938	420
TPSV337*006R0060	V	330	6.3	20.8	8	60	2041	1826	816
TPSV337*006R0100	V	330	6.3	20.8	8	100	1581	1414	632
TPSE477*006R0050	E	470	6.3	29.6	10	50	1817	1625	727
TPSE477*006R0100	E	470	6.3	29.6	10	100	1285	1149	514
TPSE477*006R0200	E	470	6.3	29.6	10	200	908	812	363
TPSV477*006R0055	V	470	6.3	29.6	10	55	2132	1907	853
TPSV477*006R0100	V	470	6.3	29.6	10	100	1581	1414	632
TPSA106*010R1800	A	10	10	1.0	6	1800	204	183	82
TPSA156*010R1000	A	15	10	1.5	6	1000	274	245	110
TPSB226*010R0700	В	22	10	2.2	6	700	348	312	139
TPSC336*010R0375	С	33	10	3.3	6	375	542	484	217
TPSC336*010R0500	С	33	10	3.3	6	500	469	420	188
TPSC476*010R0350	С	47	10	4.7	6	350	561	501	224
TPSC107*010R0200	С	100	10	10.0	8	200	742	663	297
TPSD107*010R0065	D	100	10	10.0	6	65	1519	1359	608
TPSD107*010R0080	D	100	10	10.0	6	80	1369	1225	547
TPSD107*010R0100	D	100	10	10.0	6	100	1225	1095	490
TPSD107*010R0125	D	100	10	10.0	6	125	1095	980	438
TPSD107*010R0140	D	100	10	10.0	6	140	1035	926	414
TPSD107*010R0150	D	100	10	10.0	6	150	1000	894	400
TPSE107*010R0125	L E	100	10	10.0	6	125	1149	1028	460
TPSD157*010R0100		150	10	15.0	6	100	1225	1095	490
TPSD227^010R0150		220	10	22.0	8	150	1000	894	400
TPSE227^010R0060		220	10	22.0	8	60	1658	1483	663
TPSE227*010R0100		220	10	22.0	8	100	1285	1149	514
TPSE227*010R0125		220	10	22.0	8	125	1149	1028	460
TPSE227*010R0150		220	10	22.0	8	150	1049	938	420
TPSV227*010R0060		220	10	22.0	8	150	2041	1826	817
TPSD337 010R0150		330	10	33.0	8	150	1000	894	400
TDSE337 010K0060		330	10	33.0	0 0	100	1000	1483	514
TPSU337*010P0060		330	10	33.0	0	60	2041	1149	014 017
TDSV337 01000000	V	330	10	33.0	10	100	1581	1/1/	632
TDSE477*010D0050		470	10	47.0	10	50	1917	1414	707
TPSE4// 010R0050		470	10	47.0	10	100	1017	11/0	121 514
TDSE477 01000100		470	10	47.0	10	200	000	010 010	362
11 3E4// 010R0200		4/0	10	47.0	10	200	900	012	303

TPS Series



Low ESR

RATINGS & PART NUMBER REFERENCE

AVX Dort No	Case	Capacitance	Rated	DCL	DF	ESR	100kHz Ripple Current (mA) Ratings		
Part No.	Size	με	voitage	(µA) Max.	Max.	@100kHz	25°C	85°C	125°C
TPSA335*016R3500	A	3.3	16	0.5	6	3500	146	131	59
TPSB156*016R0800	В	15	16	2.4	6	800	326	292	130
TPSC226*016R0375	С	22	16	3.5	6	375	542	484	217
TPSC336*016R0300	C	33	16	5.3	6	300	606	542	242
TPSC476*016R0350	С	47	16	7.5	6	350	561	501	224
TPSD476*016R0150	D	47	16	7.5	6	150	1000	894	400
TPSD476*016R0200	D	47	16	7.5	6	200	866	775	346
TPSD686*016R0150	D	68	16	10.9	6	150	1000	894	400
TPSD107*016R0125	D	100	16	16.0	6	125	1095	980	438
TPSD107*016R0150	D	100	16	16.0	6	150	1000	894	400
TPSE107*016R0100	E	100	16	16.0	6	100	1285	1149	514
TPSE107*016R0125	E	100	16	16.0	6	125	1149	1028	460
TPSE107*016R0150	E	100	16	16.0	6	150	1049	938	420
TPSD157*016R0150	D	150	16	24.0	8	150	1000	894	400
TPSV157*016R0075	V	150	16	24.0	8	75	1826	1633	730
TPSE227*016R0100	E	220	16	35.2	8	100	1285	1149	514
TPSV227*016R0075	V	220	16	35.2	8	75	1826	1633	730
TPSV227*016R0150	V	220	16	35.2	10	150	1291	1155	516
TPSA475*020R1800	A	4.7	20	0.9	6	1800	204	183	82
TPSB106*020R1000	В	10	20	2.0	6	1000	292	261	117
TPSC156*020R0450	C	15	20	3.0	6	450	494	442	198
TPSD336*020R0200	D	33	20	6.6	6	200	866	775	346
TPSE476*020R0150	E	47	20	9.4	6	150	1049	938	420
TPSE686*020R0125	E	68	20	13.6	6	125	1149	1028	160
TPSE686*020R0150	E	68	20	13.6	6	150	1049	938	420
TPSV107*020R0085	V	100	20	20.0	8	85	1715	1534	686
TPSV107*020R0200	V	100	20	20.0	10	200	1118	1000	447
TPSA155*025R3000	А	1.5	25	0.5	6	3000	158	141	63
TPSB475*025R1500	B	4.7	25	1.2	6	1500	238	213	95
TPSC106*025R0500	C	10	25	2.5	6	500	469	420	188
TPSD226*025R0200	D	22	25	5.5	6	200	866	775	346
TPSE336*025R0175	E	33	25	8.3	6	175	971	868	388
TPSE336*025R0200		33	25	8.3	6	200	908	812	363
TPSE336*025R0300	L E	33	25	8.3	6	300	742	663	297
TPSD476*025R0250	D	47	25	11.8	6	250	775	693	310
TPSV686*025R0095	V	68	25	17.0	8	95	1622	1451	649
TPSV686*025R0150	V	68	25	17.0	10	150	1291	1155	516
TPSV686*025R0300	V	68	25	17.0	10	300	913	816	365
TPSC475*035R0600	C	4.7	35	1.6	6	600	428	383	171
TPSD106*035R0300	D	10	35	3.5	6	300	/07	632	283
TPSE106*035R0200	E	10	35	3.5	6	200	908	812	363
TPSC156*035R0450	C	15	35	5.3	6	450	494	442	198
TPSD156*035R0300	D	15	35	5.3	6	300	/07	632	283
TPSD226*035R0400	D	22	35	7.7	6	400	612	548	245
TPSE226*035R0200	E	22	35	1.7	6	200	908	812	363
TPSE226*035R0300	E	22	35	7.7	6	300	742	663	297
TPSD336*035R0300	D	33	35	11.6	6	300	707	632	283

TACmicrochip





The world's smallest surface mount Tantalum capacitor, small enough to create space providing room for ideas to grow.

TACmicrochip is a major breakthrough in miniaturization without reduction in performance.

It offers you the highest energy store in an 0603 or 0805 case size; enhanced high frequency operation through unique ESR performance with temperature and voltage stability.



CASE DIMENSIONS: millimeters (inches)

Code	EIA Code	W +0.20 (0.008) -0.10 (0.004)	L +0.25 (0.010) -0.15 (0.006)	H +0.20 (0.008) -0.10 (0.004)
L	0603	0.85 (0.033)	1.6 (0.063)	0.85 (0.033)
R	0805	1.35 (0.053)	2.0 (0.079)	1.35 (0.053)

STANDARD CAPACITANCE RANGE (LETTER DENOTES CASE CODE)

Сар	acitance	Rated voltage at 85°C							
μF	Code	2V	3V	4V	6.3V	10V			
0.47	474					L			
0.68	684					L			
1.0	105				L	L			
1.5	155			L	L	L			
2.2	225		L	L	L	L			
3.3	335	L	L	L	L	R			
4.7	475	L	L	L		R			
6.8	685	L	L		R	R			
10.0	106			R	R				
15.0	156		R	R					
22.0	226	R	R						
33.0	336	R	R	R					
47.0	476	R	R						

= Standard Range

= Extended Range

= Development Range



RATINGS AND PART NUMBER REFERENCE

AVX Case Style Size		Capacitance µF@120Hz	Leakage µA (Max)	DF % Max	ESR Max @100kHz			
		(2 vo	lt)					
TAC	0805	22	0.5	8	6			
TAC	0805	33	0.7	8	6			
TAC	0805	47	1.0	8	6			
(3 volt)								
TAC	0805	15	0.5	8	6			
TAC	0805	22	0.7	8	6			
TAC	0805	33	1.0	8	6			
(4 volt)								
TAC	0805	10	0.5	8	6			
TAC	0805	15	0.6	8	6			
TAC	0805	22	0.9	8	6			
		(6.3 ve	olt)					
TAC	0805	6.8	0.5	8	6			
TAC	0805	10	0.6	8	6			
TAC	0805	15	0.9	8	6			
		(10 vc	olt)					
TAC	0805	4.7	0.5	8	6			
TAC	0805	6.8	0.7	8	6			
TAC	0805	10	1.0	8	6			

AVX Style	Case Size	Capacitance µF@120Hz	Leakage µA (Max)	DF % Max	ESR Max @100kHz			
		(2 vo	olt)					
TAC TAC TAC	0603 0603 0603	3.3 4.7 6.8	0.5 0.5 0.5	6 6	10 10 10			
		(3 vo	olt)					
TAC TAC TAC	0603 0603 0603	2.2 3.3 4.7	0.5 0.5 0.5	6 6 6	10 10 10			
(4 volt)								
TAC TAC TAC	0603 0603 0603	1.5 2.2 3.3	0.5 0.5 0.5	6 6 6	10 10 10			
	•	(6.3 \	/olt)					
TAC TAC TAC	0603 0603 0603	1.0 1.5 2.2	0.5 0.5 0.5	6 6 6	10 10 10			
		(10 v	olt)					
TAC TAC TAC TAC	0603 0603 0603 0603	0.47 0.68 1.0 1.5	0.5 0.5 0.5 0.5	6 6 6	12 10 10 10			

HOW TO ORDER





Case Code



pF code: 1st two digits represent significant figures, 3rd digit represents multiplier (number of zeros to follow)



Packaging X=8mm 4-1/4" Tape & Reel R=7" Tape & Reel

Solder Plated





Μ

Tolerance

K=±10%

M=±20%

TACmicrochip





Continued investment in R&D has resulted in AVX introducing revolutionary technology to the tantalum capacitor market.

The new TACmicrochip breaks new ground with the unique structure allowing 10 times more capacitance to be packaged in the 0603 case size than is possible with traditional technology.

Conventional molded tantalum technology results in an increase in ESR for each reduction in case size. Figure 1 shows a reduction in ESR performance of the TACmicrochip compared to the same case size if conventional technology were used.

Figure 2 shows a major leap forward in μ F/mm³ performance. The CV values offered in the 0603 cannot be achieved using conventional molded technology.

These features coupled with the temperature and voltage stability of tantalum, enable system designers to achieve equipment miniaturization without compromising performance, making TACmicrochip the optimum choice for size critical applications.

Enhancing Leakage Current & Battery Efficiency.

As portable electronic equipment becomes an integral part of everyday life, a key design focus becomes the ability to enhance and extend battery efficiency performance. Overall leakage current capability improvements are achieved using the unique TACmicrochip construction technology.

Enhanced ESR & High Frequency Operation.

The radically new construction technique used to manufacture the TACmicrochip eliminates a great many of the parasitic inductance resistance paths inherent in standard molded tantalum capacitors, giving the TACmicrochip an equivalent high frequency performance of larger sized product.

Volumetric Efficiency, Space & Weight Savings.

Achieving the industries highest available capacitance in 0603 case size allows high bulk energy storage with minimal use of valuable circuit board space. Add stable temperature and voltage performance and TACmicrochip becomes your preferred choice of miniature tantalum chip capacitor for size critical applications.







SURFACE MOUNTING CHIP SOLDERING

QUADS



WIRE BONDING WITHIN THE SEMICONDUCTOR CHIP PACKAGE



ARRAYS





OTHER POSSIBLE CONFIGURATIONS FOR THE WAFER CAPACITOR



The manufacturing techniques used to make the TACmicrochip allow AVX to offer various custom options. Some examples of which are shown above. Please contact your local AVX sales office if you have a specific requirement.

TAZ Series





The TAZ molded surface mount series is designed for use in applications utilizing either solder, conductive adhesive or thermal compression bonding techniques. Case sizes (A through H) are compatible with CWR06 pad layouts and are qualified as the CWR09 style.

The two styles are interchangeable per MIL-C-55365/4. Each chip is marked

with polarity, capacitance code and rated voltage. There are three termination finishes available: fused solder plated (standard) ("K" per MIL-C-55365), hot solder dipped ("C") and gold plated ("B"). In addition, the molding compound has been selected to meet the flammability requirements of UL94V-O and outgassing requirements of NASA SP-R-0022A.



NOTE: For solder coated terminations add 0.38 (0.015) max. to length and height dimensions.

Case Code	Width W±0.38 (0.015)	Length L±0.38 (0.015)	Height H±0.38 (0.015)	Term. Width W ₁	Term. Length A+0.13 (0.005)	"S" Min
"Regular"						
А	1.27 (0.050)	2.54 (0.100)	1.27 (0.050)	1.27±0.13 (0.050±0.005)	0.76 (0.030)	0.38 (0.015)
В	1.27 (0.050)	3.81 (0.150)	1.27 (0.050)	1.27±0.13 (0.050±0.005)	0.76 (0.030)	1.65 (0.065)
D	2.54 (0.100)	3.81 (0.150)	1.27 (0.050)	2.41+0.13/-0.25 (0.095+0.005/-0.010)	0.76 (0.030)	1.65 (0.065)
E	2.54 (0.100)	5.08 (0.200)	1.27 (0.050)	2.41+0.13/-0.25 (0.095+0.005/-0.010)	0.76 (0.030)	2.92 (0.115)
F	3.43 (0.135)	5.59 (0.220)	1.78 (0.070)	3.30±0.13 (0.130±0.005)	0.76 (0.030)	3.43 (0.135)
G	2.79 (0.110)	6.73 (0.265)	2.79 (0.110)	2.67±0.13 (0.105±0.005)	1.27 (0.050)	3.56 (0.140)
н	3.81 (0.150)	7.24 (0.285)	2.79 (0.110)	3.68+0.13/-0.51 (0.145+0.005/-0.020)	1.27 (0.050)	4.06 (0.160)

CASE DIMENSIONS: millimeters (inches)

Additional special case sizes are available. Contact your local sales office for details.

TECHNICAL SPECIFICATIONS

Technical Data	All technical data relate to an ambient temperature of +25°C									
Capacitance Range		0.1µF1	0.1µF to 220µF							
Capacitance Tolerance	±20%;	±20%; ±10%								
Rated Voltage (V _R)	≦ +85°C:	4	6.3	10	15	20	25	35	50	
Category Voltage (V _C)	≦ +125°C:	2.7	4	7	10	13	17	23	33	
Surge Voltage (V _S)	≦ +85°C:	5.2	8	13	20	26	33	46	65	
Surge Voltage (V _S)	≦ +125°C:	3.2	5	8	12	16	20	28	40	
Operating Temperature Range		-55°C	to +125°0	C				-		
Reliability 1% per 1000h at 85°C with a 0.1Ω/V series impedance, 60% confidence I					dence level					
Qualification		MIL-C-	55365/4							



TAZ Series



HOW TO ORDER



T	
Capacitance	1
Code	
pF code:	
1st two digits	
represent	
significant	
figures, 3rd	
digit represents	
multiplier	
(number of	
zeros to follow)	

Tolerance J=±5% K=±10% M=±20%

Μ



ion Consult page 44 ded for details

R



*Not applicable to European orders (other endings are assigned by the factory for special customer requirements)

MARKING

The positive end of body has videcon readable polarity bar marking along with the capacitance code and rated work voltage:

- Polarity Stripe (+)
- Capacitance Code
- Voltage Rating

The electrical and mechanical parameters shown on the TAZ series are general.

For specific circuit applications, special screening is available. Please contact AVX if you have special electrical or mechanical requirements.

TYPICAL LEAD FRAME MATERIAL THICKNESSES

Lead Frame: Alloy 194 Thickness: 0.005±0.0002"

- 0000 Fused Solder Plate: (60/40) 60-135 microinches nickel 300±75 microinches fused solder
- 0800 Hot Solder Dipped: (60/40) 50-100 microinches nickel Min. 60 microinches solder
- 0900 Gold Plated: 35-100 microinches nickel 50-75 microinches gold

CAPACITANCE AND VOLTAGE RANGE (LETTER DENOTES CASE CODE)

Capac	itance			I	Rated voltage	e (V _R) at 85°0			
μF	Code	4V	6V	10V	15V	20V	25V	35V	50V
0.1 0.15 0.22	104 154 224							A	A A B
0.33 0.47 0.68	334 474 684				A	A B A	A B	В	B
1.0 1.5 2.2	105 155 225	A	А	A B A	A B	B D B	B D E D	D E	E F F
3.3 4.7 6.8	335 475 685	ΒA	B A D B	D B E D	D B E D	E D E F E	E F G F	F G H	G H
10 15 22	106 156 226	D B E D	E D F E	D F E E	F E G F	G F H G	G H H	Н	
33 47 68	336 476 686	F E G	G H F	G H F G	H H	Н			
100 150 220	107 157 227	H F G	G H	Н					

NOTE: TAZ Standard Range ratings are also available as CWR09 Military parts, see page 22.



= Extended Range



TAZ Series



Standard Range

RATINGS & PART NUMBER REFERENCE (Standard Range and Special Case Sizes Only)

AVX Part No.	Case Size	Capacitance µF	DCL (µA) Max.	DF % Max.	ESR max. (Ω) @ 100 kHz			
4 v	olt @	85°C (2.5	volt @ 12	25°C)	I			
TAZA225(‡)004C* TAZB475(‡)004C* TAZD106(‡)004C* TAZE156(‡)004C* TAZF336(‡)004C* TAZG686(‡)004C* TAZG686(‡)004C*	A B D E F H	2.2 4.7 10.0 15.0 33.0 68.0 100.0	1.0 1.0 1.0 2.0 3.0 4.0	6 6 8 8 10 10	20.0 10.0 5.0 4.0 2.0 1.0			
6.3 volt @ 85°C (4 volt @ 125°C)								
TAZA155(‡)006C* TAZB335(‡)006C* TAZD685(‡)006C* TAZE106(‡)006C* TAZF226(‡)006C* TAZG476(‡)006C* TAZH686(‡)006C*	A B D F G H	1.5 3.3 6.8 10.0 22.0 47.0 68.0	1.0 1.0 1.0 2.0 3.0 4.0	6 6 6 8 10 10	12.0 12.0 12.0 6.0 4.0 2.0 2.0			
10 \	olt @	85°C (6.3	3 volt @ 1	25°C)				
TAZA105(‡)010C* TAZB225(‡)010C* TAZD475(‡)010C* TAZE685(‡)010C* TAZF156(‡)010C* TAZG336(‡)010C* TAZH476(‡)010C*	A B D E F G H	1.0 2.2 4.7 6.8 15.0 33.0 47.0	1.0 1.0 1.0 2.0 3.0 5.0	6 6 6 10 10	18.0 12.0 10.0 4.0 3.0 3.0 2.0			
15	volt @	85°C (10	volt @ 12	25°C)				
TAZA684(‡)015C* TAZB155(‡)015C* TAZD335(‡)015C* TAZE475(‡)015C* TAZF106(‡)015C* TAZG226(‡)015C* TAZH336(‡)015C*	A B D E F G H	0.68 1.5 3.3 4.7 10.0 22.0 33.0	1.0 1.0 1.0 2.0 4.0 5.0	6 6 6 6 8 8	22.0 15.0 10.0 6.0 5.0 3.0 2.0			
20	volt @	85°C (13	volt @ 12	25°C)				
TAZA474(‡)020C* TAZB684(‡)020C* TAZB105(‡)020C* TAZD225(‡)020C* TAZE335(‡)020C* TAZF685(‡)020C* TAZG156(‡)020C* TAZH226(‡)020C*	A B D E F G H	0.47 0.68 1.0 2.2 3.3 6.8 15.0 22.0	1.0 1.0 1.0 1.0 2.0 3.0 4.0	6 6 6 6 6 6 6	20.0 15.0 10.0 8.0 5.0 3.0 2.0			

All technical data relates to an ambient temperature of $+25^{\circ}$ C. Capacitance and DF are measured at 120 Hz, 0.5V RMS with a maximum DC bias of 2.2 volts. DCL is measured at rated voltage after 5 minutes.

 \ddagger Insert J for ±5% tolerance, K for ±10%, M for ±20%

* Insert letter for packing option. See ordering information on page 19.

AVX Part No.	Case Size	Capacitance µF	DCL (µA) Max.	DF % Max.	ESR max. (Ω) @ 100 kHz		
25 volt @ 85°C (16 volt @ 125°C)							
TAZA334(‡)025C* TAZB684(‡)025C* TAZD155(‡)025C* TAZE225(‡)025C* TAZF475(‡)025C* TAZG685(‡)025C* TAZG106(‡)025C* TAZG106(‡)025C*	A B D E F G G H	0.33 0.68 1.5 2.2 4.7 6.8 10.0 15.0	1.0 1.0 1.0 2.0 2.0 3.0 4.0	6 6 6 6 6 6 6 6	25.0 15.0 10.0 8.0 6.0 4.0 3.0 2.0		
35	volt @	85°C (23	volt @ 12	25°C)			
TAZA224(‡)035C* TAZB474(‡)035C* TAZD105(‡)035C* TAZE155(‡)035C* TAZF335(‡)035C* TAZG475(‡)035C* TAZH685(‡)035C*	A B D E F G H	0.22 0.47 1.0 1.5 3.3 4.7 6.8	1.0 1.0 1.0 1.0 2.0 3.0	6 6 6 6 6 6	25.0 20.0 12.0 6.0 6.0 3.0 3.0		
50	volt @	85°C (33	volt @ 12	25°C)			
TAZA104(‡)050C* TAZA154(‡)050C* TAZB224(‡)050C* TAZB334(‡)050C* TAZD684(‡)050C* TAZE105(‡)050C* TAZF155(‡)050C* TAZF225(‡)050C* TAZG335(‡)050C* TAZH475(‡)050C*	A A B B D E F F G H	0.10 0.15 0.22 0.33 0.68 1.0 1.5 2.2 3.3 4.7	1.0 1.0 1.0 1.0 1.0 1.0 2.0 2.0 3.0	6 6 6 6 6 6 6 6	30.0 30.0 25.0 20.0 12.0 10.0 6.0 4.0 2.0		

The electrical and mechanical parameters shown on the TAZ series are general. For special circuit requirements, application specific testing is available. Please contact your local AVX sales office if you have special electrical or mechanical requirements.

DCL, DF and ESR limits are general information only. Contact AVX if your application requires lower or tighter limits.



Extended Range

RATINGS & PART NUMBER REFERENCE

AVX Part No.	Case Size	Capacitance µF	DCL (µA) Max.	DF % Max.	ESR max. (Ω) @ 100 kHz				
4 volt									
TAZA475(‡)004X* TAZB106(‡)004X* TAZD226(‡)004X* TAZE336(‡)004X* TAZF107(‡)004X* TAZG157(‡)004X*	A B D E F G	4.7 10 22 33 100 150	1 1 2 4 6	6 6 8 8 10 10	20 10 10 5 4 2				
		6 vol	t						
TAZA335(‡)006X* TAZB685(‡)006X* TAZD156(‡)006X* TAZE226(‡)006X* TAZF686(‡)006X* TAZG107(‡)006X* TAZH227(‡)006X*	A B D E F G H	3.3 6.8 15 22 68 100 220	1 1 2 4 6 10	6 6 6 8 10 10	18 12 10 4 4 2 1				
		10 vo	lt						
TAZA225(‡)010X* TAZB475(‡)010X* TAZD685(‡)010X* TAZD106(‡)010X* TAZE156(‡)010X* TAZE226(‡)010X* TAZF476(‡)010X* TAZG686(‡)010X* TAZH107(‡)010X*	A B D D E E F G H	2.2 4.7 6.8 10 15 22 47 68 100	1 1 2 3 4 6 10	6 6 6 6 8 10 10	20 12 8 10 4 4 3 2 1				

AVX Part No.	Case Size	Capacitance µF	DCL (µA)	DF %	ESR max. (Ω)
		15	iviax.	iviax.	@ TUU KHZ
		15 vo	lt		
TAZA105(‡)015X*	А	1	1	6	22
IAZB335(‡)015X*	В	3.3	1	6	12
TAZD475(‡)015X^		4./		6	10
TAZE100(+)015X		10	2	6	5
TAZF220(+)015X*	Г	68	10	8	2
17211000(+)0107		00	10	0	۷.
		20 vo	lt		
TAZA684(‡)020X* TAZB225(‡)020X* TAZD335(‡)020X* TAZE475(‡)020X* TAZE685(‡)020X* TAZF156(‡)020X* TAZG226(‡)020X* TAZG226(‡)020X*	A B D E E F G H	0.68 2.2 3.3 4.7 6.8 15 22 47	1 1 2 3 4 10	6 6 6 6 6 8 8	22 12 10 8 4 3 2
		25 vo	lt		
TAZB105(‡)025X* TAZD225(‡)025X* TAZE335(‡)025X* TAZF685(‡)025X* TAZH226(‡)025X*	B D F H	1 2.2 3.3 6.8 22	1 1 1 2 6	6 6 6 8	12 10 8 6 2
		35 vo	lt		
TAZH106(‡)035X*	Н	10	4	8	2

 \ddagger Insert J for ±5% tolerance, K for ±10%, M for ±20%

* Insert letter for packing option. See ordering information on page 19.

All technical data relates to an ambient temperature of $\pm 25^{\circ}$ C. Capacitance and DF are measured at 120 Hz, 0.5V RMS with a maximum DC bias of 2.2 volts. DCL is measured at rated voltage after 5 minutes.

The electrical and mechanical parameters shown on the TAZ series are general.

For special circuit requirements, application specific testing is available. Please contact your local AVX sales office if you have special electrical or mechanical requirements.

DCL, DF and ESR limits are general information only. Contact AVX if your application requires lower or tighter limits.

NOTE: Voltage ratings are minimum values. We reserve the right to supply higher voltage ratings in the same case size, to the same reliability standards.

CWR09 Series

MIL-C-55365/4





MARKING



HOW TO ORDER (MIL-C-55365/4)



NOTES: CWR09 is fully interchangeable with CWR06. Case Sizes correspond to TAZ A through H. Packaging information can be found on page 44.



ELECTRICAL RATINGS FOR CWR09 CAPACITORS

MIL-C-55365/4	Case	Rated	Capacitance	DC L	.eakage (r	nax.)	Dissipa	ation Factor	· (max.)	Max. ESR
Number (See Note)	Sizex	(85°C) (volts)	(μF)	+25°C (μΑ)	+85°C (μΑ)	+125°C (μΑ)	+25°C (%)	+85/125°C (%)	-55°C (%)	+25°C Style CWR09 (Ohms)
CWR09C*225†@∆□	А	4	2.2	1.0	10	12	6	8	8	8.0
CWR09C*475†@∆□	В	4	4.7	1.0	10	12	6	8	8	8.0
CWR09C*685†@∆□	С	4	6.8	1.0	10	12	6	8	8	5.5
CWR09C*106†@△□	D	4	10.0	1.0	10	12	8	8	10	4.0
CWR09C*156†@∆□	E	4	15.0	1.0	10	12	8	10	12	3.5
CWR09C*336†@∆□	F	4	33.0	2.0	20	24	8	10	12	2.2
CWR09C*686†@∆□	G	4	68.0	3.0	30	36	10	12	12	1.1
CWR09C*107†@∆□	Н	4	100.0	4.0	40	48	10	12	12	0.9
CWR09D*155†@∆□	A	6	1.5	1.0	10	12	6	8	8	8.0
CWR09D*335†@∆□	В	6	3.3	1.0	10	12	6	8	8	8.0
CWR09D*475†@∆□	С	6	4.7	1.0	10	12	6	8	8	5.5
CWR09D*685†@∆□	D	6	6.8	1.0	10	12	6	8	8	4.5
CWR09D*106†@∆□	E	6	10.0	1.0	10	12	8	10	12	3.5
CWR09D*226†@∆□	F	6	22.0	2.0	20	24	8	10	12	2.2
CWR09D*476†@∆□	G	6	47.0	3.0	30	36	10	12	12	1.1
CWR09D*686†@∆□	Н	6	68.0	4.0	40	48	10	12	12	0.9
CWR09F*105†@∆□	A	10	1.0	1.0	10	12	6	8	8	10.0
CWR09F*225†@∆□	В	10	2.2	1.0	10	12	6	8	8	8.0
CWR09F*335†@∆□	С	10	3.3	1.0	10	12	6	8	8	5.5
CWR09F*475†@∆□	D	10	4.7	1.0	10	12	6	8	8	4.5
CWR09F*685†@∆□	E	10	6.8	1.0	10	12	6	8	8	3.5
CWR09F*156†@△□	F	10	15.0	2.0	20	24	8	8	10	2.5
CWR09F*336†@△□	G	10	33.0	3.0	30	36	10	12	12	1.1
CWR09F*476†@∆□	Н	10	47.0	5.0	50	60	10	12	12	0.9
CWR09H*684†@∆□	A	15	0.68	1.0	10	12	6	8	8	12.0
CWR09H*155†@△□	В	15	1.5	1.0	10	12	6	8	8	8.0
CWR09H*225†@△□	C	15	2.2	1.0	10	12	6	8	8	5.5
CWR09H*335†@△□	D	15	3.3	1.0	10	12	6	8	8	5.0
CWR09H*475†@△□	E	15	4.7	1.0	10	12	6	8	8	4.0
CWR09H*106†@△□	F	15	10.0	2.0	20	24	6	8	8	2.5
CWR09H*226†@AU	G	15	22.0	4.0	40	48	8	8	10	1.1
CWR09H*336†@∆⊔	н	15	33.0	5.0	50	60	8	8	10	0.9
CWR09J*474†@△□	A	20	0.47	1.0	10	12	6	8	8	14.0
CWR09J*684†@∆□	В	20	0.68	1.0	10	12	6	8	8	10.0
CWR09J*105†@∆□	B	20	1.0	1.0	10	12	6	8	8	12.0
	C	20	1.5	1.0	10	12	6	8	8	6.0
CWR09J*225†@∆□		20	2.2	1.0	10	12	6	8	8	5.0
	E	20	3.3	1.0	10	12	6	8	8	4.0
	F	20	6.8	2.0	20	24	6	8	8	2.4
	G	20	15.0	3.0	30	36	6	8	8	1.1
CWR09J^226†@AL	н	20	22.0	4.0	40	48	6	8	8	0.9

* = Termination Finish

B = Gold Plated

C = Hot Solder Dipped

† = Tolerance Code $J = \pm 5\%$

K = Solder Fused

 $K = \pm 10\%$

$M = \pm 20\%$

@ = Failure Rate Level Exponential: M = 1.0% per 1000 hours

P = 0.1% per 1000 hours

- R = 0.01% per 1000 hours
- S = 0.001% per 1000 hours

Weibull:

- B = 0.1% per 1000 hours
- C = 0.01% per 1000 hours

 \triangle = Optional Surge Current A = 10 cycles at 25°C

B = 10 cycles at -55°C and +85°C

□ = Packaging Bulk Standard \TR=7" Tape & Reel \TR13=13" Tape & Reel \W=Waffle Pack

 \bigstar The C case has limited availability. Where possible D case should be substituted.



MIL-C-55365/4

ELECTRICAL RATINGS FOR CWR09 CAPACITORS

MIL-C-55365/4	Case	Rated	Capacitance	DC L	.eakage (n	nax.)	Dissipa	ation Factor	(max.)	Max. ESR
Number (See Note)	Size*	(85°C) (volts)	(μF)	+25°C (μΑ)	+85°C (μΑ)	+125°C (μΑ)	+25°C (%)	+85/125°C (%)	-55°C (%)	+25°C Style CWR09 (Ohms)
CWR09K*334†@∆□	А	25	0.33	1.0	10	12	6	8	8	15.0
CWR09K*684†@∆□	В	25	0.68	1.0	10	12	6	8	8	7.5
CWR09K*105†@∆□	С	25	1.0	1.0	10	12	6	8	8	6.5
CWR09K*155†@∆□	D	25	1.5	1.0	10	12	6	8	8	6.5
CWR09K*225†@∆□	E	25	2.2	1.0	10	12	6	8	8	3.5
CWR09K*475†@∆□	F	25	4.7	2.0	20	24	6	8	8	2.5
CWR09K*685†@∆□	G	25	6.8	2.0	20	24	6	8	8	1.2
CWR09K*106†@∆□	G	25	10.0	3.0	30	36	6	8	8	1.4
CWR09K*156†@∆□	Н	25	15.0	4.0	40	48	6	8	8	1.0
CWR09M*224†@∆□	A	35	0.22	1.0	10	12	6	8	8	18.0
CWR09M*474†@∆□	В	35	0.47	1.0	10	12	6	8	8	10.0
CWR09M*684†@∆□	С	35	0.68	1.0	10	12	6	8	8	8.0
CWR09M*105†@∆□	D	35	1.0	1.0	10	12	6	8	8	6.5
CWR09M*155†@∆□	E	35	1.5	1.0	10	12	6	8	8	4.5
CWR09M*335†@∆□	F	35	3.3	1.0	10	12	6	8	8	2.5
CWR09M*475†@∆□	G	35	4.7	2.0	20	24	6	8	8	1.5
CWR09M*685†@∆□	Н	35	6.8	3.0	30	36	6	8	8	1.3
CWR09N*104†@∆□	A	50	0.10	1.0	10	12	6	8	8	22.0
CWR09N*154†@∆□	A	50	0.15	1.0	10	12	6	8	8	17.0
CWR09N*224†@∆□	В	50	0.22	1.0	10	12	6	8	8	14.0
CWR09N*334†@∆□	В	50	0.33	1.0	10	12	6	8	8	12.0
CWR09N*474†@∆□	С	50	0.47	1.0	10	12	6	8	8	8.0
CWR09N*684†@∆□	D	50	0.68	1.0	10	12	6	8	8	7.0
CWR09N*105†@∆□	E	50	1.0	1.0	10	12	6	8	8	6.0
CWR09N*155†@∆□	F	50	1.5	1.0	10	12	6	8	8	4.0
CWR09N*225†@∆□	F	50	2.2	2.0	20	24	6	8	8	2.5
CWR09N*335†@∆□	G	50	3.3	2.0	20	24	6	8	8	2.0
CWR09N*475†@∆□	Н	50	4.7	3.0	30	36	6	8	8	1.5

NOTE: To complete the MIL-C-55365/4 Part Number, additional information must be added:

Contact your local AVX sales office for latest qualification status.

* = Termination Finish B = Gold Plated C = Hot Solder Dipped K = Solder Fused **† = Tolerance Code** $J = \pm 5\%$ $K = \pm 10\%$ $M = \pm 20\%$

@ = Failure Rate Level

Exponential: M = 1.0% per 1000 hours

- P = 0.1% per 1000 hours
- R = 0.01% per 1000 hours
- S = 0.001% per 1000 hours
- Weibull:
 - B = 0.1% per 1000 hours C = 0.01% per 1000 hours

\triangle = Optional Surge Current A = 10 cycles at 25°C

A = 10 cycles at 25 CB = 10 cycles at -55°C and +85°C Packaging
Bulk Standard

TR=7" Tape & Reel

TR13=13" Tape & Reel

W=Waffle Pack

★ The C case has limited availability. Where possible D case should be substituted.

CWR11 Style

MIL-C-55365/8



MARKING





CASE DIMENSIONS: millimeters (inches)

Case Code	w	L	н	W ₂ ±0.1 (±0.004)	P ±0.3 (±0.012)	H ₂ (min)
Α	1.6±0.2 (0.063±0.008)	3.2±0.2 (0.126±0.008)	1.6±0.2 (0.063±0.008)	1.2 (0.047)	0.8 (0.031)	0.7 (0.028)
В	2.8±0.2 (0.110±0.008)	3.5±0.2 (0.138±0.008)	1.9±0.2 (0.075±0.008)	2.2 (0.087)	0.8 (0.031)	0.7 (0.028)
с	3.2±0.3 (0.126±0.012)	6.0±0.3 (0.236±0.012)	2.5±0.3 (0.098±0.012)	2.2 (0.087)	1.3 (0.0.51)	1.0 (0.039)
D	4.3±0.3 (0.169±0.012)	7.3±0.3 (0.287±0.012)	2.8±0.3 (0.110±0.012)	2.4 (0.094)	1.3 (0.0.51)	1.0 (0.039)

HOW TO ORDER (MIL-C-55365/8)





ELECTRICAL RATINGS FOR CWR11 CAPACITORS

MIL-C-55365/8	Case	Rated	Rated	Capacitance	DC L	.eakage (r	nax.)	Dissipa	ation Factor	r (max.)	Max.
Number (See Note)	Size	(85°C) (volts)	(μF)	+25°C (μΑ)	+85°C (μΑ)	+125°C (μΑ)	+25°C (%)	+85/125°C (%)	-55°C (%)	ESR 100 kHz (Ω)	
CWR11D*155†@∆□	A	6	1.5	0.5	5.0	6.0	6	9	9	8.0	
CWR11D*225†@∆□	A	6	2.2	0.5	5.0	6.0	6	6	9	8.0	
CWR11D*335†@∆□	A	6	3.3	0.5	5.0	6.0	6	9	9	8.0	
CWR11D*475†@∆□	В	6	4.7	0.5	5.0	6.0	6	9	9	5.5	
CWR11D*685†@∆□	В	6	6.8	0.5	5.0	6.0	6	6	9	4.5	
CWR11D*106†@∆□	В	6	10.0	0.6	6.0	7.2	6	9	9	3.5	
CWR11D*156†@∆□	С	6	15.0	0.9	9.0	10.8	6	6	9	3.0	
CWR11D*226†@∆□	С	6	22.0	1.4	14.0	16.8	6	9	9	2.2	
CWR11D*476†@∆□	D	6	47.0	2.8	28.0	33.6	6	6	9	1.1	
CWR11F*105†@∆□	A	10	1.0	0.5	5.0	6.0	4	6	6	10.0	
CWR11F*155†@∆□	A	10	1.5	0.5	5.0	6.0	6	6	9	8.0	
CWR11F*225†@∆□	A	10	2.2	0.5	5.0	6.0	6	9	9	8.0	
CWR11F*335†@∆□	В	10	3.3	0.5	5.0	6.0	6	9	9	5.5	
CWR11F*475†@∆□	В	10	4.7	0.5	5.0	6.0	6	9	9	4.5	
CWR11F*685†@∆□	В	10	6.8	0.7	7.0	8.4	6	9	9	3.5	
CWR11F*156†@∆□	C	10	15.0	1.5	15.0	18.0	6	6	9	2.5	
CWR11F*336†@∆□	D	10	33.0	3.3	33.0	39.6	6	6	9	1.1	
CWR11H*684†@∆□	A	15	0.68	0.5	5.0	6.0	4	6	6	12.0	
CWR11H*105†@∆□	A	15	1.0	0.5	5.0	6.0	4	6	9	10.0	
CWR11H*155†@∆□	A	15	1.5	0.5	5.0	6.0	6	9	9	8.0	
CWR11H*225†@∆□	В	15	2.2	0.5	5.0	6.0	6	9	9	5.5	
CWR11H*335†@∆□	В	15	3.3	0.5	5.0	6.0	6	8	9	5.0	
CWR11H*475†@∆□	В	15	4.7	0.7	7.0	8.4	6	9	9	4.0	
CWR11H*106†@△□	С	15	10.0	1.6	16.0	19.2	6	8	9	2.5	
CWR11H*226†@∆□	D	15	22.0	3.3	33.0	39.6	6	8	9	1.1	
CWR11J*474†@∆□	A	20	0.47	0.5	5.0	6.0	4	6	6	14.0	
CWR11J*684†@∆□	A	20	0.68	0.5	5.0	6.0	4	6	6	12.0	
CWR11J*105†@∆□	A	20	1.0	0.5	5.0	6.0	4	6	6	10.0	
CWR11J*155†@∆□	В	20	1.5	0.5	5.0	6.0	6	9	9	6.0	
CWR11J*225†@∆□	В	20	2.2	0.5	5.0	6.0	6	8	9	5.0	
CWR11J*335†@∆□	В	20	3.3	0.7	7.0	8.4	6	9	9	4.0	
CWR11J*475†@∆□	С	20	4.7	1.0	10.0	12.0	6	8	9	3.0	
CWR11J*685†@∆□	С	20	6.8	1.4	14.0	16.8	6	9	9	2.4	
CWR11J*156†@∆□	D	20	15.0	3.0	30.0	36.0	6	8	9	1.1	

NOTE: To complete the MIL-C-55365/8 Part Number, additional information must be added:

	' = Termination Finish	
--	------------------------	--

B = Gold Plated C = Hot Solder Dipped

K = Solder Fused

† = Tolerance Code $J = \pm 5\%$ $K = \pm 10\%$ $M = \pm 20\%$

@ = Failure Rate Level

- Exponential: M = 1.0% per 1000 hours

 - P = 0.1% per 1000 hours R = 0.01% per 1000 hours
 - S = 0.001% per 1000 hours
- Weibull:
 - B = 0.1% per 1000 hours
 - C = 0.01% per 1000 hours D = 0.001% Per 1000 hours

Contact your local AVX sales office for latest qualification status.

 \triangle = Optional Surge Current A = 10 cycles at 25°C B = 10 cycles at -55°C

and +85°C

= Packaging Bulk Standard \TR=7" Tape & Reel \TR13=13" Tape & Reel \W=Waffle Pack



ELECTRICAL RATINGS FOR CWR11 CAPACITORS

MIL-C-55365/8	Case	Rated	Capacitance	DC L	.eakage (n	nax.)	Dissipa	ation Factor	(max.)	Max.
Number (See Note)	Size	(85°C) (volts)	(nom.) (μF)	+25°C (μΑ)	+85°C (μΑ)	+125°C (μΑ)	+25°C (%)	+85/125°C (%)	-55°C (%)	ESR 100 kHz (Ω)
CWR11K*334†@∆□	А	25	0.33	0.5	5.0	6.0	4	6	6	15.0
CWR11K*474†@∆□	А	25	0.47	0.5	5.0	6.0	4	6	6	14.0
CWR11K*684†@∆□	В	25	0.68	0.5	5.0	6.0	4	6	6	7.5
CWR11K*105†@∆□	В	25	1.0	0.5	5.0	6.0	4	6	6	6.5
CWR11K*155†@∆□	В	25	1.5	0.5	5.0	6.0	6	8	9	6.5
CWR11K*225†@∆□	С	25	2.2	0.6	6.0	7.2	6	9	9	3.5
CWR11K*335†@∆□	С	25	3.3	0.9	9.0	10.8	6	8	9	3.5
CWR11K*475†@∆□	С	25	4.7	1.2	12.0	14.4	6	9	9	2.5
CWR11K*685†@∆□	D	25	6.8	1.7	17.0	20.4	6	9	9	1.4
CWR11K*106†@∆□	D	25	10.0	2.5	25.0	30.0	6	8	9	1.2
CWR11M*104†@∆□	А	35	0.10	0.5	5.0	6.0	4	6	6	24.0
CWR11M*154†@∆□	A	35	0.15	0.5	5.0	6.0	4	6	6	21.0
CWR11M*224†@∆□	A	35	0.22	0.5	5.0	6.0	4	6	6	18.0
CWR11M*334†@∆□	A	35	0.33	0.5	5.0	6.0	4	6	6	15.0
CWR11M*474†@∆□	В	35	0.47	0.5	5.0	6.0	4	6	6	10.0
CWR11M*684†@∆□	В	35	0.68	0.5	5.0	6.0	4	6	6	8.0
CWR11M*105†@∆□	В	35	1.0	0.5	5.0	6.0	4	6	6	6.5
CWR11M*155†@∆□	С	35	1.5	0.5	5.0	6.0	6	8	9	4.5
CWR11M*225†@∆□	С	35	2.2	0.8	8.0	9.6	6	8	9	3.5
CWR11M*335†@∆□	С	35	3.3	1.2	12.0	14.4	6	8	9	2.5
CWR11M*475†@∆□	D	35	4.7	1.7	17.0	20.4	6	8	9	1.5
CWR11N*104†@∆□	А	50	0.10	0.5	5.0	6.0	4	6	6	22.0
CWR11N*154†@∆□	В	50	0.15	0.5	5.0	6.0	4	6	6	17.0
CWR11N*224†@∆□	В	50	0.22	0.5	5.0	6.0	4	6	6	14.0
CWR11N*334†@∆□	В	50	0.33	0.5	5.0	6.0	4	6	6	12.0
CWR11N*474†@∆□	С	50	0.47	0.5	5.0	6.0	4	6	6	8.0
CWR11N*684†@∆□	С	50	0.68	0.5	5.0	6.0	6	6	6	7.0
CWR11N*105†@∆□	С	50	1.0	0.5	5.0	6.0	6	6	6	6.0
CWR11N*155†@∆□	D	50	1.5	0.8	8.0	9.6	6	8	9	4.0
CWR11N*225†@∆□	D	50	2.2	1.1	11.0	13.2	6	8	9	2.5

NOTE: To complete the MIL-C-55365/8 Part Number, additional information must be added:

† = Tolerance Code

 $J=\pm5\%$

 $K = \pm 10\%$

 $M = \pm 20\%$

* = Termination Finish

Designator:

B = Gold Plated

- C = Hot Solder Dipped K = Solder Fused

@ = Failure Rate Level

Exponential: M = 1.0% per 1000 hours P = 0.1% per 1000 hours R = 0.01% per 1000 hours S = 0.001% per 1000 hours

Weibull: B = 0.1% per 1000 hours

C = 0.01% per 1000 hours D = 0.001% per 1000 hours

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 \triangle = Optional Surge Current A = 10 cycles at 25°C B = 10 cycles at -55°C and +85°C

□ = Packaging Bulk Standard \TR=7" Tape & Reel \TR13=13" Tape & Reel \W=Waffle Pack



INTRODUCTION

Tantalum capacitors are manufactured from a powder of pure tantalum metal. The typical particle size is between 2 and 10 $\mu m.$







Figure 1.

20000µFV

The powder is compressed under high pressure around a Tantalum wire to form a 'pellet' (known as the Riser Wire). The riser wire is the anode connection to the capacitor.

This is subsequently vacuum sintered at high temperature (typically 1500 - 2000°C). This helps to drive off any impurities within the powder by migration to the surface.

During sintering the powder becomes a sponge like structure with all the particles interconnected in a huge lattice.

This structure is of high mechanical strength and density, but is also highly porous giving a large internal surface area (see Figure 2).

The larger the surface area the larger the capacitance. Thus high CV (capacitance/voltage product) powders, which have a low average particle size, are used for low voltage, high capacitance parts. The figure below shows typical powders. Note the very great difference in particle size between the powder CVs.

By choosing which powder is used to produce each capacitance/voltage rating the surface area can be controlled.

The following example uses a $22\mu\text{F}$ 25V capacitor to illustrate the point.

$$C = \frac{\varepsilon_0 \varepsilon_\gamma A}{d}$$

where ε_0 is the dielectric constant of free space (8.855 x 10⁻¹² Farads/m)

 ε_{γ} is the relative dielectric constant for Tantalum Pentoxide (27)

d is the dielectric thickness in meters (for a typical 25V part)

C is the capacitance in Farads and A is the surface area in meters Rearranging this equation gives:

$$A = \frac{Cd}{\varepsilon_0 \varepsilon_r}$$

thus for a 22μ F/25V capacitor the surface area is 150 square centimeters, or nearly half the size of this page.

The dielectric is then formed over all the tantalum surfaces by the electrochemical process of anodization. To achieve this, the 'pellet' is dipped into a very weak solution of phosphoric acid.

The dielectric thickness is controlled by the voltage applied during the forming process. Initially the power supply is kept in a constant current mode until the correct thickness of dielectric has been reached (that is the voltage reaches the 'forming voltage'), it then switches to constant voltage mode and the current decays to close to zero.



Figure 2. Sintered Tantalum

The chemical equations describing the process are as follows:

Anode:	2 Ta \rightarrow 2 Ta ⁵⁺ + 10 e
	2 Ta ⁵ + 10 OH- \rightarrow Ta ₂ O ₅ + 5 H ₂ O
Cathode:	$10 \text{ H}_{2}\text{O} - 10 \text{ e} \rightarrow 5 \text{H}_{2} \uparrow + 10 \text{ OH}^{2}$

The oxide forms on the surface of the Tantalum but it also grows into the metal. For each unit of oxide two thirds grows out and one third grows in. It is for this reason that there is a limit on the maximum voltage rating of Tantalum capacitors with present technology powders (see Figure 3).

The dielectric operates under high electrical stress. Consider a 22 μF 25V part:

Formation voltage

- = Formation Ratio x Working Voltage
- = 4 x 25
- = 100 Volts



The pentoxide (Ta_2O_5) dielectric grows at a rate of 1.7 x $10^{\text{-9}}$ m/V

Dielectric thickness (d)

= 100 x 1.7 x 10⁻⁹ = 0.17 μm

Electric Field strength

Working Voltage / d147 KV/mm



Figure 3. Dielectric Layer

The next stage is the production of the cathode plate. This is achieved by pyrolysis of Manganese Nitrate into Manganese Dioxide.

The 'pellet' is dipped into an aqueous solution of nitrate and then baked in an oven at approximately 250°C to produce the dioxide coat. The chemical equation is:

$$Mn (NO_3)_2 \rightarrow Mn O_2 + 2NO_2^{\uparrow}$$



Figure 4. Manganese Dioxide Layer

This process is repeated several times through varying specific densities of nitrate to build up a thick coat over all internal and external surfaces of the 'pellet', as shown in Figure 4.

The 'pellet' is then dipped into graphite and silver to provide a good connection to the Manganese Dioxide cathode plate. Electrical contact is established by deposition of carbon onto the surface of the cathode. The carbon is then coated with a conductive material to facilitate connection to the cathode termination. Packaging is carried out to meet individual specifications and customer requirements. This manufacturing technique is adhered to for the whole range of AVX tantalum capacitors, which can be subdivided into four basic groups: Chip / Resin dipped / Rectangular boxed / Axial.

Further information on the production of Tantalum Capacitors can be obtained from the technical paper "Basic Tantalum Technology", by John Gill, available from your local AVX representative.







SECTION 1 ELECTRICAL CHARACTERISTICS AND EXPLANATION OF TERMS

1.1 CAPACITANCE

1.1.1 Rated capacitance (C_R).

This is the nominal rated capacitance. For tantalum capacitors it is measured as the capacitance of the equivalent series circuit at 20°C using a measuring bridge supplied by a 0.5Vpk-pk 120Hz sinusoidal signal, free of harmonics with a maximum bias of 2.2Vd.c.

1.1.2 Capacitance tolerance.

This is the permissible variation of the actual value of the capacitance from the rated value. For additional reading, please consult the AVX technical publication "Capacitance Tolerances for Solid Tantalum Capacitors".

1.1.3 Temperature dependence of capacitance.

The capacitance of a tantalum capacitor varies with temperature. This variation itself is dependent to a small extent on the rated voltage and capacitor size.

1.2 VOLTAGE

1.2.1 Rated d.c. voltage (V_B)

This is the rated d.c. voltage for continuous operation at 85°C.

1.2.2 Category voltage (V_c)

This is the maximum voltage that may be applied continuously to a capacitor. It is equal to the rated voltage up to +85°C, beyond which it is subject to a linear derating, to 2/3 $V_{\rm R}$ at 125°C.

1.2.3 Surge voltage (V_s)

This is the highest voltage that may be applied to a capacitor for short periods of time. The surge voltage may be applied up to 10 times in an hour for periods of up to 30 seconds at a time. The surge voltage must not be used as a parameter in the design of circuits in which, in the normal course of operation, the capacitor is periodically charged and discharged.

85°C		125°C	
Rated Voltage (Vdc.)	Surge Voltage (Vdc.)	Category Voltage (Vdc.)	Surge Voltage (Vdc.)
4	5.2	2.7	3.2
6.3	8	4	5
10	13	7.0	8
16	20	10	12
20	26	13	16
25	32	17	20
35	46	23	28
50	65	33	40

1.2.4 Effect of surges

The solid Tantalum capacitor has a limited ability to withstand voltage and current surges. This is in common with all other electrolytic capacitors and is due to the fact that they operate under very high electrical stress across the dielectric. For example a 25 volt capacitor has an Electrical Field of 147 KV/mm when operated at rated voltage.

1.1.4 Frequency dependence of the capacitance.

The effective capacitance decreases as frequency increases. Beyond 100KHz the capacitance continues to drop until resonance is reached (typically between 0.5 - 5MHz depending on the rating). Beyond the resonant frequency the device becomes inductive.





It is important to ensure that the voltage across the terminals of the capacitor never exceeds the specified surge voltage rating.

Solid tantalum capacitors have a self healing ability provided by the Manganese Dioxide semiconducting layer used as the negative plate. However, this is limited in low impedance applications.

In the case of low impedance circuits, the capacitor is likely to be stressed by current surges. Derating the capacitor by 50% or more increases the reliability of the component. (See Figure 2 page 37). The "AVX Recommended Derating Table" (page 38) summarizes voltage rating for use on common voltage rails, in low impedance applications.

In circuits which undergo rapid charge or discharge a protective resistor of 1Ω /V is recommended. If this is impossible, a derating factor of up to 70% should be used.

In such situations a higher voltage may be needed than is available as a single capacitor. A series combination should be used to increase the working voltage of the equivalent capacitor: For example two 22µF 25V parts in series is equivalent to one 11µF 50V part. For further details refer to J.A. Gill's paper "Investigation into the effects of connecting Tantalum capacitors in series", available from AVX offices worldwide.

NOTE:

While testing a circuit (e.g. at ICT or functional) it is likely that the capacitors will be subjected to large voltage and current transients, which will not be seen in normal use. These conditions should be borne in mind when considering the capacitor's rated voltage for use. These can be controlled by ensuring a correct test resistance is used.

1.2.5 Reverse voltage and Non-Polar operation.

The values guoted are the maximum levels of reverse voltage which should appear on the capacitors at any time. These limits are based on the assumption that the capacitors are polarized in the correct direction for the majority of their working life. They are intended to cover short term reversals of polarity such as those occurring during switching transients of during a minor portion of an impressed waveform. Continuous application of reverse voltage without normal polarization will result in a degradation of leakage current. In conditions under which continuous application of a reverse voltage could occur two similar capacitors should be used in a back-to-back configuration with the negative terminations connected together. Under most conditions this combination will have a capacitance one half of the nominal capacitance of either capacitor. Under conditions of isolated pulses or during the first few cycles, the capacitance may approach the full nominal value.

The reverse voltage ratings are designed to cover exceptional conditions of small level excursions into incorrect polarity. The values quoted are not intended to cover continuous reverse operation. The peak reverse voltage applied to the capacitor must not exceed:

10% of the rated d.c. working voltage to a maximum of 1.0v at 25°C

3% of the rated d.c. working voltage to a maximum of 0.5v at 85°C

1% of the category d.c. working voltage to a maximum of 0.1v at 125°C

1.2.6 Superimposed A.C. Voltage (Vr.m.s.) -Ripple Voltage.

This is the maximum r.m.s. alternating voltage; superimposed on a d.c. voltage, that may be applied to a capacitor. The sum of the d.c. voltage and peak value of the super-imposed a.c. voltage must not exceed the category voltage, Vc.

Full details are given in Section 2.

1.2.7 Forming voltage.

This is the voltage at which the anode oxide is formed. The thickness of this oxide layer is proportional to the formation voltage for a tantalum capacitor and is a factor in setting the rated voltage.

1.3 DISSIPATION FACTOR AND TANGENT OF LOSS ANGLE (TAN δ)

1.3.1 Dissipation factor (D.F.).

Dissipation factor is the measurement of the tangent of the loss angle (tan δ) expressed as a percentage. The measurement of DF is carried out using a measuring bridge which supplies a 0.5Vpk-pk 120Hz sinusoidal signal, free of harmonics with a maximum bias of 2.2Vdc. The value of DF is temperature and frequency dependent.

Note: For surface mounted products the maximum allowed DF values are indicated in the ratings table and it is important to note that these are the limits met by the component AFTER soldering onto the substrate.

1.3.2 Tangent of Loss Angle (tan δ).

This is a measurement of the energy loss in the capacitor. It is expressed as tan δ and is the power loss of the capacitor divided by its reactive power at a sinusoidal voltage of specified frequency. Terms also used are power factor, loss factor and dielectric loss. Cos (90 - δ) is the true power factor. The measurement of tan δ is carried out using a measuring bridge which supplies a 0.5Vpk-pk 120Hz sinusoidal signal, free of harmonics with a maximum bias of 2.2Vdc.



1.3.3 Frequency dependence of Dissipation Factor.

Dissipation Factor increases with frequency as shown in the typical curves:



1.3.4 Temperature dependence of Dissipation Factor.

Dissipation factor varies with temperature as the typical curves show. For maximum limits please refer to ratings tables.



1.4 IMPEDANCE, (Z) AND EQUIVALENT SERIES RESISTANCE (ESR)

1.4.1 Impedance, Z.

This is the ratio of voltage to current at a specified frequency. Three factors contribute to the impedance of a tantalum capacitor; the resistance of the semiconductor layer; the capacitance value and the inductance of the electrodes and leads.

At high frequencies the inductance of the leads becomes a limiting factor. The temperature and frequency behavior of these three factors of impedance determine the behavior of the impedance Z. The impedance is measured at 20° C and 100kHz.

1.4.2 Equivalent Series Resistance, ESR.

Resistance losses occur in all practical forms of capacitors. These are made up from several different mechanisms, including resistance in components and contacts, viscous forces within the dielectric and defects producing bypass current paths. To express the effect of these losses they are considered as the ESR of the capacitor. The ESR is frequency dependent and can be found by using the relationship;

$$\mathsf{ESR} = \frac{\tan \delta}{2\pi \mathsf{fC}}$$

Where f is the frequency in Hz, and C is the capacitance in farads.

The ESR is measured at 20°C and 100kHz.

ESR is one of the contributing factors to impedance, and at high frequencies (100kHz and above) it becomes the dominant factor. Thus ESR and impedance become almost identical, impedance being only marginally higher.

1.4.3 Frequency dependence of Impedance and ESR.

ESR and Impedance both increase with decreasing frequency. At lower frequencies the values diverge as the extra contributions to impedance (due to the reactance of the capacitor) become more significant. Beyond 1MHz (and beyond the resonant point of the capacitor) impedance again increases due to the inductance of the capacitor.











1.4.4 Temperature dependence of the Impedance and ESR.

At 100kHz, impedance and ESR behave identically and decrease with increasing temperature as the typical curves show.



1.5 D.C. LEAKAGE CURRENT

1.5.1 Leakage current.

The leakage current is dependent on the voltage applied, the elapsed time since the voltage was applied and the component temperature. It is measured at $+20^{\circ}$ C with the rated voltage applied. A protective resistance of 1000Ω is connected in series with the capacitor in the measuring circuit. Three to five minutes after application of the rated voltage the leakage current must not exceed the maximum values indicated in the ratings table. These are based on the formulae 0.01CV or 0.5µA (whichever is the greater).

Reforming of tantalum capacitors is unnecessary even after prolonged storage periods without the application of voltage.

1.5.2 Temperature dependence of the leakage current.

The leakage current increases with higher temperatures, typical values are shown in the graph. For operation between 85°C and 125°C, the maximum working voltage must be derated and can be found from the following formula.

$$Vmax = \left(1 - \frac{(T - 85)}{125}\right) \times V_{R}volts, where T is the required$$

operating temperature.



1.5.3 Voltage dependence of the leakage current.

The leakage current drops rapidly below the value corresponding to the rated voltage $V_{\rm R}$ when reduced voltages are applied. The effect of voltage derating on the leakage current is shown in the graph. This will also give a significant increase in the reliability for any application. See Section 3.1 for details.

LEAKAGE CURRENT vs. RATED VOLTAGE



For additional information on Leakage Current, please consult the AVX technical publication "Analysis of Solid Tantalum Capacitor Leakage Current" by R. W. Franklin.

1.5.4 Ripple current.

The maximum ripple current allowed can be calculated from the power dissipation limits for a given temperature rise above ambient temperature (please refer to Section 2).



LEAKAGE CURRENT vs. BIAS VOLTAGE



SECTION 2

A.C. OPERATION, RIPPLE VOLTAGE AND RIPPLE CURRENT

2.1 RIPPLE RATINGS (A.C.)

In an a.c. application heat is generated within the capacitor by both the a.c. component of the signal (which will depend upon the signal form, amplitude and frequency), and by the d.c. leakage. For practical purposes the second factor is insignificant. The actual power dissipated in the capacitor is calculated using the formula:

and

where

rearranged to I =
$$\sqrt{\frac{\mathbb{P}_{\text{B}}}{\mathbb{P}_{\text{B}}}}$$
(Eq. 1)

and substituting

$$I = \sqrt{(P_R)}$$
$$P = F^2 B$$

$$Z^2$$

- I = rms ripple current, amperes
 - R = equivalent series resistance, ohms
 - E = rms ripple voltage, volts
 - P = power dissipated, watts
 - Z = impedance, ohms, at frequency under consideration

Maximum a.c. ripple voltage (E_{max}).

From the previous equation:

$$E_{max} = Z \sqrt{(P_R)} \dots (Eq. 2)$$

Where P is the maximum permissible power dissipated as listed for the product under consideration (see tables). However care must be taken to ensure that:

- **1.** The d.c. working voltage of the capacitor must not be exceeded by the sum of the positive peak of the applied a.c. voltage and the d.c. bias voltage.
- **2.** The sum of the applied d.c. bias voltage and the negative peak of the a.c. voltage must not allow a voltage reversal in excess of the "Reverse Voltage".

Historical ripple calculations.

Previous ripple current and voltage values were calculated using an empirically derived power dissipation required to give a 10°C rise of the capacitors body temperature from room temperature, usually in free air. These values are shown in Table I. Equation 1 then allows the maximum ripple current to be established, and Equation 2, the maximum ripple voltage. But as has been shown in the AVX article on thermal management by I. Salisbury, the thermal conductivity of a Tantalum chip capacitor varies considerably depending upon how it is mounted.

Table I: Power Dissipation Ratings (In Free Air)

TAJ/TPS/CWR11 Series Molded Chip

Case size	Max. power dissipation (W)	
A	0.075	
В	0.085	
С	0.110	
D	0.150	
E	0.165	
М	0.090	
N	0.130	
R	0.055	
S	0.065	
Т	0.080	
V	0.250	

TAZ/CWR09 Series Molded Chip

Case size	Max. power dissipation (W)
А	0.050
В	0.070
С	0.075
D	0.080
E	0.090
F	0.100
G	0.125
Н	0.150

TAJ/TPS/CWR11 TAZ/CWR09 Series Molded Chip

Temperature		
derating factors		
Temp. °C Factor		
+25	1.0	
+55	0.90	
+85	0.80	
+125	0.16	

Temperature correction factor for ripple current		
Temp. °C Factor		
+25	1.0	
+55 0.95		
+85 0.90		
+125	0.40	

A piece of equipment was designed which would pass sine and square wave currents of varying amplitudes through a biased capacitor. The temperature rise seen on the body for the capacitor was then measured using an infra-red probe. This ensured that there was no heat loss through any thermocouple attached to the capacitor's surface.

Results for the C, D and E case sizes



Several capacitors were tested and the combined results are shown here. All these capacitors were measured on FR4 board, with no other heatsinking. The ripple was supplied at various frequencies from 1KHz to 1MHz.

As can be seen in the figure above, the average ${\rm P}_{\rm max}$ value for the C case capacitors was 0.11 Watts. This is the same as that quoted in Table I.

The D case capacitors gave an average $\rm P_{max}$ value 0.125 Watts. This is lower than the value quoted in the Table I by 0.025 Watts.

The E case capacitors gave an average $\rm P_{max}$ of 0.200 Watts which was much higher than the 0.165 Watts from Table I.

If a typical capacitor's ESR with frequency is considered, e.g. figure below, it can be seen that there is variation. Thus for a set ripple current, the amount of power to be dissipated by the capacitor will vary with frequency. This is clearly shown in figure in top of next column, which shows that the surface temperature of the unit rises less for a given value of ripple current at 1MHz than at 100KHz.

The graph below shows a typical ESR variation with frequency. Typical ripple current versus temperature rise for 100KHz and 1MHz sine wave inputs.







If I²R is then plotted it can be seen that the two lines are in fact coincident, as shown in figure below.



Example

A Tantalum capacitor is being used in a filtering application, where it will be required to handle a 2 Amp peak-to-peak, 200KHz square wave current.

A square wave is the sum of an infinite series of sine waves at all the odd harmonics of the square waves fundamental frequency. The equation which relates is:

 $I_{Square} = I_{pk} \sin (2\pi f) + I_{pk} \sin (6\pi f) + I_{pk} \sin (10\pi f) + I_{pk} \sin (14\pi f) + ...$ Thus the special components are:

Frequency	Peak-to-peak current (Amps)	RMS current (Amps)
200 KHz	2.000	0.707
600 KHz	0.667	0.236
1 MHz	0.400	0.141
1.4 MHz	0.286	0.101

Let us assume the capacitor is a TAJD686M006 Typical ESR measurements would yield.

Frequency	Typical ESR (Ohms)	Power (Watts) Irms ² x ESR
200 KHz	0.120	0.060
600 KHz	0.115	0.006
1 MHz	0.090	0.002
1.4 MHz	0.100	0.001

Thus the total power dissipation would be 0.069 Watts.

From the D case results shown in figure top of previous column, it can be seen that this power would cause the capacitors surface temperature to rise by about 5°C. For additional information, please refer to the AVX technical publication "Ripple Rating of Tantalum Chip Capacitors" by R.W. Franklin.





2.2 Thermal Management

The heat generated inside a tantalum capacitor in a.c. operation comes from the power dissipation due to ripple current. It is equal to I²R, where I is the rms value of the current at a given frequency, and R is the ESR at the same frequency with an additional contribution due to the leakage current. The heat will be transferred from the outer surface by conduction. How efficiently it is transferred from this point is dependent on the thermal management of the board.

The power dissipation ratings given in Section 2.1 are based on free-air calculations. These ratings can be approached if efficient heat sinking and/or forced cooling is used. In practice, in a high density assembly with no specific thermal management, the power dissipation required to give a 10°C rise above ambient may be up to a factor of 10 less. In these cases, the actual capacitor temperature should be established (either by thermocouple probe or infra-red scanner) and if it is seen to be above this limit it may be necessary to specify a lower ESR part or a higher voltage rating.

Please contact application engineering for details or contact the AVX technical publication entitled "Thermal Management of Surface Mounted Tantalum Capacitors" by Ian Salisbury.



Thermal Dissipation from the Mounted Chip

Thermal Impedance Graph with Ripple Current







SECTION 3 RELIABILITY AND CALCULATION OF FAILURE RATE

3.1 STEADY-STATE

Tantalum Dielectric has essentially no wear out mechanism and in certain circumstances is capable of limited self healing. However, random failures can occur in operation. The failure rate of Tantalum capacitors will decrease with time and not increase as with other electrolytic capacitors and other electronic components.



Figure 1. Tantalum Reliability Curve

The useful life reliability of the Tantalum capacitor is affected by three factors. The equation from which the failure rate can be calculated is:

 $F = FU \times FT \times FR \times FB$

where FU is a correction factor due to operating voltage/voltage derating

FT is a correction factor due to operating temperature

FR is a correction factor due to circuit series resistance

FB is the basic failure rate level. For standard Tantalum product this is 1%/1000 hours

Base failure rate.

Standard tantalum product conforms to Level M reliability (i.e., 1%/1000 hrs.) at rated voltage, rated temperature, and 0.1 Ω /volt circuit impedance. This is known as the base failure rate, FB, which is used for calculating operating reliability. The effect of varying the operating conditions on failure rate is shown on this page.

Operating voltage/voltage derating.

If a capacitor with a higher voltage rating than the maximum line voltage is used, then the operating reliability will be improved. This is known as voltage derating.

The graph, Figure 2, shows the relationship between voltage derating (the ratio between applied and rated voltage) and the failure rate. The graph gives the correction factor FU for any operating voltage.

Figure 2. Correction factor to failure rate F for voltage derating of a typical component (60% con. level).



Operating Temperature.

If the operating temperature is below the rated temperature for the capacitor then the operating reliability will be improved as shown in Figure 3. This graph gives a correction factor FT for any temperature of operation.





Circuit Impedance.

All solid tantalum capacitors require current limiting resistance to protect the dielectric from surges. A series resistor is recommended for this purpose. A lower circuit impedance may cause an increase in failure rate, especially at temperatures higher than 20°C. An inductive low impedance circuit may apply voltage surges to the capacitor and similarly a non-inductive circuit may apply current surges to the capacitor, causing localized over-heating and failure. The recommended impedance is 1 Ω per volt. Where this is not feasible, equivalent voltage derating should be used (See MIL HANDBOOK 217E). The graph, Figure 4, shows the correction factor, FR, for increasing series resistance.





Figure 4. Correction factor to failure rate F for series resistance R on basic failure rate FB for a typical component (60% con. level).

Circuit resistance FR ohms/volt	
3.0	0.07
2.0	0.1
1.0	0.2
0.8	0.3
0.6	0.4
0.4	0.6
0.2	0.8
0.1	1.0

Example calculation

Consider a 12 volt power line. The designer needs about 10μ F of capacitance to act as a decoupling capacitor near a video bandwidth amplifier. Thus the circuit impedance will be limited only by the output impedance of the board's power unit and the track resistance. Let us assume it to be about 2 Ohms minimum, i.e. 0.167 Ohms/Volt. The operating temperature range is -25°C to +85°C. If a 10 μ F 16 Volt capacitor was designed in the operating failure rate would be as follows.

a) FT = 1.0 @ 85°C

- b) FR = 0.85 @ 0.167 Ohms/Volt
- c) FU = 0.08 @ applied voltage/rated voltage = 75%

Thus $FB = 1.0 \times 0.85 \times 0.08 \times 1 = 0.068\%/1000$ Hours If the capacitor was changed for a 20 volt capacitor, the operating failure rate will change as shown.

FU = 0.018 @ applied voltage/rated voltage = 60%

 $FB = 1.0 \times 0.85 \times 0.018 \times 1 = 0.0153\%/1000$ Hours

3.2 Dynamic.

As stated in Section 1.2.4, the solid Tantalum capacitor has a limited ability to withstand voltage and current surges. Such current surges can cause a capacitor to fail. The expected failure rate cannot be calculated by a simple formula as in the case of steady-state reliability. The two parameters under the control of the circuit design engineer known to reduce the incidence of failures are derating and series resistance.

The table below summarizes the results of trials carried out at AVX with a piece of equipment which has very low series resistance with no voltage derating applied. That is the capacitor was tested at its rated voltage.

Results of production scale dera	ting experiment
----------------------------------	-----------------

Capacitance and Voltage	Number of units tested	50% derating applied	No derating applied
47µF 16V	1,547,587	0.03%	1.1%
100µF 10V	632,876	0.01%	0.5%
22µF 25V	2,256,258	0.05%	0.3%

As can clearly be seen from the results of this experiment, the more derating applied by the user, the less likely the probability of a surge failure occurring.

It must be remembered that these results were derived from a highly accelerated surge test machine, and failure rates in the low ppm are more likely with the end customer.

A commonly held misconception is that the leakage current of a Tantalum capacitor can predict the number of failures which will be seen on a surge screen. This can be disproved by the results of an experiment carried out at AVX on 47μ F 10V surface mount capacitors with different leakage currents. The results are summarized in the table below.

Leakage current vs number of surge failures

	Number tested	Number failed surge
Standard leakage range 0.1 µA to 1µA	10,000	25
Over Catalog limit 5µA to 50µA	10,000	26
Classified Short Circuit 50µA to 500µA	10,000	25

Again, it must be remembered that these results were derived from a highly accelerated surge test machine, and failure rates in the low ppm are more likely with the end customer.

AVX recommended derating table

Voltage Rail	Working Cap Voltage
3.3	6.3
5	10
10	20
12	25
15	35
≥24	Series Combinations (11)

For further details on surge in Tantalum capacitors refer to J.A. Gill's paper "Surge in solid Tantalum capacitors", available from AVX offices worldwide.

An added bonus of increasing the derating applied in a circuit, to improve the ability of the capacitor to withstand surge conditions, is that the steady-state reliability is improved by up to an order. Consider the example of a 6.3 volt capacitor being used on a 5 volt rail.

The steady-state reliability of a Tantalum capacitor is affected by three parameters; temperature, series resistance and voltage derating. Assume 40°C operation and 0.1 Ohms/Volt series resistance.





The capacitors reliability will therefore be:

- Failure rate = $F_U \times F_T \times F_R \times 1\%/1000$ hours
 - = 0.15 x 0.1 x 1 x 1%/1000 hours = 0.015%/1000 hours

If a 10 volt capacitor was used instead, the new scaling factor would be 0.006, thus the steady-state reliability would be:

- Failure rate = $F_U \times F_T \times F_R \times 1\%/1000$ hours
 - = 0.006 x 0.1 x 1 x 1%/1000 hours
 - $= 6 \times 10^{-4} \% / 1000$ hours

SECTION 4 APPLICATION GUIDELINES FOR TANTALUM CAPACITORS

So there is an order improvement in the capacitors steadystate reliability.

Soldering Conditions and Board Attachment.

The soldering temperature and time should be the minimum for a good connection.

A suitable combination for wavesoldering is 230 - 250°C for 3 - 5 seconds.

For vapor phase or infra-red reflow soldering the profile below shows allowable and dangerous time/temperature combinations. The profile refers to the peak reflow temperature and is designed to ensure that the temperature of the internal construction of the capacitor does not exceed 220°C. Preheat conditions vary according to the reflow system used, maximum time and temperature would be 10 minutes at 150°C. Small parametric shifts may be noted immediately after reflow, components should be allowed to stabilize at room temperature prior to electrical testing.

Both TAJ and TAZ series are designed for reflow and wave soldering operations. In addition TAZ is available with gold terminations compatible with conductive epoxy or gold wire bonding for hybrid assemblies.



Under the CECC 00 802 International Specification, AVX Tantalum capacitors are a Class A component.

The capacitors can therefore be subjected to one IR reflow, one wave solder and one soldering iron cycle.

If more aggressive mounting techniques are to be used please consult AVX Tantalum for guidance.





SECTION 4

APPLICATION GUIDELINES FOR TANTALUM CAPACITORS

Recommended soldering profiles for surface mounting of tantalum capacitors is provided in figure below.



After soldering the assembly should preferably be allowed to cool naturally. In the event that assisted cooling is used, the rate of change in temperature should not exceed that used in reflow.





SECTION 5

MECHANICAL AND THERMAL PROPERTIES OF CAPACITORS

5.1 Acceleration

98.1m/s² (10g)

5.2 Vibration Severity

10 to 2000Hz, 0.75mm of 98.1m/s² (10g)

5.3 Shock

Trapezoidal Pulse, 98.1m/s² for 6ms.

5.4 Adhesion to Substrate

IEC 384-3. minimum of 5N.

5.5 Resistance to Substrate Bending

The component has compliant leads which reduces the risk of stress on the capacitor due to substrate bending.

5.6 Soldering Conditions

Dip soldering is permissible provided the solder bath temperature is \leq 270°C, the solder time < 3 seconds and the circuit board thickness \geq 1.0mm.

5.7 Installation Instructions

The upper temperature limit (maximum capacitor surface temperature) must not be exceeded even under the most unfavorable conditions when the capacitor is installed. This must be considered particularly when it is positioned near components which radiate heat strongly (e.g. valves and power transistors). Furthermore, care must be taken, when bending the wires, that the bending forces do not strain the capacitor housing.

5.8 Installation Position

No restriction.

5.9 Soldering Instructions

Fluxes containing acids must not be used.

5.9.1 Guidelines for Surface Mount Footprints

Component footprint and reflow pad design for AVX capacitors.

The component footprint is defined as the maximum board area taken up by the terminators. The footprint dimensions are given by A, B, C and D in the diagram, which corresponds to W, max., A max., S min. and L max. for the component. The footprint is symmetric about the center lines.

The dimensions x, y and z should be kept to a minimum to reduce rotational tendencies while allowing for visual inspection of the component and its solder fillet.

SECTION 6 EPOXY FLAMMABILITY

EPOXY	UL RATING	OXYGEN INDEX
TAJ	UL94 V-0	35%
TPS	UL94 V-0	35%
TAZ	UL94 V-0	35%



Dimensions PS (Pad Separation) and PW (Pad Width) are calculated using dimensions x and z. Dimension y may vary, depending on whether reflow or wave soldering is to be performed.

For reflow soldering, dimensions PL (Pad Length), PW (Pad Width), and PSL (Pad Set Length) have been calculated. For wave soldering the pad width (PWw) is reduced to less than the termination width to minimize the amount of solder pick up while ensuring that a good joint can be produced.

NOTE: These recommendations (also in compliance with EIA) are guidelines only. With care and control, smaller footprints may be considered for reflow soldering.

Nominal footprint and pad dimensions for each case size are given in the following tables:

PAD DIMENSIONS: millimeters (inches)

CASE		DSI	DI	DS	D\//	DW/w
CASE		FOL	FL.	P3	FVV	F VV VV
TAJ	А	4.0 (0.157)	1.4 (0.054)	1.2 (0.047)	1.8 (0.071)	0.9 (0.035)
	В	4.0 (0.157)	1.4 (0.054)	1.2 (0.047)	2.8 (0.110)	1.6 (0.063)
	С	6.5 (0.056)	2.0 (0.079)	2.5 (0.098)	2.8 (0.110)	1.6 (0.063)
	D	8.0 (0.315)	2.0 (0.079)	4.0 (0.157)	3.0 (0.119)	1.7 (0.068)
	V	8.3 (0.325)	2.3 (0.090)	3.7 (0.145)	6.2 (0.245)	1.7 (0.068)
	Е	8.0 (0.315)	2.0 (0.079)	4.0 (0.157)	3.0 (0.119)	1.7 (0.068)
	R	2.7 (0.100)	1.0 (0.040)	1.0 (0.040)	1.6 (0.060)	0.8 (0.030)
	S	4.0 (0.160)	1.4 (0.050)	1.0 (0.040)	1.8 (0.070)	0.8 (0.030)
	Т	4.0 (0.160)	1.4 (0.050)	1.0 (0.040)	2.8 (0.110)	0.8 (0.030)
TAC	L	2.4 (0.095)	0.7 (0.027)	0.9 (0.035)	1.0 (0.039)	-
	R	3.0 (0.120)	0.7 (0.027)	1.6 (0.063)	1.5 (0.059)	-
TAZ	А	3.3 (0.126)	1.4 (0.054)	0.5 (0.020)	2.5 (0.098)	1.0 (0.039)
	В	4.5 (0.178)	1.4 (0.054)	1.8 (0.070)	2.5 (0.098)	1.0 (0.039)
	D	4.5 (0.178)	1.4 (0.054)	1.8 (0.070)	3.6 (0.143)	2.0 (0.079)
	Е	5.8 (0.228)	1.4 (0.054)	3.0 (0.120)	3.6 (0.143)	2.2 (0.085)
	F	6.3 (0.248)	1.4 (0.054)	3.6 (0.140)	4.5 (0.178)	3.0 (0.119)
	G	7.4 (0.293)	1.9 (0.074)	3.7 (0.145)	4.0 (0.157)	2.4 (0.095)
	Н	8.0 (0.313)	1.9 (0.074)	4.2 (0.165)	5.0 (0.197)	3.4 (0.135)

SECTION 7 QUALIFICATION APPROVAL STATUS

DESCRIPTION	STYLE	SPECIFICATION
Surface mount capacitors	TAJ	CECC 30801 - 005 Issue 2 CECC 30801 - 011 Issue 1 MIL-C-55365/8 (CWR11)
	TAZ	MIL-C-55365/4 (CWR09)



Tape and Reel Packaging

Tape and reel packaging for automatic component placement. Please enter required Suffix on order. Bulk product is not available.

Case Size reference	Tape width mm	P mm	103mm Suffix	(4") reel Qty.	180mm Suffix	(7") reel Qty.	330mm	(13") reel
А	8	4			R	2000	S	8000
В	8	4			R	2000	S	8000
С	12	8			R	500	S	3000
D	12	8			R	500	S	2500
Е	12	8			R	400	S	1500
V	12	8			R	400	S	1500
R	8	4			R	2500	S	10000
S	8	4			R	2500	S	10000
Т	8	4			R	2500	S	10000
TACL	8	4	Х	500	R	3500		
TACR	8	4	Х	500	R	2500		

TAC. TAJ AND TPS TAPING SUFFIX TABLE

Total Tape Thickness — K max							
TAC/TAJ/TPS							
Case size reference	K Ao Bo						
A	2.3 (0.090)	1.9	3.5				
В	2.6 (0.102)	3.1	3.8				
С	3.3 (0.130)	3.7	6.9				
D	3.6 (0.142)	4.8	7.6				
E	4.8 (0.189)	4.5	7.5				
V	4.0 (0.156)	6.4	7.6				
R	1.9 (0.075)	1.7	2.5				
S	1.9 (0.075)	1.9	3.5				
Т	1.9 (0.075)	3.1	3.8				
L	1.1 (0.043)	1.1	2.0				

PLASTIC TAPE DIMENSIONS

Code		8mm Tape		12mm Tape
	4±0.1	(0.157±0.004)	4±0.1	(0.157±0.004)
P*	or		or	
	8±0.1	(0.315±0.004)	8±0.1	(0.315±0.004)
G	0.75 min	(0.03 min)	0.75 min	(0.03 min)
F	3.5±0.05	(0.138±0.002)	5.5±0.05	(0.22±0.002)
E	1.75±0.1	(0.069±0.004)	1.75±0.1	(0.069±0.004)
W	8±0.3	(0.315±0.012)	12±0.3	(0.472±0.012)
P2	2±0.05	(0.079±0.002)	2±0.05	(0.079±0.002)
P ₀	4±0.1	(0.157±0.004)	4±0.1	(0.157±0.004)
D	1.5±0.1	(0.059±0.004)	1.5±0.1	(0.059±0.004)
	-0	(-0)	-0	(-0)
D1	1.0 min	(0.039 min)	1.5 min	(0.059 min)

*See taping suffix tables for actual P dimension (component pitch).

TAPE SPECIFICATION

Tape dimensions comply to EIA RS 481 A

Dimensions A₀ and B₀ of the pocket and the tape thickness, K, are dependent on the component size.

Tape materials do not affect component solderability during storage. Carrier Tape Thickness <0.4mm

Standard Dimensions mm

A: 9.5mm (8mm tape) 13.0mm (12mm tape)

Cover Tape Dimensions

Thickness: 75±25µ Width of tape: 5.5mm + 0.2mm (8mm tape) 9.5mm + 0.2mm (12mm tape)



TAJ & TPS Marking



MARKING: TAJ SERIES

For TAJ, the positive end of body has videcon readable polarity bar marking, with the AVX logo "A" as shown in the diagram. Bodies are marked by indelible laser marking on top surface with capacitance value, voltage and date of manufacture. Due to the small size of the A, B, S and T cases, a voltage code is used as shown to the right. R case is an exception in which only the voltage and capacitance values are printed.

Voltage Code A, B, S and T Cases	Rated Voltage at 85°C
F	2
G	4
J	6.3
A	10
С	16
D	20
E	25
V	35
Т	50

POLARITY BAR INDICATES ANODE (+) TERMINATION



R Case:

- 1. Voltage
- 2. Capacitance in µF



A, B, S and T Case:

- 1. Voltage Code (see table)
- 2. Capacitance in µF
- 3. Date Code



C, D, E and V Case:

- 1. Capacitance in μF
- 2. Rated Voltage at 85°C
- 3. Date Code



TAZ, CWR09, CWR11 Series



Tape and Reel Packaging

Solid Tantalum Chip TAZ Tape and reel packaging for automatic component placement. Please enter required Suffix on order. Bulk packaging is standard.

TAZ TAPING SUFFIX TABLE

Case Size	Tape width	Р	7" (180mm) reel		13" reel (33	30mm) reel
reference	mm	mm	Suffix	Qty.	Suffix	Qty.
A	8	4	R	2500	S	9000
В	12	4	R	2500	S	9000
D	12	4	R	2500	S	8000
E	12	4	R	2500	S	8000
F	12	8	R	1000	S	3000
G	12	8	R	500	S	2500
Н	12	8	R	500	S	2500

Total Tape Thickness — K max				
TAZ Case size Millimeters (Inches)				
reterence	DIM			
А	2.0 (0.079)			
В	4.0 (0.157)			
D	4.0 (0.157)			
Е	4.0 (0.157)			
F	4.0 (0.157)			
G	4.0 (0.157)			
H	4.0 (0.157)			

Code		8mm Tape		12mm Tape
	4±0.1	(0.157±0.004)	4±0.1	(0.157±0.004)
P*	or		or	
	8±0.1	(0.315±0.004)	8±0.1	(0.315±0.004)
G	0.75 min	(0.03 min)	0.75 min	(0.03 min)
F	3.5±0.05	(0.138±0.002)	5.5±0.05	(0.22±0.002)
E	1.75±0.1	(0.069±0.004)	1.75±0.1	(0.069±0.004)
W	8±0.3	(0.315±0.012)	12±0.3	(0.472±0.012)
P2	2±0.05	(0.079±0.002)	2±0.05	(0.079±0.002)
P ₀	4±0.1	(0.157±0.004)	4±0.1	(0.157±0.004)
D	1.5±0.1	(0.059±0.004)	1.5±0.1	(0.059±0.004)
	-0	(-0)	-0	(-0)
D1	1.0 min	(0.039 min)	1.5 min	(0.059 min)

*See taping suffix tables for actual P dimension (component pitch).

TAPE SPECIFICATION

Tape dimensions comply to EIA RS 481 A Dimensions A_0 and B_0 of the pocket and the tape thickness, K, are dependent on the component size.

Tape materials do not affect component solderability during storage.

Carrier Tape Thickness < 0.4mm





TAZ, CWR09, CWR11 Series

Tape and Reel Packaging

PLASTIC TAPE REEL DIMENSIONS



Standard Dimensions mm A: 9.5mm (8mm tape)

13.0mm (12mm tape)

Cover Tape Dimensions Thickness: 75±25µ Width of tape: 5.5mm + 0.2mm (8mm tape) 9.5mm + 0.2mm (12mm tape)

Waffle Packaging - 2" x 2" hard plastic waffle trays. To order Waffle packaging use a "W" in part numbers packaging position.

Case Size	Maximum Quantity Per Waffle	
TAZ A	160	
TAZ B	112	
TAZ D	88	
TAZ E	60	
TAZ F	48	
TAZ G	50	
TAZ H	28	
CWR11 A	96	
CWR11 B	72	
CWR11 C	54	
CWR11 D	28	

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NOTE: Orientation of parts in waffle packs varies by case size.



Some commonly asked questions regarding Tantalum Capacitors:

Question: If I use several tantalum capacitors in serial/parallel combinations, how can I ensure equal current and voltage sharing?

Answer: Connecting two or more capacitors in series and parallel combinations allows almost any value and rating to be constructed for use in an application. For example, a capacitance of more than 60μ F is required in a circuit for stable operation. The working voltage rail is 24 volts dc with a superimposed ripple of 1.5 volts at 120 Hz.

The maximum voltage seen by the capacitor is V_{dC} + $V_{ac}\text{=}25.5V$

Applying the 50% derating rule tells us that a 50V capacitor is required.

Connecting two 25V rated capacitors in series will give the required capacitance voltage rating, but the



effective capacitance will be halved, so for greater than $60\mu\text{F},$ four such series combinations are required, as shown.



In order to ensure reliable operation, the capacitors should be connected as shown below to allow current sharing of the ac noise and ripple signals. This prevents any one capacitor heating more than its neighbors and thus being the weak link in the chain.



The two resistors are used to ensure that the leakage currents of the capacitors does not affect the circuit reliability, by ensuring that all the capacitors have half the working voltage across them.

Question: What are the advantages of tantalum over other capacitor technologies?

Answer:

- 1. Tantalum capacitors have high volumetric efficiency.
- **2.** Electrical performance over temperature is very stable.
- **3.** They have a wide operating temperature range -55 degrees C to +125 degrees C.
- **4.** They have better frequency characteristics than aluminum electrolytics.
- **5.** No wear out mechanism. Because of their construction, solid tantalum capacitors do not degrade in performance or reliability over time.

Question: How does TPS differ from your standard product?

Answer: TPS has been designed from the initial anode production stages for power supply applications. Special manufacturing processes provide the most robust capacitor dielectric by maximizing the volumetric efficiency of the package. After manufacturing, parts are conditioned by being subjected to elevated temperature overvoltage burn in applied for a minimum of two hours. Parts are monitored on a 100% basis for their direct current leakage performance at elevated temperatures. Parts are then subjected to a low impedance current surge. This current surge is performed on a 100% basis with each capacitor individually monitored. At this stage, the capacitor undergoes 100% test for capacitance, Dissipation Factor, leakage current, and 100 KHz ESR to TPS requirements.

Question: If the part is rated as a 25 volt part and you have current surged it, why can't I use it at 25 volts in a low impedance circuit?

Answer: The high volumetric efficiency obtained using tantalum technology is accomplished by using an extremely thin film of tantalum pentoxide as the dielectric. Even an application of the relatively low voltage of 25 volts will produce a large field strength as seen by the dielectric. As a result of this, derating has a significant impact on reliability as described under the reliability section. The following example uses a 22 microfarad capacitor rated at 25 volts to illustrate the point. The equation for determining the amount of surface area for a capacitor is as follows:

Questions & Answers



 $C = ((E) (E_{\circ}) (A)) / d$

$$A = ((C) (d)) / ((E_{\circ})(E))$$

$$A = ((22 \times 10^{-6}) (170 \times 10^{-9})) / ((8.85 \times 10^{-12}) (27))$$

A = 0.015 square meters (150 square centimeters) Where

- C = Capacitance in farads
- A = Dielectric (Electrode) Surface Area (m²)
- d = Dielectric thickness (Space between dielectric) (m)

E = Dielectric constant (27 for tantalum)

 E_{\circ} = Dielectric Constant relative to a vacuum (8.855 x 10⁻¹² Farads x m⁻¹)

To compute the field voltage potential felt by the dielectric we use the following logic.

Dielectric formation potential = Formation Ratio x Working Voltage
= 4 x 25
Formation Potential = 100 volts
Dielectric (Ta $_2O_5$) Thickness (d) is 1.7 x 10 ⁻⁹ Meters Per Volt
d = 0.17 μ meters
Electric Field Strength = Working Voltage / d
= (25 / 0.17 μ meters)
= 147 Kilovolts per millimeter
= 147 Megavolts

per meter

No matter how pure the raw tantalum powder or the precision of processing, there will always be impurity sites in the dielectric. We attempt to stress these sites in the factory with overvoltage surges, and elevated temperature burn in so that components will fail in the factory and not in your product. Unfortunately, within this large area of tantalum pentoxide, impurity sites will exist in all capacitors. To minimize the possibility of providing enough activation energy for these impurity sites to turn from an amorphous state to a crystalline state that will conduct energy, series resistance and derating is recommended. By reducing the electric field within the anode at these sites, the tantalum capacitor has increased reliability. Tantalums differ from other electrolytics in that charge transients are carried by electronic conduction rather than absorption of ions.

Question: What negative transients can Solid Tantalum Capacitors operate under?

Answer: The reverse voltage ratings are designed to cover exceptional conditions of small level excursions into incorrect polarity. The values quoted are not intended to cover continuous reverse operation. The peak reverse voltage applied to the capacitor must not exceed:

10% of rated DC working voltage to a maximum of 1 volt at 25° C.

3% of rated DC working voltage to a maximum of 0.5 volt at 85°C.

1% of category DC working voltage to a maximum of 0.1 volt at 125°C.

Question: I have read that manufacturers recommend a series resistance of 0.1 ohm per working volt. You suggest we use 1 ohm per volt in a low impedance circuit. Why?

Answer: We are talking about two very different sets of circuit conditions for those recommendations. The 0.1 ohm per volt recommendation is for steady-state conditions. This level of resistance is used as a basis for the series resistance variable in a 1% / 1000 hours 60% confidence level reference. This is what steady-state life tests are based on. The 1 ohm per volt is recommended for dynamic conditions which include current in-rush applications such as inputs to power supply circuits. In many power supply topologies where the di/dt through the capacitor(s) is limited, (such as most implementations of buck (current mode), forward converter, and flyback), the requirement for series resistance is decreased.

Question: How long is the shelf life for a tantalum capacitor?

Answer: Solid tantalum capacitors have no limitation on shelf life. The dielectric is stable and no reformation is required. The only factors that affect future performance of the capacitors would be high humidity conditions and extreme storage temperatures. Solderability of solder coated surfaces may be affected by storage in excess of one year under temperatures greater than 40°C or humidities greater than 80% relative humidity. Terminations should be checked for solderability in the event an oxidation develops on the solder plating.

Question: Do you recommend the use of tantalum capacitors on the input side of DC-DC converters?

Answer: No. Typically the input side of a converter is fed from the voltage sources which are not regulated and are of nominally low impedance. Examples would be Nickel-Metal-Hydride batteries, Nickel-Cadmium batteries, etc., whose internal resistance is typically in the low milliohm range.

Technical Publications



- 1. Steve Warden and John Gill, "Application Guidelines on IR Reflow of Surface Mount Solid Tantalum Capacitors."
- 2. John Gill, "Glossary of Terms used in the Tantalum Industry."
- 3. R.W. Franklin, "Over-Heating in Failed Tantalum Capacitors," AVX Ltd.
- 4. R.W. Franklin, "Upgraded Surge Performance of Tantalum Capacitors," Electronic Engineering 1985
- 5. R.W. Franklin, "Screening beats surge threat," Electronics Manufacture & Test, June 1985
- 6. AVX Surface Mounting Guide
- 7. Ian Salisbury, "Thermal Management of Surface Mounted Tantalum Capacitors," AVX
- 8. John Gill, "Investigation into the Effects of Connecting Tantalum Capacitors in Series," AVX
- 9. Ian Salisbury, "Analysis of Fusing Technology for Tantalum Capacitors," AVX-Kyocera Group Company
- 10. R.W. Franklin, "Analysis of Solid Tantalum Capacitor Leakage Current," AVX Ltd.
- 11. R.W. Franklin, "An Exploration of Leakage Current," AVX, Ltd.
- 12. William A. Millman, "Application Specific SMD Tantalum Capacitors," Technical Operations, AVX Ltd.
- 13. R.W. Franklin, "Capacitance Tolerances for Solid Tantalum Capacitors," AVX Ltd.
- 14. Arch G. Martin, "Decoupling Basics," AVX Corporation

- 15. R.W. Franklin, "Equivalent Series Resistance of Tantalum Capacitors," AVX Ltd.
- John Stroud, "Molded Surface Mount Tantalum Capacitors vs Conformally Coated Capacitors," AVX Corporation, Tantalum Division
- 17. Chris Reynolds, "Reliability Management of Tantalum Capacitors," AVX Tantalum Corporation
- 18. R.W. Franklin, "Ripple Rating of Tantalum Chip Capacitors," AVX Ltd.
- 19. Chris Reynolds, "Setting Standard Sizes for Tantalum Chips," AVX Corporation
- 20. John Gill, "Surge In Solid Tantalum Capacitors," AVX Ltd.
- David Mattingly, "Increasing Reliability of SMD Tantalum Capacitors in Low Impedance Applications," AVX Corporation
- 22. John Gill, "Basic Tantalum Technology," AVX Ltd.
- 23. Ian Salisbury, "Solder Update Reflow Mounting TACmicrochip Tantalum Capacitor," AVX Ltd.
- 24. Ian Salisbury, "New Tantalum Capacitor Design for 0603 Size," AVX Ltd.
- 25. John Gill, "Capacitor Technology Comparison," AVX Ltd.
- 26. Scott Chiang, "High Performance CPU Capacitor Requirements, how AVX can help," AVX Kyocera Taiwan
- 27. John Gill and Ian Bishop, "Reverse Voltage Behavior of Solid Tantalum Capacitors."

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Project Jaunch da	ate:					
0-3mths	G-6mths	G-12mths	⊡ >12mths			
Circuit Applicatio	on:					
Decoupling	Timing	Filtering	DC Blocking	Other		
Market Sector:						
Telecoms	Auto	PC	Storage	Power Supply	Industrial	
Cellular	Other					
Please rank your critical design factors between 1-6 (1 most critical)						
		Ù Ì				
Size	Max Capacitance	Impedance	Temperature Stability	Leakage Current	Height	
Please specify any CV ratings required outside of current matrix:						
What other SMD	products are used	in this project:				
Ceramic	Aluminum	Film	Chip Arrays	Conductive Polymer	Os-con	
Please specify any non standard special requirements:						
Non Std Cap	Low ESR	Temp	Cap Tolerance	Interest in spec of Short Form (ials shown overleaf Catalog	
Forecast usage of	of Tantalum:	1998		1999	2000	
Standard SM	MD Tantalum ACmicrochip					
Eavored Supplier Please rank 1-5 (1 most favorable):						
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Other engineers in your company who would like information:						
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