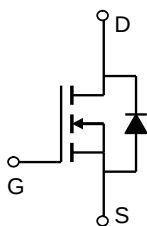




N-Channel MOSFET AO4430

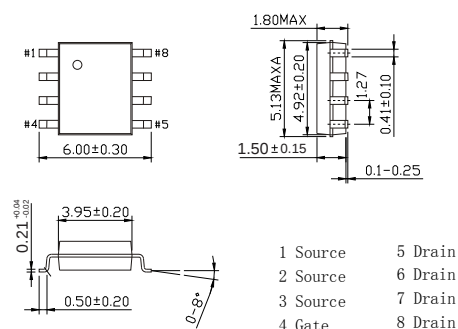
■ Features

- $V_{DS} (V) = 30V$
- $I_D = 18 A$ ($V_{GS} = 10V$)
- $R_{DS(ON)} < 4.5m\Omega$ ($V_{GS} = 10V$)
- $R_{DS(ON)} < 6.5m\Omega$ ($V_{GS} = 4.5V$)



SOP-8

Unit: mm



■ Absolute Maximum Ratings $T_a = 25^\circ\text{C}$

Parameter		Symbol	Rating	Unit
Drain-Source Voltage		V _{DS}	30	V
Gate-Source Voltage		V _{GS}	±20	
Continuous Drain Current	T _A =25℃	I _D	18	A
	T _A =70℃		15	
Pulsed Drain Current		I _{DM}	80	
Avalanche Current		I _{AR}	30	
Repetitive Avalanche Energy	L=0.3mH	E _{AR}	135	mJ
Power Dissipation	T _A =25℃	P _D	3	W
	T _A =70℃		2.1	
Thermal Resistance.Junction- to-Ambient	t ≤ 10s	R _{thJA}	40	℃/W
	Steady-State		75	
Thermal Resistance.Junction- to-Lead		R _{thJL}	24	
Junction Temperature		T _J	150	℃
Storage Temperature Range		T _{stg}	-55 to 150	

■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V_{DS}	$I_D=250\mu A$, $V_{GS}=0V$	30			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=30V$, $V_{GS}=0V$			1	μA
		$V_{DS}=30V$, $V_{GS}=0V$, $T_J=55^\circ C$			5	
Gate-Body Leakage Current	I_{GSS}	$V_{DS}=0V$, $V_{GS}=\pm 20V$			± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_D=250\mu A$	1		2.5	V
Static Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=10V$, $I_D=18A$			4.5	m Ω
		$V_{GS}=20V$, $I_D=18A$, $T_J=125^\circ C$			7	
		$V_{GS}=4.5V$, $I_D=15A$			6.5	
On State Drain Current	$I_{D(on)}$	$V_{GS}=4.5V$, $V_{DS}=5V$	80			A
Forward Transconductance	g_{FS}	$V_{DS}=5V$, $I_D=18A$		82		S
Input Capacitance	C_{iss}	$V_{GS}=0V$, $V_{DS}=15V$, $f=1MHz$	4660		7270	pF
Output Capacitance	C_{oss}		425		960	
Reverse Transfer Capacitance	C_{rss}		240		530	
Gate Resistance	R_g	$V_{GS}=0V$, $V_{DS}=0V$, $f=1MHz$	0.2		0.9	Ω
Total Gate Charge (10V)	Q_g	$V_{GS}=10V$, $V_{DS}=15V$, $I_D=18A$	80		124	nC
Total Gate Charge (4.5V)			37		58	
Gate Source Charge				18		
Gate Drain Charge				15		
Turn-On DelayTime	$t_{d(on)}$	$V_{GS}=10V$, $V_{DS}=15V$, $R_L=0.83\Omega$, $R_{GEN}=3\Omega$		12	16	ns
Turn-On Rise Time	t_r			8	12	
Turn-Off DelayTime	$t_{d(off)}$			51.5	70	
Turn-Off Fall Time	t_f			8.8	14	
Body Diode Reverse Recovery Time	t_{rr}	$I_F=18A$, $di/dt=100A/\mu s$		33.5	44	nC
Body Diode Reverse Recovery Charge	Q_{rr}			22	30	
Maximum Body-Diode Continuous Current	I_S				4.5	A
Diode Forward Voltage	V_{SD}	$I_S=1A$, $V_{GS}=0V$			1	V

Note : The static characteristics in Figures 1 to 6 are obtained using <300 us pulses, duty cycle 0.5% max.

■ Typical Characteristics

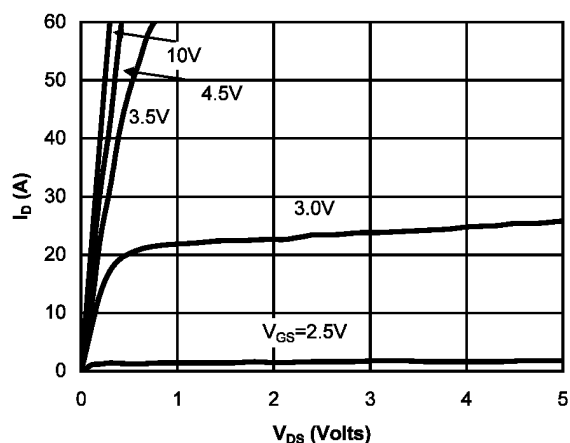


Fig 1: On-Region Characteristics

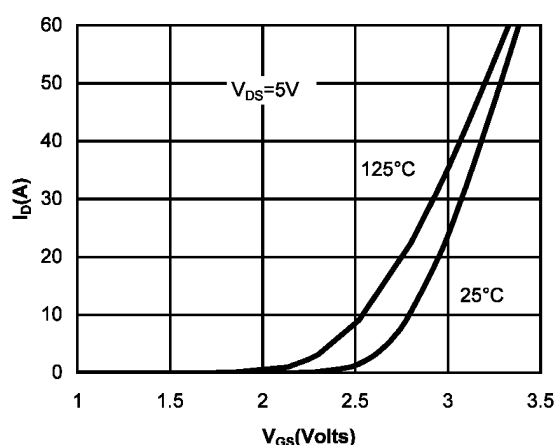


Figure 2: Transfer Characteristics

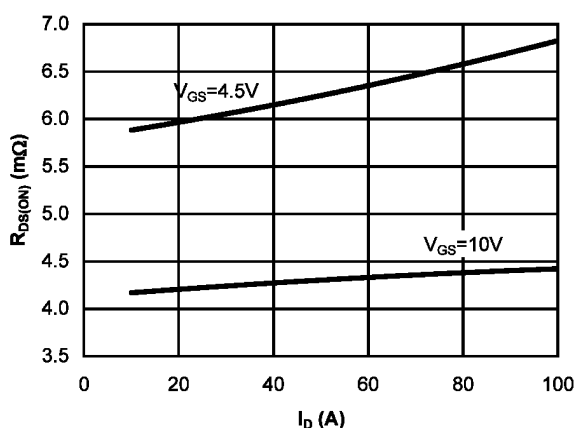


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

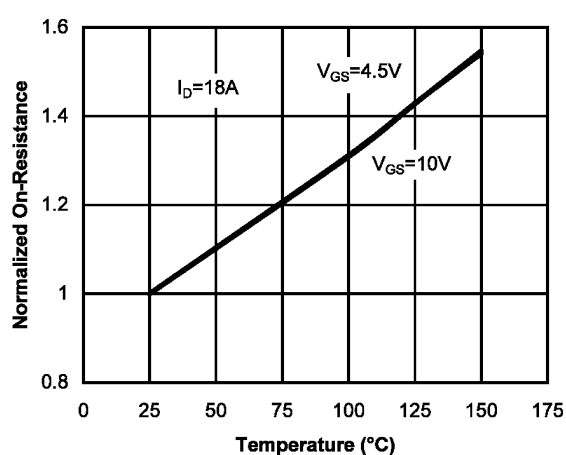


Figure 4: On-Resistance vs. Junction Temperature

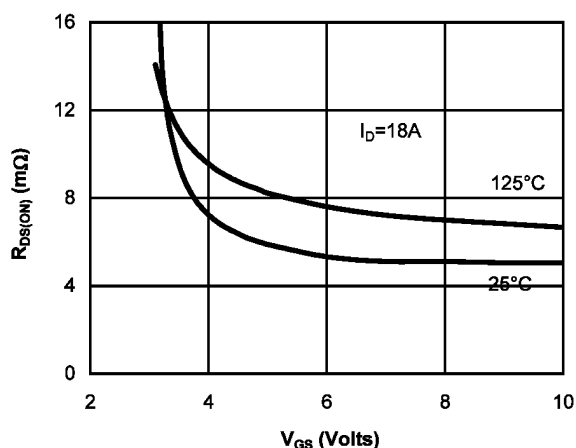


Figure 5: On-Resistance vs. Gate-Source Voltage

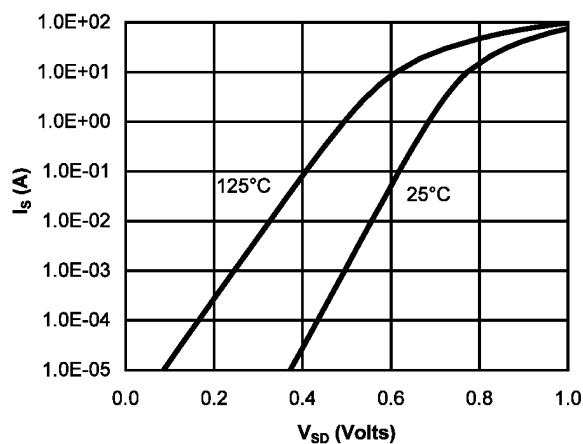


Figure 6: Body-Diode Characteristics

Typical Characteristics

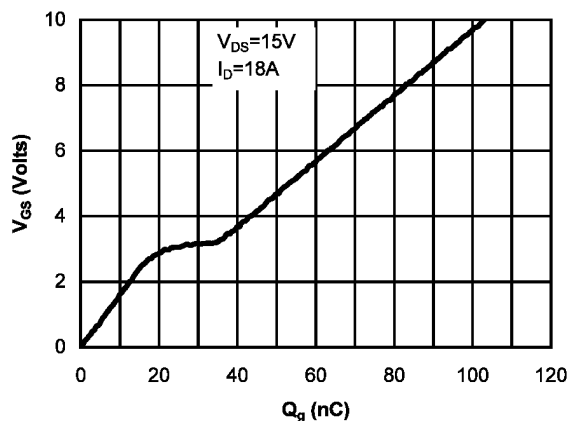


Figure 7: Gate-Charge Characteristics

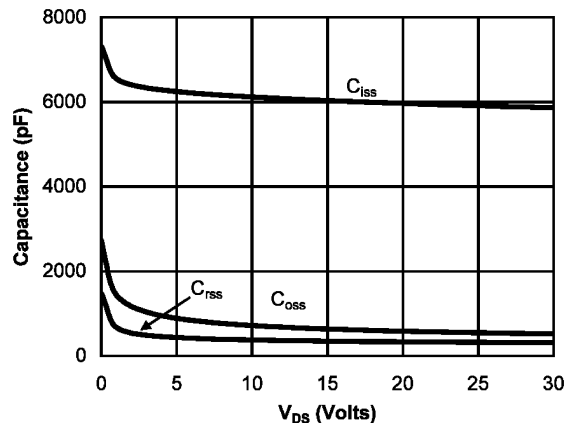


Figure 8: Capacitance Characteristics

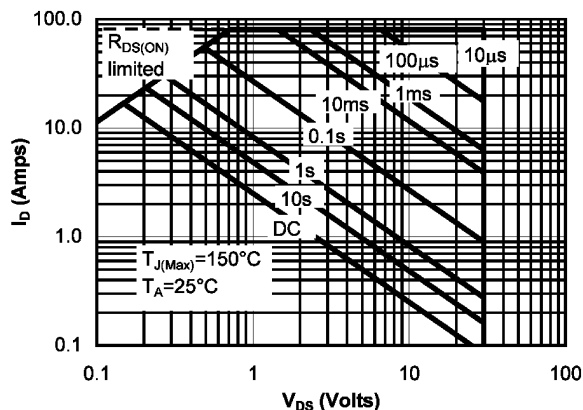


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

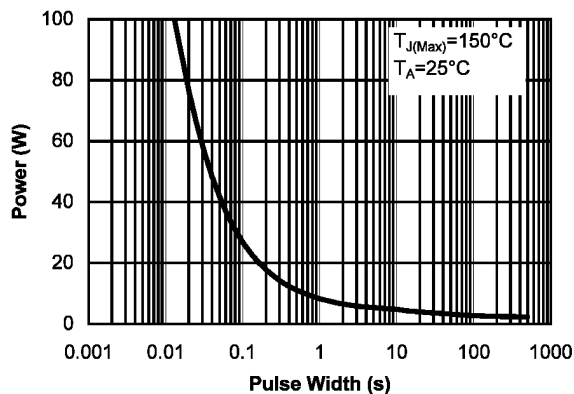


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

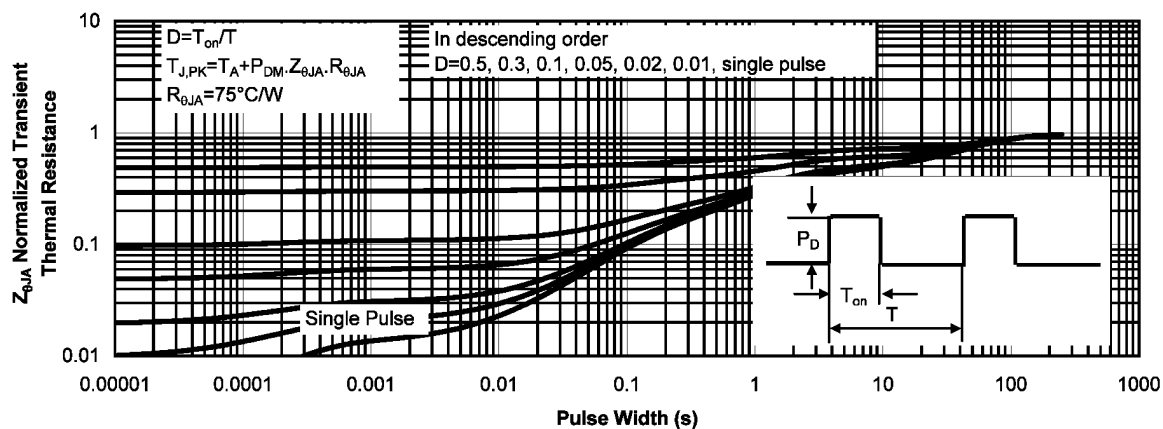


Figure 11: Normalized Maximum Transient Thermal Impedance