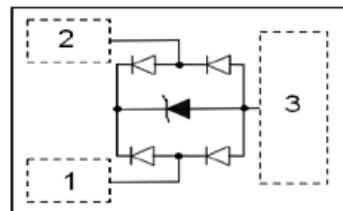


## FEATURES

- Uni-directional ESD protection of two lines or bi-directional ESD protection of one line
- Reverse standoff voltage 3.3 and 5 V
- Low diode capacitance
- Ultra low leakage current
- Leadless ultra small SOT883 surface mount package ( $1 \times 0.6 \times 0.5$  mm)
- Board space  $1.17 \text{ mm}^2$  (approx. 10% of SOT23)
- ESD protection >15 kV
- IEC 61000-4-2; level 4 (ESD); 15 kV (air) or 8 kV (contact).



## APPLICATIONS

- Cellular handsets and accessories
- Portable electronics
- Computers and peripherals
- Communication systems
- Audio and video equipment.

## DESCRIPTION

Low capacitance ESD protection diode in a three pad SOT883 leadless ultra small plastic package designed to protect up to two transmission or data lines from ElectroStatic Discharge (ESD) damage.

## LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
<b>Per diode</b>					
$I_{pp}$	peak pulse current PESD3V3L2UM PESD5V0L2UM	8/20 $\mu\text{s}$ pulse; notes 1, 2 and 3	—	3	A
			—	2.5	A
$P_{pp}$	peak pulse power	8/20 $\mu\text{s}$ pulse; notes 1, 2 and 3	—	30	W
$I_{FSM}$	non-repetitive peak forward current	$t_p = 1 \text{ ms}$ ; square pulse	—	3.5	A
$I_{ZSM}$	non-repetitive peak reverse current PESD3V3L2UM PESD5V0L2UM	$t_p = 1 \text{ ms}$ ; square pulse	—	0.9	A
			—	0.8	A
$P_{tot}$	total power dissipation	$T_{amb} = 25^\circ\text{C}$ ; note 4	—	250	mW
$P_{ZSM}$	non-repetitive peak reverse power dissipation	$t_p = 1 \text{ ms}$ ; square pulse; see Fig.4	—	6	W
$T_{stg}$	storage temperature		-65	+150	$^\circ\text{C}$
$T_j$	junction temperature		—	150	$^\circ\text{C}$
ESD	electrostatic discharge	IEC 61000-4-2 (contact discharge)	15	—	kV
		HBM MIL-Std 883	10	—	kV

1. Non-repetitive current pulse 8/20  $\mu\text{s}$  exponential decay waveform; see Fig.5.

2. Pins 1 and 3 or 2 and 3.

3. Pins 1 and 2.

4. Device mounted on standard printed-circuit board.

**ESD standards compliance**

IEC 61000-4-2, level 4 (ESD)	>15 kV (air); >8 kV (contact)
HBM MIL-Std 883, class 3	>4 kV

**THERMAL CHARACTERISTICS**

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th\ j-a}$	thermal resistance from junction to ambient	all diodes loaded; note 1	500	K/W
		one diode loaded; note 2	290	K/W

1. Refer to SOT883 standard mounting conditions (footprint), FR4 with 60  $\mu\text{m}$  copper strip line.2. FR4 single-sided copper 1 cm<sup>2</sup>. $T_j = 25^\circ\text{C}$  unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
<b>Per diode</b>						
$V_F$	forward voltage	$I_F = 200 \text{ mA}$	–	1	1.2	V
$V_{RWM}$	reverse stand-off voltage PESD3V3L2UM PESD5V0L2UM		–	–	3.3	V
–			–	–	5	V
$I_{RM}$	reverse leakage current PESD3V3L2UM PESD5V0L2UM	$V_R = 3.3 \text{ V}$ $V_R = 5 \text{ V}$	–	75	300	nA
–			–	5	25	nA
$V_{(CL)R}$	clamping voltage PESD3V3L2UM	8/20 $\mu\text{s}$ pulse $I_{pp} = 1 \text{ A}$ ; notes 1 and 2 $I_{pp} = 3 \text{ A}$ ; notes 1 and 2 $I_{pp} = 1 \text{ A}$ ; notes 1 and 3 $I_{pp} = 3 \text{ A}$ ; notes 1 and 3 $I_{pp} = 1 \text{ A}$ ; notes 1 and 2 $I_{pp} = 2.5 \text{ A}$ ; notes 1 and 2 $I_{pp} = 1 \text{ A}$ ; notes 1 and 3 $I_{pp} = 2.5 \text{ A}$ ; notes 1 and 3	– – – – – – – – –	– – – – – – – – –	8 12 9 13 10 13 11 15	V V V V V V V V
$V_{BR}$	breakdown voltage PESD3V3L2UM PESD5V0L2UM	$I_Z = 1 \text{ mA}$	5.32 6.46	5.6 6.8	5.88 7.14	V V
$S_z$	temperature coefficient PESD3V3L2UM PESD5V0L2UM	$I_Z = 1 \text{ mA}$	– –	1.3 2.9	– –	mV/K mV/K
$r_{diff}$	differential resistance PESD3V3L2UM PESD5V0L2UM	$I_R = 1 \text{ mA}$	– –	– –	200 100	$\Omega$ $\Omega$
$C_d$	diode capacitance PESD3V3L2UM	$f = 1 \text{ MHz}; V_R = 0$ $f = 1 \text{ MHz}; V_R = 5$	– –	22 12	28 17	pF pF
	PESD5V0L2UM	$f = 1 \text{ MHz}; V_R = 0$ $f = 1 \text{ MHz}; V_R = 5$	– –	16 8	19 11	pF pF

1. Non-repetitive current pulse 8/20  $\mu\text{s}$  exponential decay waveform; see Fig.5.

2. Pins 1 and 3 or 2 and 3.

3. Pins 1 and 2.

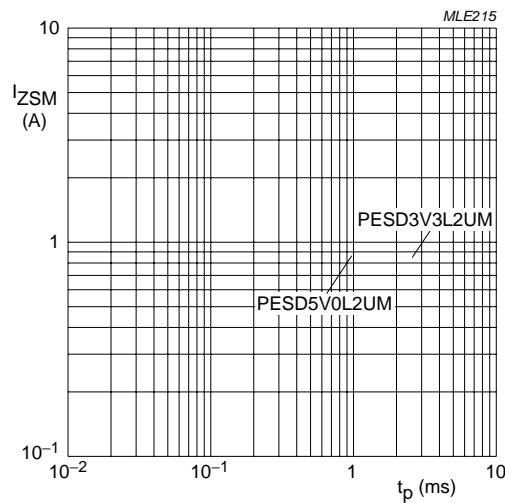
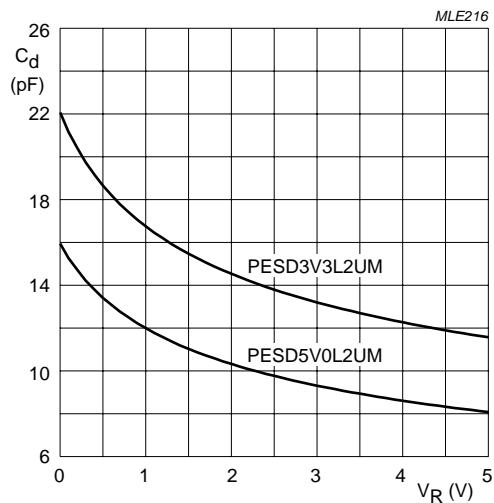
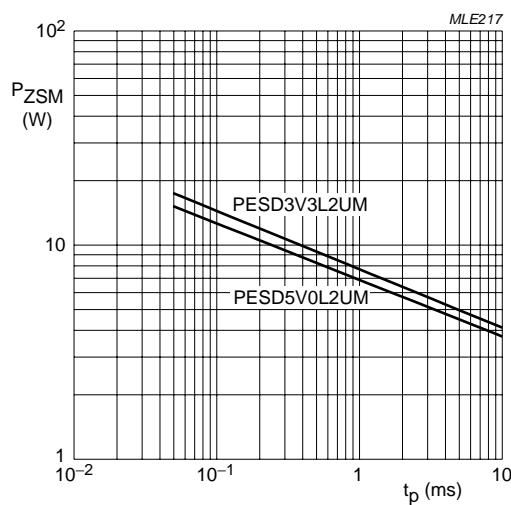


Fig.2 Non-repetitive peak reverse current as a function of pulse time (square pulse).



$T_j = 25^\circ\text{C}; f = 1 \text{ MHz}.$

Fig.3 Diode capacitance as a function of reverse voltage; typical values.



$P_{ZSM} = V_{ZSM} \times I_{ZSM}.$   
 $V_{ZSM}$  is the non-repetitive peak reverse voltage at  $I_{ZSM}$ .

Fig.4 Maximum non-repetitive peak reverse power dissipation as a function of pulse duration (square pulse).

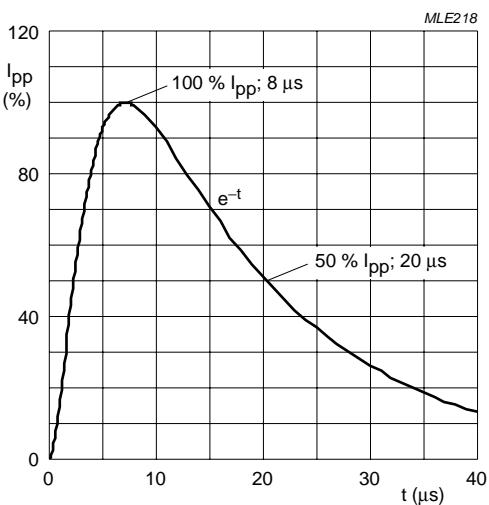


Fig.5 8/20  $\mu\text{s}$  pulse waveform according to IEC 61000-4-5.

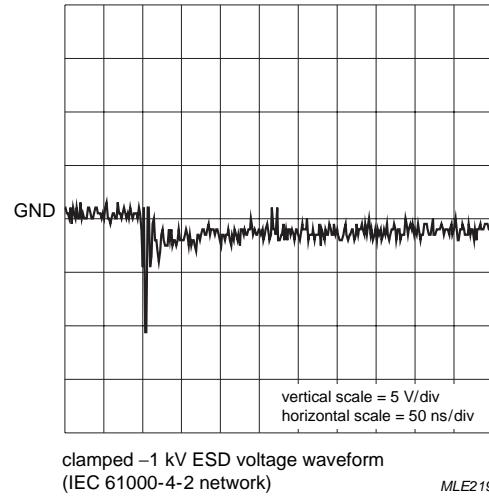
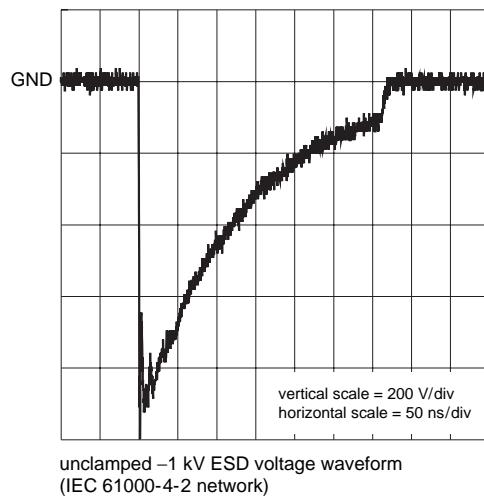
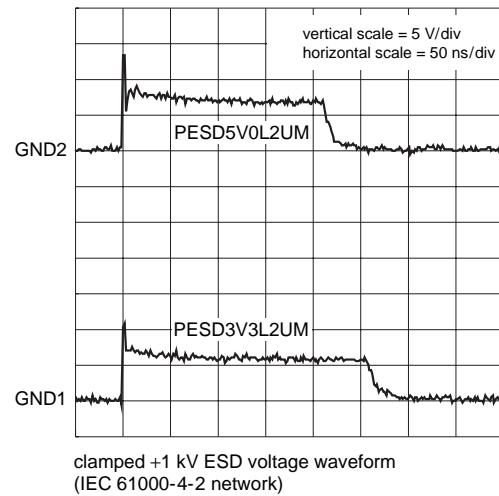
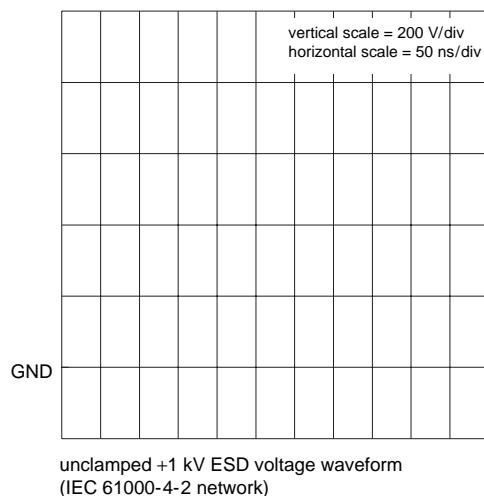
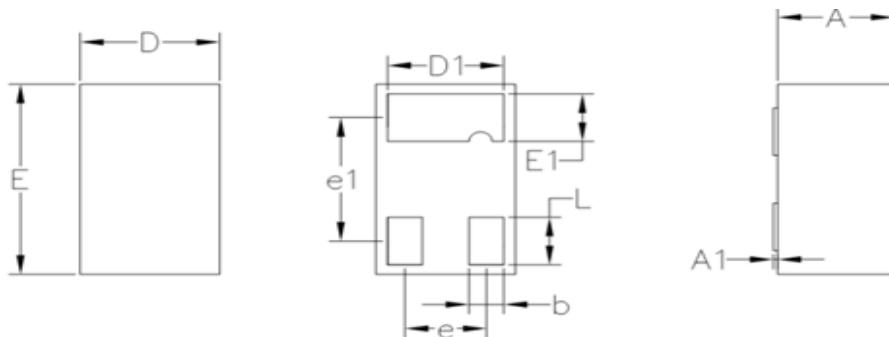
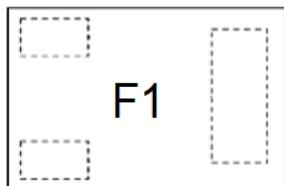


Fig.6 ESD clamping test set-up and waveforms.

**SOT-883 PACKAGE OUTLINE DIMENSIONS**

SYMBOL	DIMENSIONS IN MM		
	MIN	NOM	MAX
A	0.45	0.50	0.55
A1	0.00	—	0.05
D	0.55	0.60	0.65
E	0.95	1.00	1.05
D1	0.45	0.50	0.55
E1	0.20	0.25	0.30
L	0.20	0.25	0.30
b	0.10	0.15	0.20
e	0.35BSC		
e1	0.65BSC		

**Marking****Ordering information**

Order code	Marking code	Package	Baseqty	Delivery mode
PESD3V3L2UM	F2	SOT-883	10000	Tape and reel
PESD5V0L2UM	F1	SOT-883	10000	Tape and reel