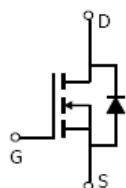


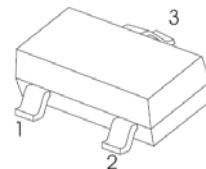
N-Channel Enhancement MOSFET

■ Features

- $V_{DS} (V) = 20V$
- $I_D = 4.2A$ ($V_{GS}=4.5V$)
- $R_{DS(ON)} < 26m\Omega$ ($V_{GS} = 4.5V$)
- $R_{DS(ON)} < 36m\Omega$ ($V_{GS} = 2.5V$)
- $R_{DS(ON)} < 57m\Omega$ ($V_{GS} = 1.8V$)



SOT - 23



1. GATE
2. SOURCE
3. DRAIN

■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	20	V
Gate-Source Voltage	V_{GS}	± 8	V
Continuous Drain $T_a=25^\circ C$	I_D	4.2	A
Current *1 $T_a=70^\circ C$		3.2	
Pulsed Drain Current *2	I_{DM}	15	
Power Dissipation *1 $T_a=25^\circ C$	P_D	1.4	W
$T_a=70^\circ C$		0.9	
Thermal Resistance.Junction-to-Ambient *1	R_{thJA}	125	$^\circ C/W$
Thermal Resistance.Junction-to-Case	R_{thJC}	80	$^\circ C/W$
Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150	$^\circ C$

*1The value of R_{thJA} is measured with the device mounted on 1in² FR-4 board with 2oz.

Copper, in a still air environment with $T_a = 25^\circ C$

Electrical Characteristics $T_a = 25^\circ\text{C}$

Parameter	Symbol	Testconditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V_{DSS}	$I_D=250\mu\text{A}, V_{GS}=0\text{V}$	20			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=16\text{V}, V_{GS}=0\text{V}$			1	μA
		$V_{DS}=16\text{V}, V_{GS}=0\text{V}, T_J=55^\circ\text{C}$			5	
Gate-Body leakage current	I_{GSS}	$V_{DS}=0\text{V}, V_{GS}=\pm 8\text{V}$			± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	0.4	0.6	1	V
Static Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=4.5\text{V}, I_D=4.2\text{A}$		23	26	$\text{m}\Omega$
		$V_{GS}=2.5\text{V}, I_D=3.7\text{A}$		32	36	
		$V_{GS}=1.8\text{V}, I_D=3.2\text{A}$		48	57	
On state drain current	$I_{D(on)}$	$V_{GS}=4.5\text{V}, V_{DS}=5\text{V}$	15			A
Forward Transconductance	g_{FS}	$V_{DS}=5\text{V}, I_D=4.2\text{A}$		11		S
Input Capacitance	C_{iss}	$V_{GS}=0\text{V}, V_{DS}=-10\text{V}, f=1\text{MHz}$		436		pF
Output Capacitance	C_{oss}			66		pF
Reverse Transfer Capacitance	C_{rss}			44		pF
Gate resistance	R_g	$V_{GS}=0\text{V}, V_{DS}=0\text{V}, f=1\text{MHz}$		3		Ω
Total Gate Charge	Q_g	$V_{GS}=4.5\text{V}, V_{DS}=-10\text{V}, I_D=4.2\text{A}$		6.2		nC
Gate Source Charge	Q_{gs}			1.6		nC
Gate Drain Charge	Q_{gd}			0.5		nC
Turn-On DelayTime	$t_{D(on)}$	$V_{GS}=4.5\text{V}, V_{DS}=10\text{V}, R_L=2.7\Omega, R_{GEN}=6\Omega$		5.5		ns
Turn-On Rise Time	t_r			6.3		ns
Turn-Off DelayTime	$t_{D(off)}$			40		ns
Turn-Off FallTime	t_f			12.7		ns
Body Diode Reverse Recovery Time	t_{rr}	$I_F=4\text{A}, dI/dt=100\text{A}/\mu\text{s}$		12.3		ns
Body Diode Reverse Recovery Charge	Q_{rr}	$I_F=4\text{A}, dI/dt=100\text{A}/\mu\text{s}$		3.5		nC
Maximum Body-Diode Continuous Current	I_s				2	A
Diode Forward Voltage	V_{SD}	$I_s=1\text{A}, V_{GS}=0\text{V}$		0.76	1	V

■ Typical Characteristics

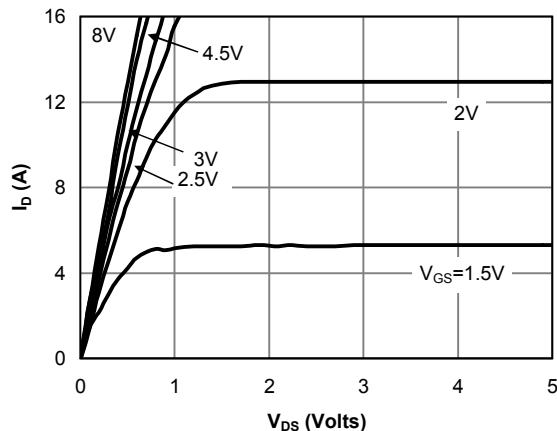


Fig 1: On-Region Characteristics

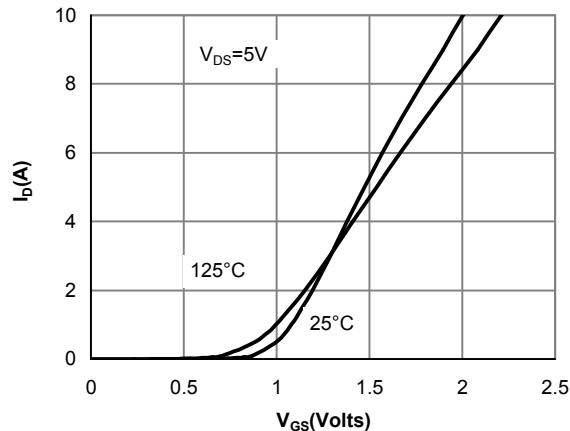


Figure 2: Transfer Characteristics

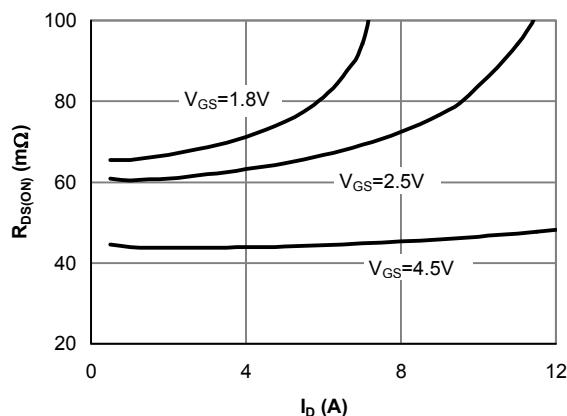


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

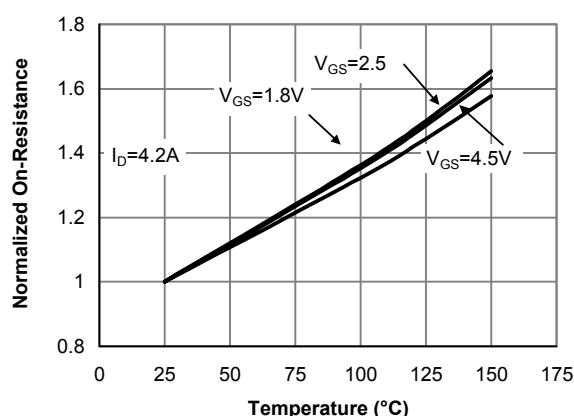


Figure 4: On-Resistance vs. Junction Temperature

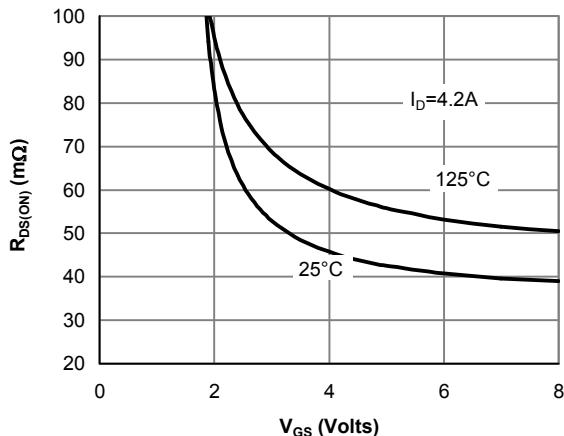


Figure 5: On-Resistance vs. Gate-Source Voltage

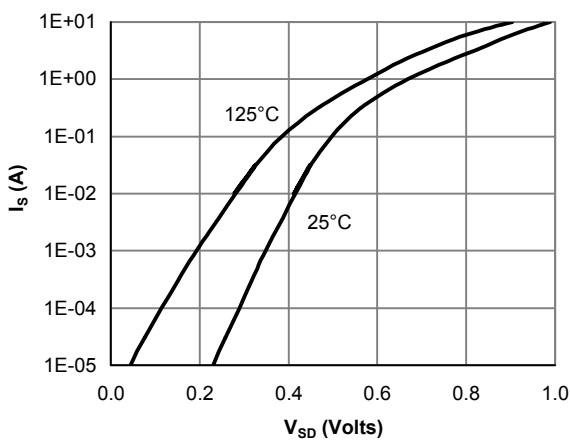
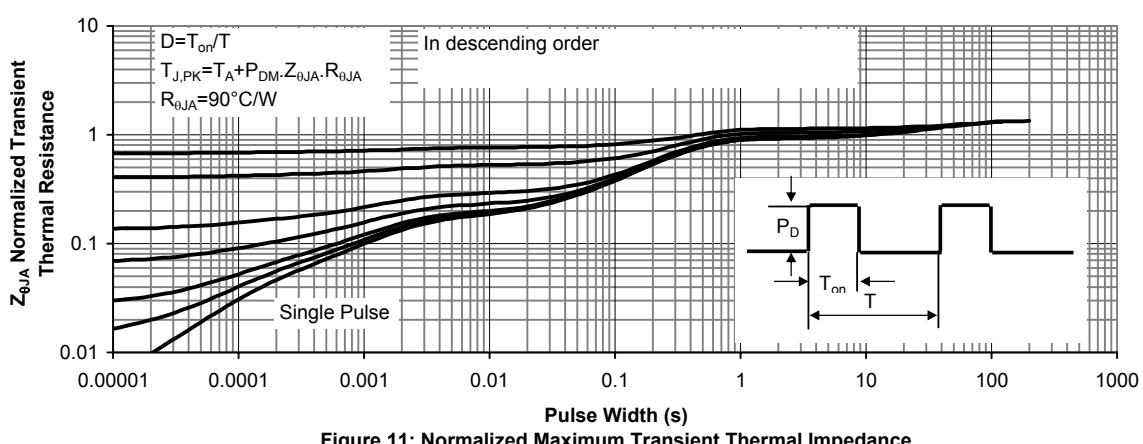
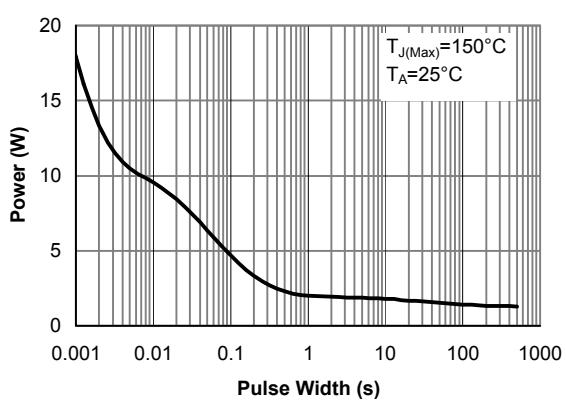
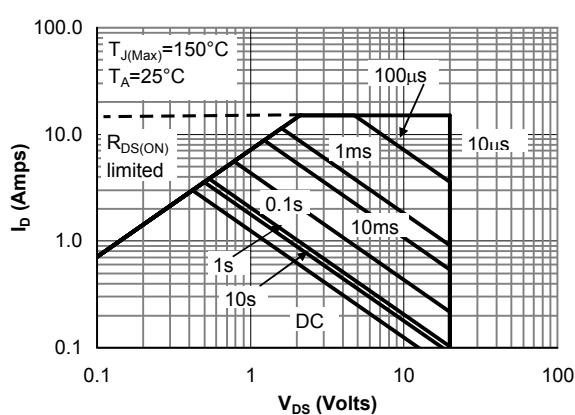
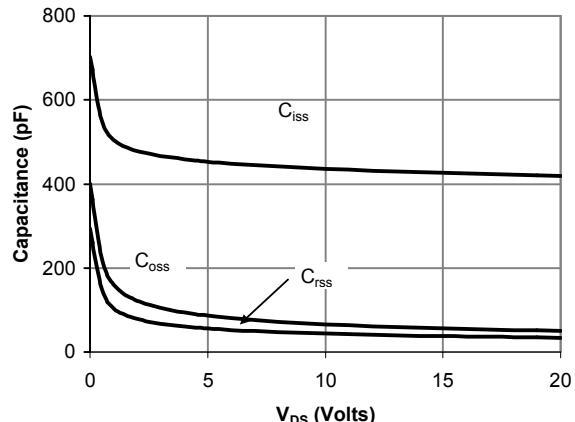
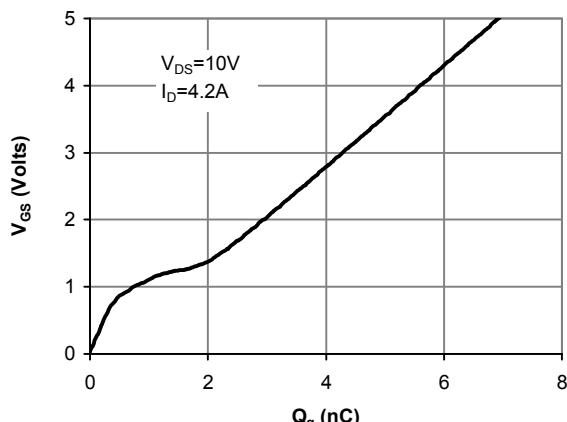
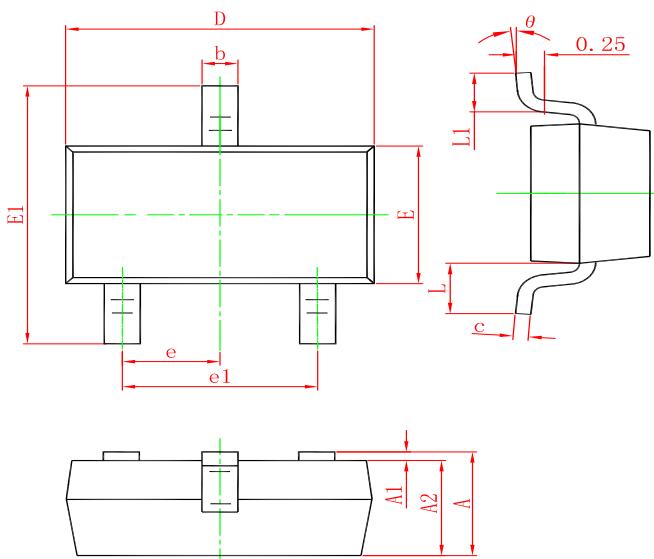


Figure 6: Body-Diode Characteristics

■ Typical Characteristics

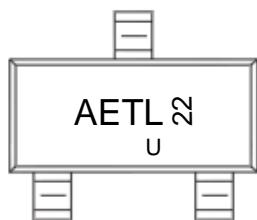


SOT-23 PACKAGE OUTLINE DIMENSIONS



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP.		0.037 TYP.	
e1	1.800	2.000	0.071	0.079
L	0.550 REF.		0.022 REF.	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°

Marking



Ordering information

Order code	Package	Baseqty	Deliverymode
AO3414A	SOT-23	3000	Tape and reel