

CD4148WP

1:6

Switching Diode CD4148WP



FEATURES

- n Silicon epitaxial planar diode
- n SMD chip pattern, available in various dimension included 0805 & 0603
- n Leadfree and RoHS compliance components
- n For small signal switching and operating ambient temperature less than 55°C and voltage withstand less than 60V; not suitable for AC switching input as rectified circuit and high reverse voltage location. CD4148WN is suitable for those application

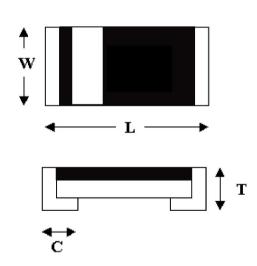
MECHANICAL CHARACTERISTICS

n Size: 1206

n Weight: approx. 10mg n Marking: Cathode terminal

DIMENSIONS

Dimension/mm	1206
L	3.2±0.2
W	1.5±0.2
Т	0.75±0.1
С	0.55±0.2



THERMAL CHARACTERISTICS¹⁾

Parameter at T _{amb} =25°C ¹⁾	Symbol	Value	Unit
Forward Power Dissipation	D	400	mW
Power derating above 25°C	P _{tot}	3.2	mW/°C
Junction Temperature	T _i	150	°C
Thermal Resistance Junction to Ambient air	R _{JA}	375	°C/W
Operating Storage Temperature range	T _{sta}	-55 to 150	°C

¹⁾ Valid provided that electrodes are kept at ambient temperature.

LIZ



CD4148WP

2:6

MAXIMUM RATING¹⁾

Parameter at T _{amb} =25°C ¹⁾	Symbol	Value	Unit
Repetitive Peak Reverse Voltage	V_{RRM}	75	V
Average rectified current sin half wave rectification with resistive load	I _{F(AV)}	150	mA
Repetitive Peak Forward Current at T _{amb} =25°C	I _{FRM}	300	mA
Non-Repetitive Surge Forward Current at t<1s and T _i =25°C	I _{FSM}	500	mA
at t 8.3ms and T _i =25°C		1000	mA

¹⁾ Valid provided that electrodes are kept at ambient temperature.

ELECTRICAL CHARACTERISTICS¹⁾

Parameter at T _{amb} =25°C ¹⁾	Symbol	Value	Unit
Forward Voltage at I _F =10mA	M	1.0 _{MAX}	V
at I _F =100mA	V_{F}	1.25 _{MAX}	V
Leakage Current at V _R =20V		0.025 _{MAX}	uA
Leakage Current at V _R =75V	I _R	5 _{MAX}	uA
Capacitance at V _R =0V, f=1MHz	C_{tot}	4 _{MAX}	pF
Reverse Recovery Time at $I_F = I_R = 10 \text{mA}, R_L = 100$	t _{rr}	4 _{MAX}	ns

¹⁾ Valid provided that electrodes are kept at ambient temperature.

TYPICAL CHARACTERISTICS

Figure 1. Forward Characteristic

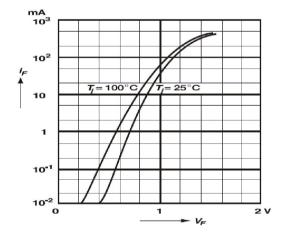
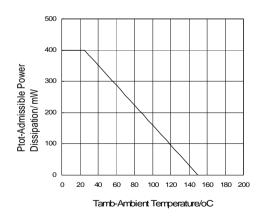


Figure 2. Power De-rating



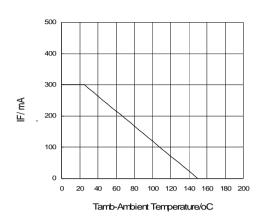


CD4148WP

Figure 4. Reverse Voltage De-rating

3:6

Figure 3. Forward Current Derating



150 100 VR/V 50

Tamb-Ambient Temperature/oC

TEST CHARACTERISTICS

Test Item	Test Condition	Requirement
Solderability	Sn bath at 245±5°C for 2±0.5s	>95% area tin covered
Resistance to Soldering Heat	Sn bath at 260±5°C for 10±2s	V _F ,V _R & I _R within spec; no mechanical damage
Humidity Steady State	At 85°C 85%RH for 168hrs	V _F ,V _R & I _R within spec
Continue Forward Operating Life	At 25° C I _F = 1.1I _F for 1000hrs	V _F ,V _R & I _R within spec
Thermal Shock	$-55 \pm 5^{\circ}$ C/5min to $150 \pm 5^{\circ}$ C/5min for 10cycles	V _F ,V _R & I _R within spec
Bending Strength	Bending up to 2mm for 1cycle	V _F ,V _R & I _R within spec; no mechanical damage

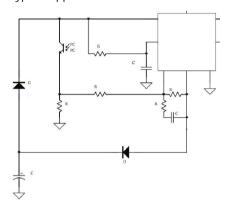


CD4148WP

4:6

APPLICATIONS

- Function: suit for small signal switching application n
- Typical Application circuit:



Typical Product field: General application except high reverse voltage location n

n Soldering Condition:

Soldering Condition & Caution

Recommended Soldering Condition

(Refer to IPC/JEDEC J-STD-020D 4-1&5.2)

Recommended Profile Condition	Sn-Pb Soldering	Leadfree Soldering	Wave Soldering
Ramp-up rate (from pre-heat stage)	<3°C/s	<3°C/s	T<150°C
Dro hoot Tomporature 9 Time	100-150 °C	150-200 °C	100-150 °C
Pre-heat Temperature & Time	60-120s	60-120s	60-120s
Coldoring Tomporature 9 Time	183 °C	217 °C	260±5°C
Soldering Temperature & Time	60-150s	60-150s	5±2s
Dook Tomporaturo	230±5°C	245±5°C	260±5°C
Peak Temperature	<260°C	<260°C	200±3 C
Time within 5°C of peak temperature	10-20s	20-30s	-
Ramp-down rate	<6°C/s	<6°C/s	<6°C/s
Time 25°C to peak temperature	<6min	<8min	-

Manual Soldering: Approx. 350°C for 3s, avoid solder iron tip direct touch the components body



CD4148WP

5:6

Recommended Soldering Profile

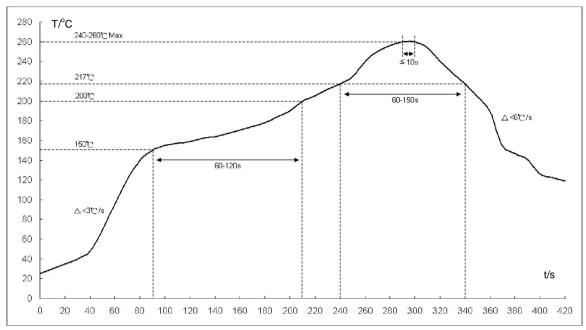


Fig1: Reflow soldering profile for lead-free solder (SnAgCu)

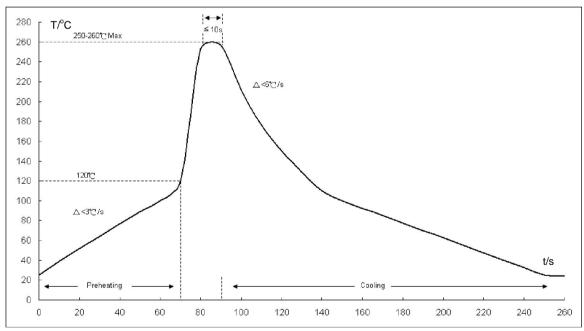


Fig2: Wave soldering profile

- *1. The recommended profiles are referring to IPC/JEDEC J-STD-020D & IEC-60068-2-58
- *2. Chip diodes are able to stand maximum soldering temperature up to 260°C max for 10s, and the soldering cycles with max 3 times, referring to IEC-60068-2-58

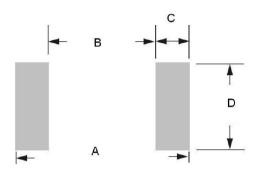
LIZ



CD4148WP

6:6

n Recommended Soldering Footprint:



Reflow/Wave Soldering					
Product Size	Dimension/ mm				
Product Size	А	B C D			
1206	3.8-4.6	2.2	0.8-1.2	1.5-1.7	

n Storage Condition: Product termination solderability can degrade due to high temperature and humidity or chemical environment. Storage condition must be in an ambient temperature of $<40^{\circ}$ C and ambient humidity of <75%RH, and free from chemical.

ENVIRONMENTAL CHARACTERISTICS

	Hazardous Substance or Element/ppm					
Product	Pb	Cd	Hg	Cr ⁶⁺	PBB	PBDE
	<1000	<100	<1000	<1000	<1000	<1000
	Halogen Substance/ ppm					
Product	F	CI	Е	Br	I	Total
	<900	< 900) <9	000	<900	<1500

PACKING METHOD

Product	Quality/Reel	Reel Size	Tape
rioduct	5,000pcs	7"	Paper

DISCLAIMERS

These products are not designed for use in applications where any failure or malfunction may resulted in personal injury, death or severe property or environmental damage such as medical, military, aircraft, space or life support equipments.

LIZ ______ www.lizgroup.com