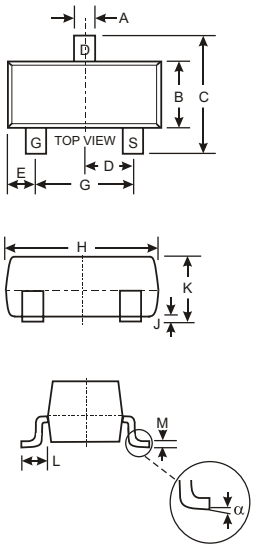
2N7002K

N-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR

Features

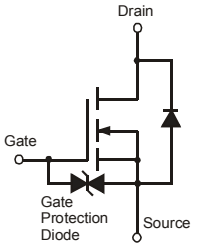
- High density cell design for Low $R_{DS(on)}$.
- Voltage controlled small signal switch.
- Rugged and reliable.
- High saturation current capability.
- ESD protected.
- Marking Code:72K



SOT-23		
Dim	**Min**	**Max**
A	0.37	0.51
B	1.20	1.40
C	2.30	2.50
D	0.89	1.03
E	0.45	0.60
G	1.78	2.05
H	2.80	3.00
J	0.013	0.10
K	0.903	1.10
L	0.45	0.61
M	0.085	0.180
α	0°	8°
All Dimensions in mm		

Maximum Ratings @ T_A = 25°C unless otherwise specified

Parameter	Symbol	Limit	Unit
Drain-source Voltage	V_{DS}	60	V
Gate-source Voltage	V_{GS}	±20	V
Drain Current	I_D	300	mA
Pulsed Drain Current [A]	I_{DM}	1.5	A
Total Power Dissipation @ T_A=25℃	P_D	350	mW
Thermal Resistance Junction-to-Ambient @ Steady State [B]	$R_{\theta JA}$	357	℃/ W
Junction and Storage Temperature Range	T_J ,T_{STG}	-55～+150	℃



EQUIVALENT CIRCUIT

A. Pulse Test: Pulse Width≤300us,Duty cycle ≤2%.
B. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch.

Electrical Characteristics @ T_A = 25°C unless otherwise specified

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Static Parameter						
Drain-Source Breakdown Voltage	BV_{DSS}	VGS= 0V, ID=250μA	60			V
Zero Gate Voltage Drain Current	I_{DSS}	VDS=60V,VGS=0V			1	μA
Gate-Body Leakage Current	I_{GSS}	VGS= ±20V, VDS=0V			±10	μA
Gate Threshold Voltage	$V_{GS(th)}$	VDS= VGS, ID=250μA	0.8	1.5	3.0	V
Static Drain-Source On-Resistance	$R_{DS(ON)}$	VGS= 10V, ID=450mA		2.0	3.5	Ω
		VGS= 4.5V, ID=200mA		2.8	4.5	
Diode Forward Voltage	V_{SD}	IS=450mA,VGS=0V			1.2	V
Maximum Body-Diode Continuous Current	IS				300	mA
Dynamic Parameters						
Input Capacitance	C_{iss}	VDS=60V,VGS=0V ,f=1MHZ		18		pF
Output Capacitance	C_{oss}			12		
Reverse Transfer Capacitance	C_{rss}			7		
Switching Parameters						
Total Gate Charge	Qg	VGS=10V,VDS=60V, ID=0.3A		1.7	2.4	nC
Turn-on Delay Time	$t_{D(on)}$	VGS=10V,VDD=30V, ID=300mA, RGEN=6Ω		5		ns
Turn-off Delay Time	$t_{D(off)}$			17		
Reverse recovery Time	trr	VGS=0V,IS=300mA, VR=25V, dIS/dt=- 100A/μs		30		ns

TYPICAL TRANSIENT CHARACTERISTICS

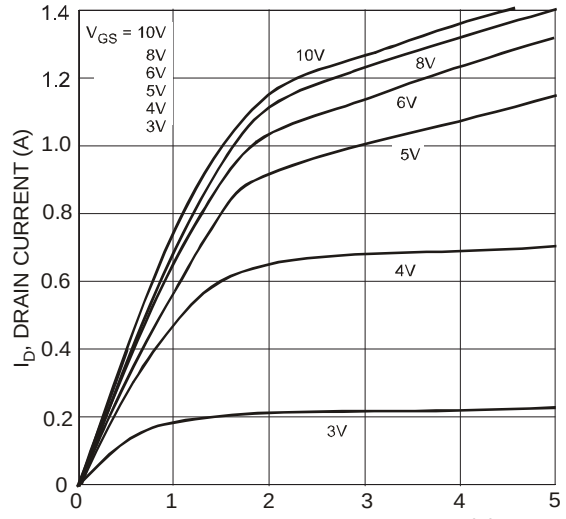


Fig. 1 Typical Output Characteristics

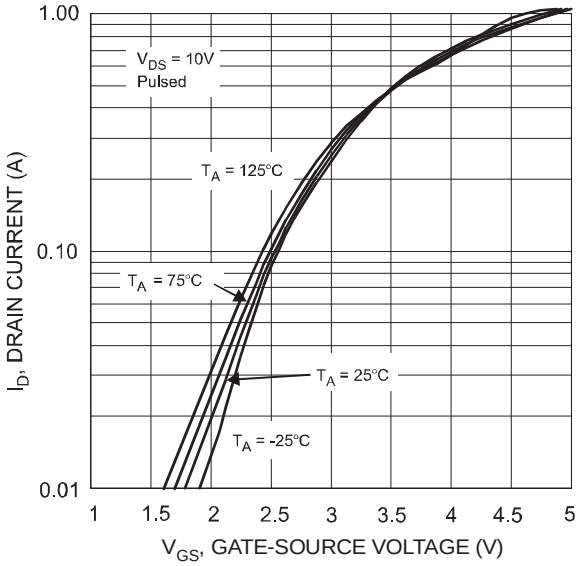


Fig. 2 Typical Transfer Characteristics

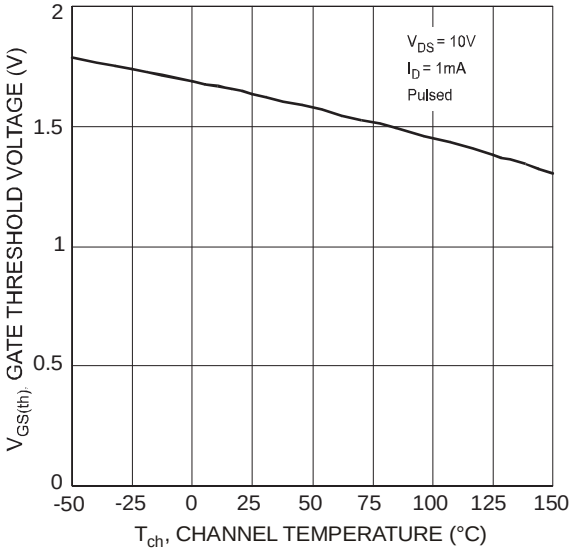


Fig. 3 Gate Threshold Voltage vs. Channel Temperature

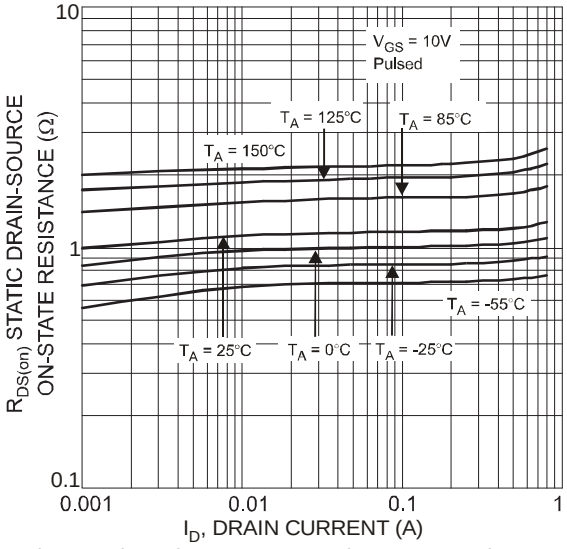


Fig. 4 Static Drain-Source On-Resistance vs. Drain Current

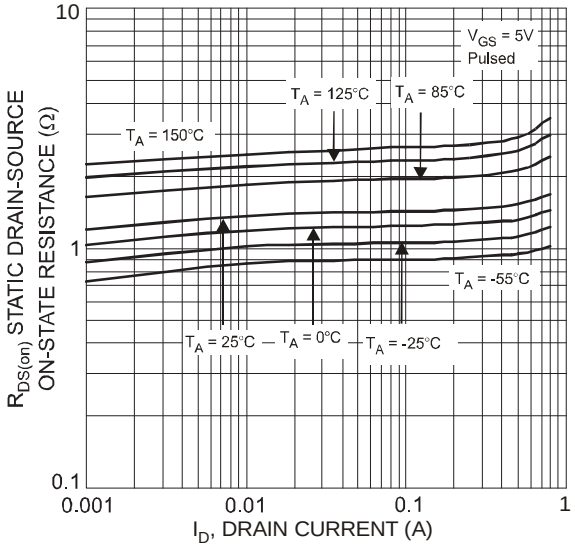


Fig. 5 Static Drain-Source On-Resistance vs. Drain Current

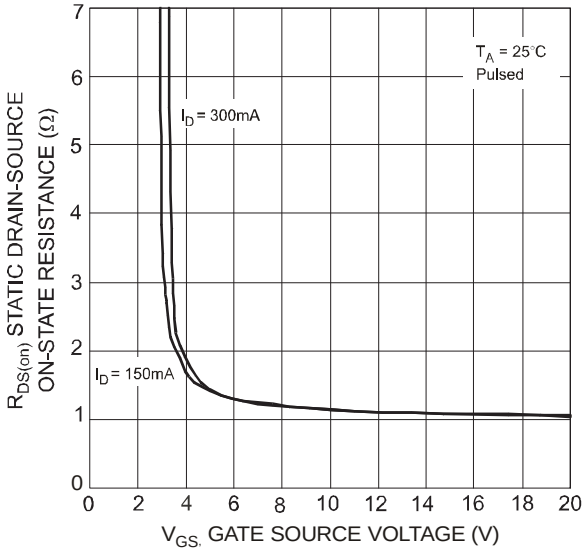


Fig. 6 Static Drain-Source On-Resistance vs. Gate-Source Voltage

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