

4-Line Ultra Low Capacitance TVS Diode Array

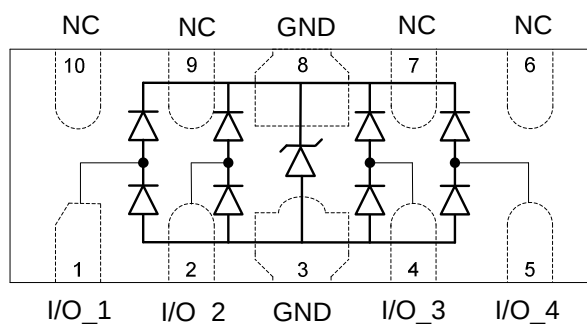
Description

The RCLAMP0524P is an ultra low capacitance TVS array, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive high-speed data lines. The RCLAMP0524P has an ultra-low capacitance with a typical value at 0.3pF, and complies with the IEC 61000-4-2 (ESD) standard with $\pm 15\text{kV}$ air and $\pm 8\text{kV}$ contact discharge. It is assembled into a 10-pin 2.5 x 1.0 x 0.5mm lead-free DFN package. The flow through style package allows for easy PCB layout and matched trace lengths necessary to maintain consistent impedance between high speed differential lines such as USB 3.0 and HDMI. The small size, ultra-low capacitance and high ESD surge protection make RCLAMP0524P an ideal choice to protect HDMI, MDDI, USB 3.0 and other high speed ports.

Mechanical Characteristics

- ◆ Package: DFN2510-10 (2.5 x 1.0 x 0.5mm)
- ◆ Lead Finish: Matte Tin
- ◆ UL Flammability Classification Rating 94V-0
- ◆ Case Material: "Green" Molding Compound
- ◆ Moisture Sensitivity: Level 3 per J-STD-020
- ◆ Terminal Connections: See Diagram Below
- ◆ Marking Information: See Below

Dimensions and Pin Configuration



Circuit and Pin Schematic

Ordering Information

Part Number	Marking	Packaging	Reel Size
RCLAMP0524P	0524P	3000/Tape & Reel	7 inch

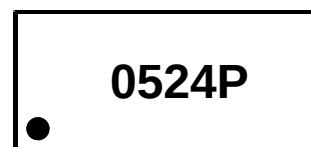
Features

- ◆ Ultra low capacitance: 0.3pF typical (I/O to I/O)
- ◆ Ultra low leakage: nA level
- ◆ Low operating voltage: 5V
- ◆ Low clamping voltage
- ◆ Up to 4 lines protects
- ◆ Leadless flow-through package
- ◆ Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test
 - Air discharge: $\pm 25\text{kV}$
 - Contact discharge: $\pm 20\text{kV}$
 - IEC61000-4-4 (EFT) 40A (5/50ns)
 - IEC61000-4-5 (Lightning) : 5A(8/20 μs)
- ◆ ROHS Compliant

Applications

- ◆ HDMI 1.3 & 1.4, USB 2.0 & 3.0 and MDDI ports
- ◆ Monitors and flat panel displays
- ◆ Set-top box and digital TV
- ◆ Video graphics cards
- ◆ Digital video interface(DVI)
- ◆ Notebook Computers
- ◆ PCI express and Serial SATA ports

Marking Information



Dot denotes Pin1

4-Line Ultra Low Capacitance TVS Diode Array

Absolute Maximum Ratings ($T_A=25^{\circ}\text{C}$ unless otherwise specified)

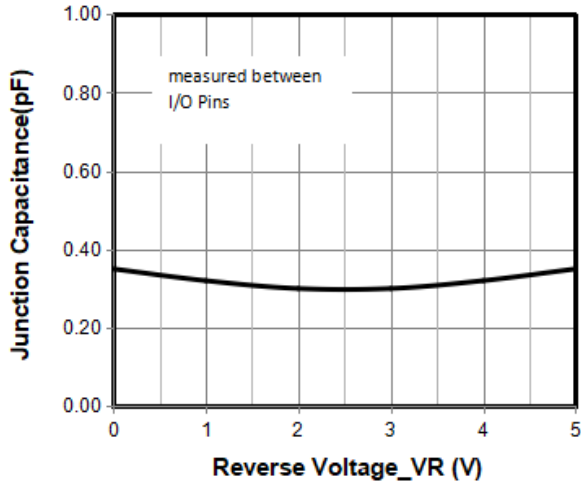
Parameter	Symbol	Value	Unit
Peak Pulse Power (tp=8/20μs)	PPP	80	W
Peak Pulse Current (tp=8/20μs)	IPP	5	A
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	VESD	±25 ±20	kV
Operating Temperature Range	TJ	-55 to +125	°C
Storage Temperature Range	Tstg	-55 to +150	°C

Electrical Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise specified)

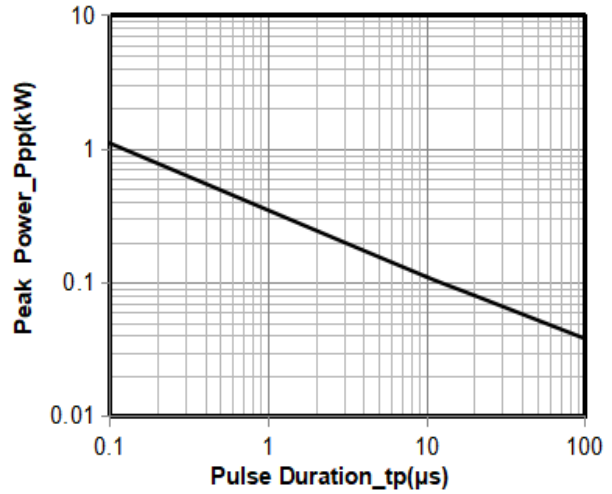
Parameter	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Working Voltage	VRWM			5	V	Any I/O pin to ground
Breakdown Voltage	VBR	6			V	IT = 1mA, any I/O pin to ground
Reverse Leakage Current	IR		0.01	0.5	μA	VRWM = 5V, any I/O pin to ground
Clamping Voltage	VC			9	V	IPP = 1A (8 x 20μs pulse), any I/O pin to ground
Clamping Voltage	VC			16	V	IPP = 5A (8 x 20μs pulse), any I/O pin to ground
Junction Capacitance	CJ		0.3	0.4	pF	VR = 0V, f = 1MHz, between I/O pins
Junction Capacitance	CJ			0.8	pF	VR = 0V, f = 1MHz, any I/O pin to ground

4-Line Ultra Low Capacitance TVS Diode Array

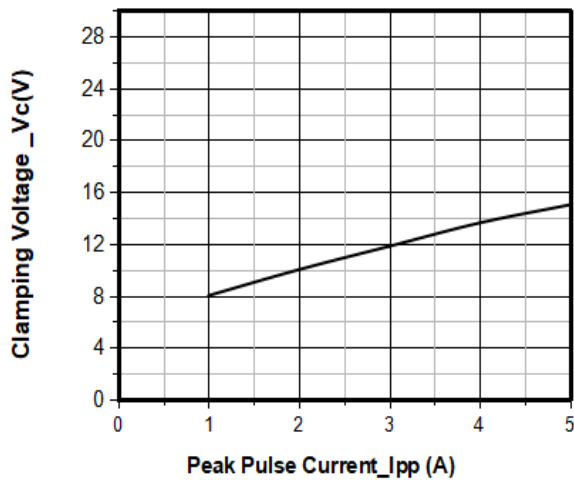
Typical Performance Characteristics (TA=25°C unless otherwise Specified)



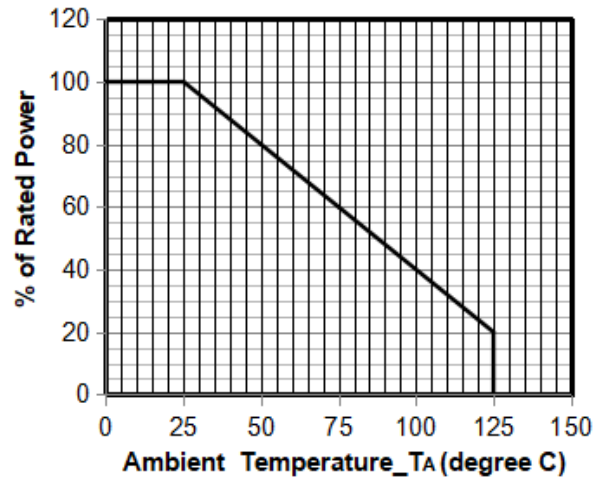
Junction Capacitance vs. Reverse Voltage



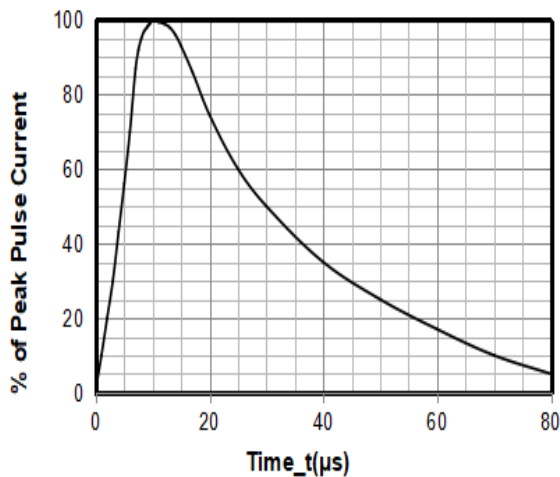
Peak Pulse Power vs. Pulse Time



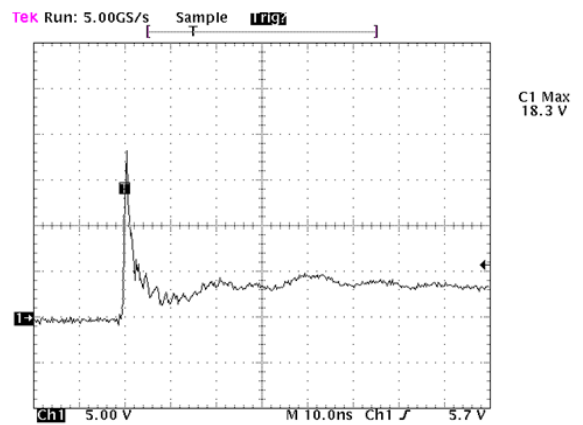
Clamping Voltage vs. Peak Pulse Current



Power Derating Curve



8 X 20μs Pulse Waveform



Note: Data is taken with a 10x attenuator

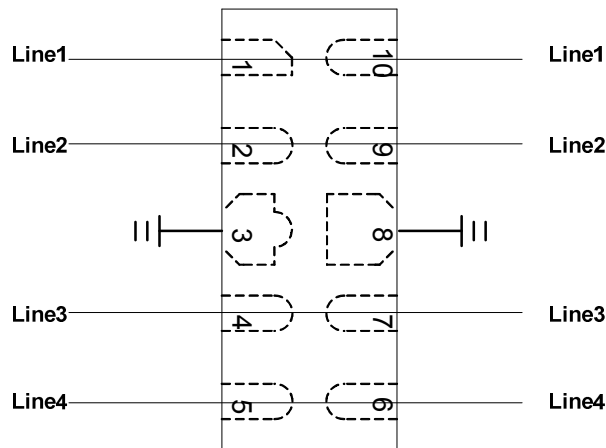
ESD Clamping Voltage

8 kV Contact per IEC61000-4-2

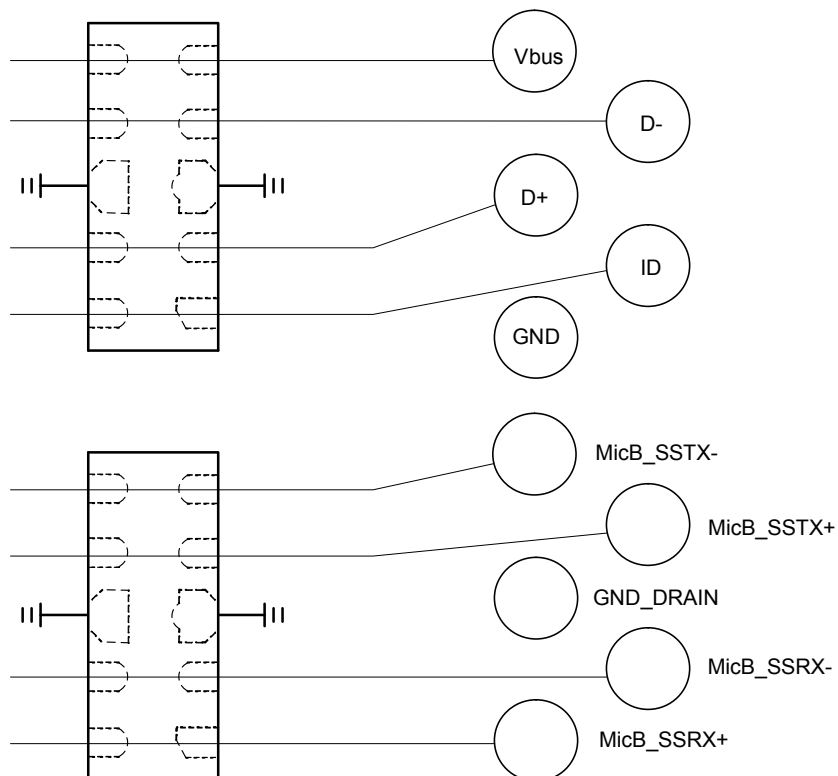
4-Line Ultra Low Capacitance TVS Diode Array

Typical Application

The RCLAMP0524P is designed for easy PCB layout by allowing the traces to run straight through the device. The PCB traces could be used to connect the pin pairs for each line. For example, line 1 enters at pin 1 and exits at pin 10 and the PCB trace connects pin 1 and pin 10 together. Ground is connected at pin 3 and pin 8.

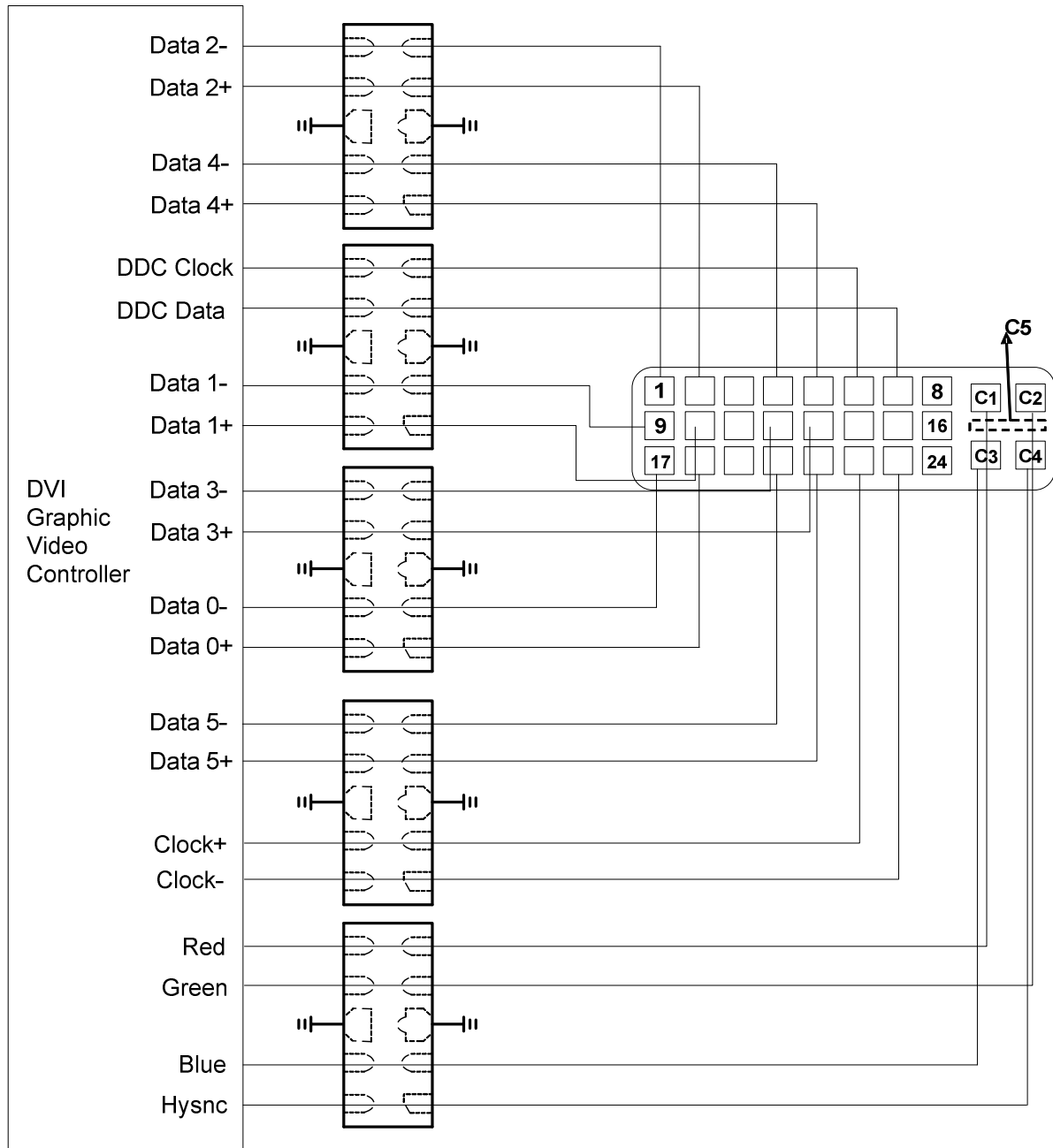


RCLAMP0524P on USB 3.0 Port Application

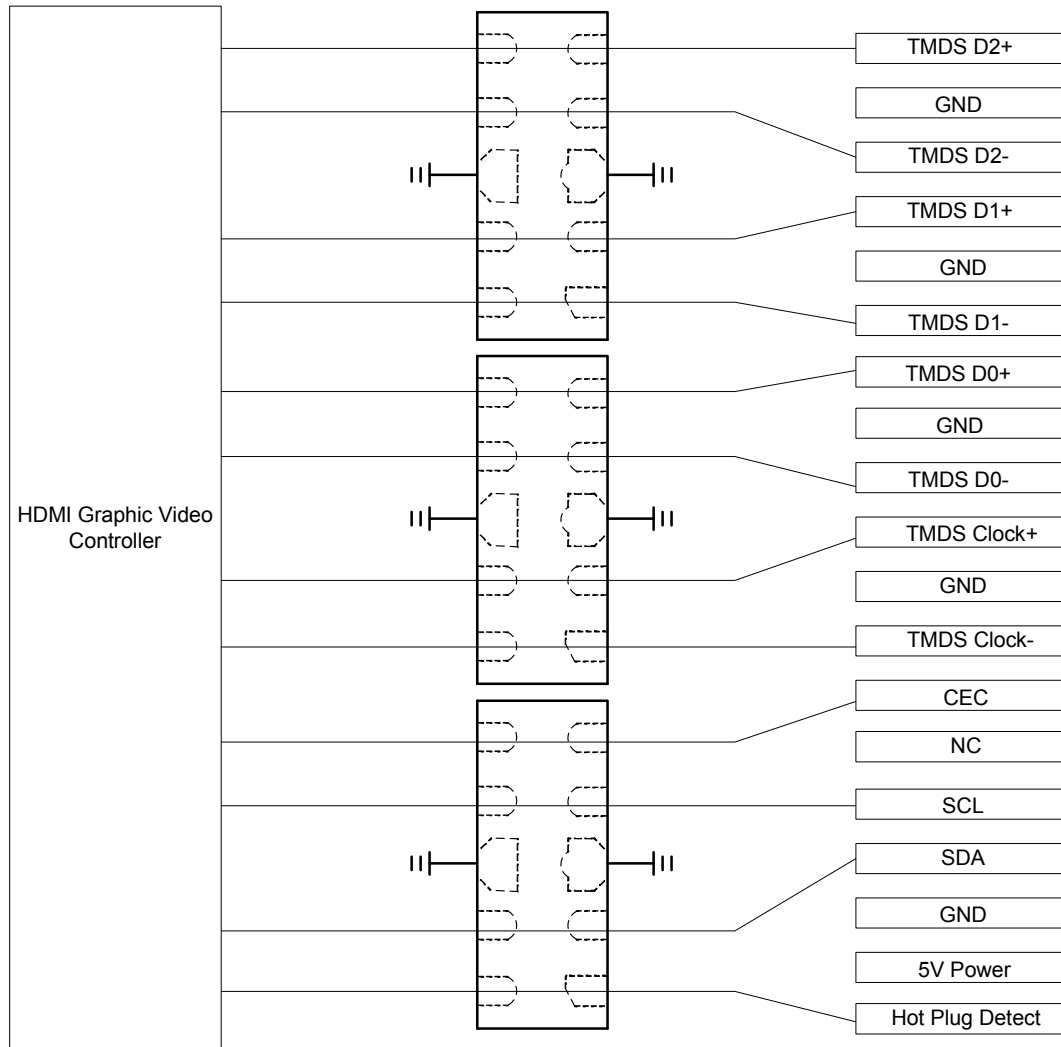


4-Line Ultra Low Capacitance TVS Diode Array

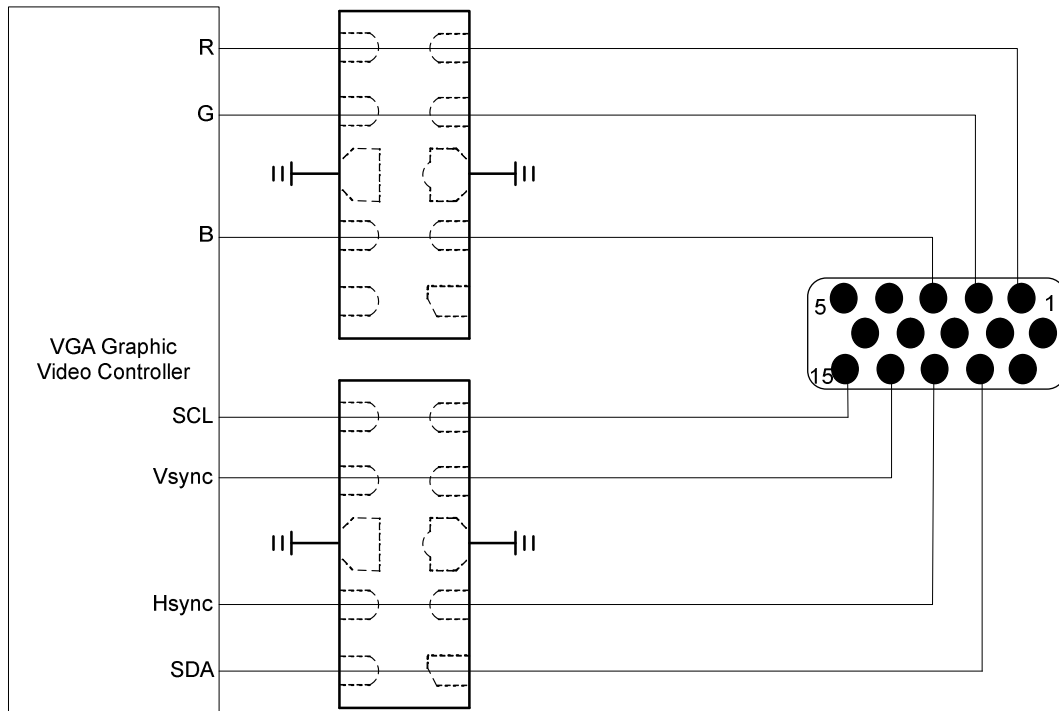
RCLAMP0524P on DVI Port Application



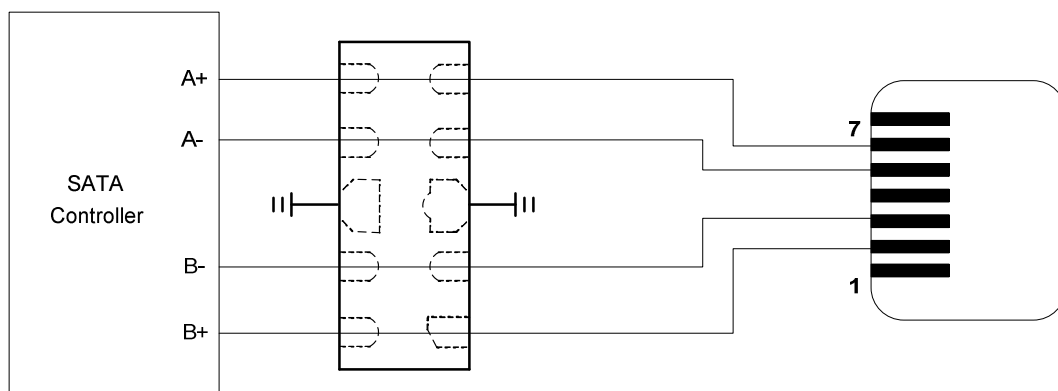
4-Line Ultra Low Capacitance TVS Diode Array
RCLAMP0524P on HDMI Port Application



4-Line Ultra Low Capacitance TVS Diode Array
RCLAMP0524P on VGA Port Application

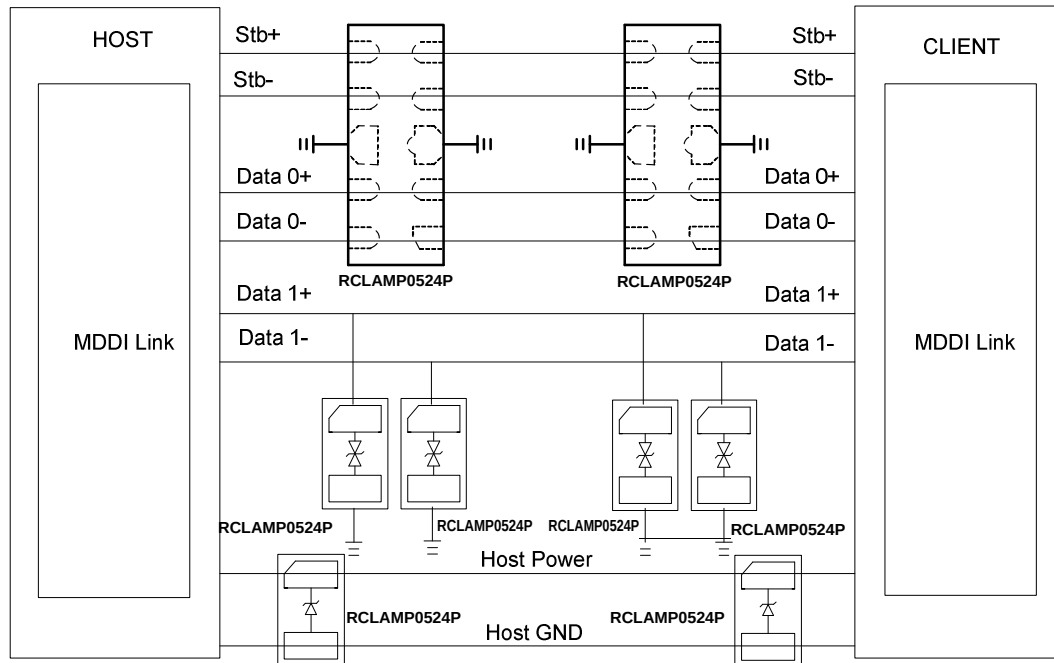


RCLAMP0524P on SATA Port Application

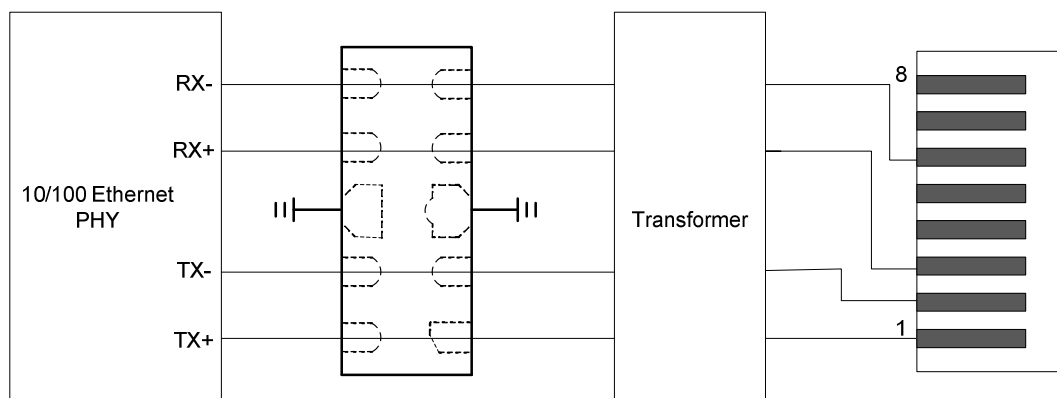


4-Line Ultra Low Capacitance TVS Diode Array

RCLAMP0524P on MDDI Port Application

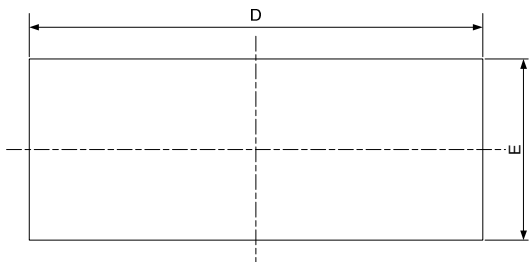


RCLAMP0524P on 10/100 Base Ethernet Port Application

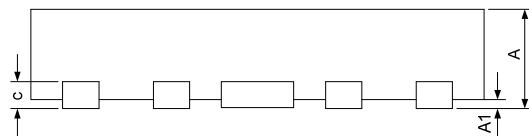


4-Line Ultra Low Capacitance TVS Diode Array

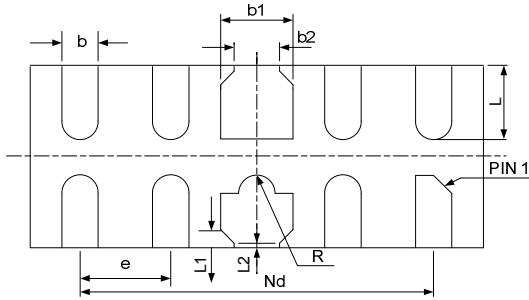
DFN2510-10 Package Outline Drawing



TOP VIEW



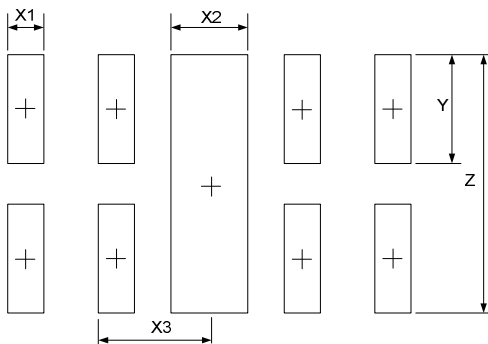
SIDE VIEW



BOTTOM VIEW

SYM	DIMENSIONS					
	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.45	0.50	0.55	0.018	0.020	0.022
A1	0.00	0.02	0.05	0.000	0.001	0.002
b	0.15	0.20	0.25	0.006	0.008	0.010
b1	0.35	0.40	0.45	0.014	0.016	0.018
b2	0.20	0.25	0.30	0.008	0.010	0.012
c	0.10	0.15	0.20	0.004	0.006	0.008
D	2.45	2.50	2.55	0.098	0.100	0.102
e	0.50BSC			0.020BSC		
Nd	2.00BSC			0.080BSC		
E	0.95	1.00	1.05	0.038	0.040	0.042
L	0.35	0.40	0.45	0.014	0.016	0.018
L1	0.075REF			0.003REF		
L2	0.050REF			0.002REF		
h	0.08	0.12	0.15	0.003	0.005	0.006
R	0.05	0.10	0.15	0.002	0.004	0.006

Suggested Land Pattern



SYM	DIMENSIONS	
	MILLIMETERS	INCHES
X1	0.200	0.008
X2	0.400	0.016
X3	0.500	0.020
Y	0.600	0.024
Z	1.400	0.056