



N 沟道增强型场效应晶体管 N-CHANNEL MOSFET FHA310N1F1A

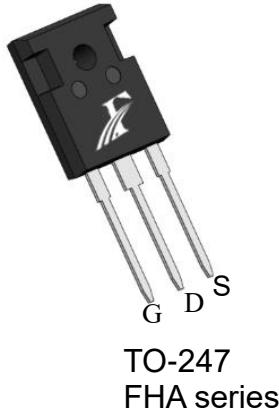
主要参数 MAIN CHARACTERISTICS

ID	310 A
VDSS	100 V
Rdson-typ (@Vgs=10V)	1.85 mΩ
Qg-typ	240 nC

用途 APPLICATIONS

高频开关电源	High efficiency switch mode power supplies
逆变器	Power Management in Inverter System
同步整流	Synchronous Rectification

封装形式 Package

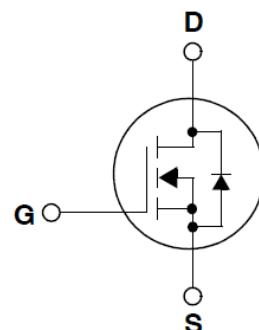


TO-247
FHA series

产品特性 FEATURES

低栅极电荷	Low gate charge
低 Crss (典型值 935 pF)	Low Crss (typical 935 pF)
开关速度快	Fast switching
100% 经过雪崩测试	100% avalanche tested
100% 经过热阻测试	100% DVDS tested
100% 经过 RG 测试	100% Rg tested
高抗 dv/dt 能力	Improved dv/dt capability
SGT 工艺	SGT technology
RoHS 产品	RoHS product

等效电路 Equivalent Circuit



绝对最大额定值 ABSOLUTE RATINGS (Tc=25°C)

项目 Parameter	符号 Symbol	数值 Value	单位 Unit
		FHA310N1F1A	
最高漏极—源极直流电压 Drain-Source Voltage	VDS	100	V
连续漏极电流* Drain Current -continuous *	Id (Tc=25°C)	310	A
	Id (Tc=100°C)	254	A
最大脉冲漏极电流 (注 1) Drain Current – pulse (note 1)	Idm	1240	A
最高栅源电压 Gate-Source Voltage	VGS	±20	V
单脉冲雪崩能量 (注 2) Single Pulsed Avalanche Energy (note 2)	EAS	800	mJ
雪崩电流 (注 1) Avalanche Current (note 1)	IAR	40	A
重复雪崩能量 (注 1) Repetitive Avalanche Current (note 1)	EAR	80	mJ
二极管反向恢复最大电压变化速率 (注 3) Peak Diode Recovery dv/dt (note 3)	dv/dt	5.0	V/ns
耗散功率 Power Dissipation	Pd (TC=25°C)	357	W
	-Derate above 25°C	2.86	W/°C
最高结温及存储温度 Operating and Storage Temperature Range	Tj, Tstg	150, -55 to 150	°C
引线最高焊接温度 Maximum Lead Temperature for Soldering Purposes	T _L	300	°C

*漏极电流由最高结温限制

*Drain current limited by maximum junction temperature

电特性 ELECTRICAL CHARACTERISTICS

项目 Parameter	符号 Symbol	测试条件 Tests conditions	最小 Min	典型 Typ	最大 Max	单位 Units	
关态特性 Off -Characteristics							
漏一源击穿电压 Drain-Source Voltage	BV_{DSS}	$I_D=250\mu A, V_{GS}=0V$	100	-	-	V	
击穿电压温度特性 Breakdown Voltage Temperature Coefficient	$\Delta BV_{DSS}/\Delta T_J$	$I_D=250\mu A$, referenced to $25^\circ C$	-	0.1	-	V/ $^\circ C$	
零栅压下漏极漏电流 Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=100V, V_{GS}=0V, T_c=25^\circ C$	-	-	1	μA	
		$V_{DS}=80V, T_c=125^\circ C$	-	-	100	μA	
栅极体漏电流 Gate-body leakage current	$I_{GSS} (F/R)$	$V_{DS}=0V, V_{GS} =\pm 30V$	-	-	± 100	nA	
通态特性 On-Characteristics							
阈值电压 Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D=250\mu A$	2.0	-	4.0	V	
静态导通电阻 Static Drain-Source On-Resistance	$R_{DS(ON)}$	$V_{GS} =10V$, $I_D=50A$	-	1.85	2.1	$m\Omega$	
正向跨导 Forward Transconductance	g_{fs}	$V_{DS} = 15V, I_D=50A$ (note 4)	-	40	-	S	
动态特性 Dynamic Characteristics							
栅电阻 Gate Resistance	R_g	$f=1.0MHz, V_{DS} OPEN$	-	1.4	-	Ω	
输入电容 Input capacitance	C_{iss}	$V_{DS}=50V,$ $V_{GS} =0V,$ $f=1.0MHz$	-	14100	-	pF	
输出电容 Output capacitance	C_{oss}		-	4210	-		
反向传输电容 Reverse transfer capacitance	C_{rss}		-	915	-		
开关特性 Switching Characteristics							
延迟时间 Turn-On delay time	$t_{d(on)}$	$V_{DS}=50V,$ $I_D=50A,$ $R_g=1.6\Omega$ $V_{GS} =10V$ (note 4, 5)	-	45	-	ns	
上升时间 Turn-On rise time	t_r		-	51	-	ns	
延迟时间 Turn-Off delay time	$t_{d(off)}$		-	123	-	ns	
下降时间 Turn-Off Fall time	t_f		-	52	-	ns	
栅极电荷总量 Total Gate Charge	Q_g	$V_{DS} =50V$, $I_D=50A$, $V_{GS} =10V$ (note 4, 5)	-	240	-	nC	
栅一源电荷 Gate-Source charge	Q_{gs}		-	64	-	nC	
栅一漏电荷 Gate-Drain charge	Q_{gd}		-	64	-	nC	
漏一源二极管特性及最大额定值 Drain-Source Diode Characteristics and Maximum Ratings							
正向最大连续电流 Maximum Continuous Drain -Source Diode Forward Current	I_s		-	-	310	A	
正向最大脉冲电流 Maximum Pulsed Drain-Source Diode Forward Current	I_{SM}		-	-	1240	A	
正向压降 Drain-Source Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_{SD}=50A$	-	-	1.2	V	
反向恢复时间 Reverse recovery time	t_{rr}	$V_{GS}=0V, I_s=50A, dI/dt=50A/\mu s$ (note 4)	-	87	-	ns	
反向恢复电荷 Reverse recovery charge	Q_{rr}		-	220	-	nC	

热特性 THERMAL CHARACTERISTIC

项目 Parameter	符号 Symbol	FHA310N1F1A	单位 Unit
结到管壳的热阻 Thermal Resistance, Junction to Case	R _{th(j-c)}	0.35	°C/W
结到环境的热阻 Thermal Resistance, Junction to Ambient	R _{th(j-A)}	40	°C/W

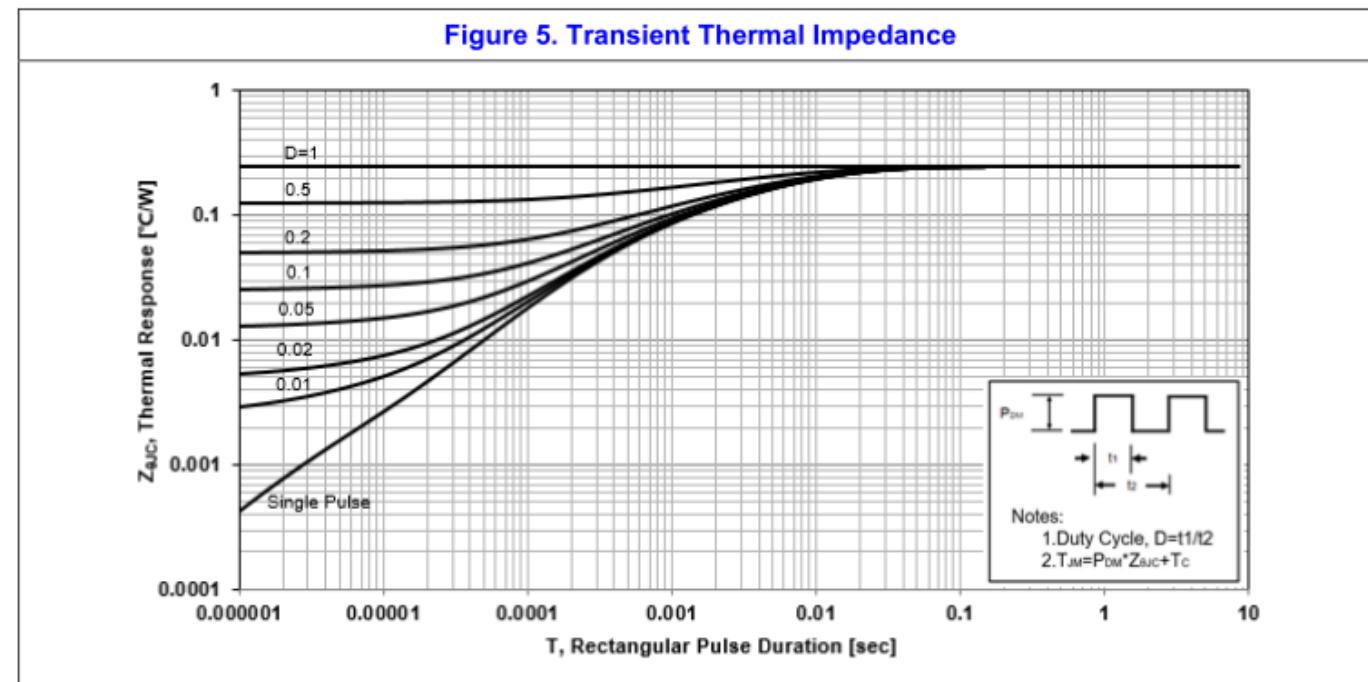
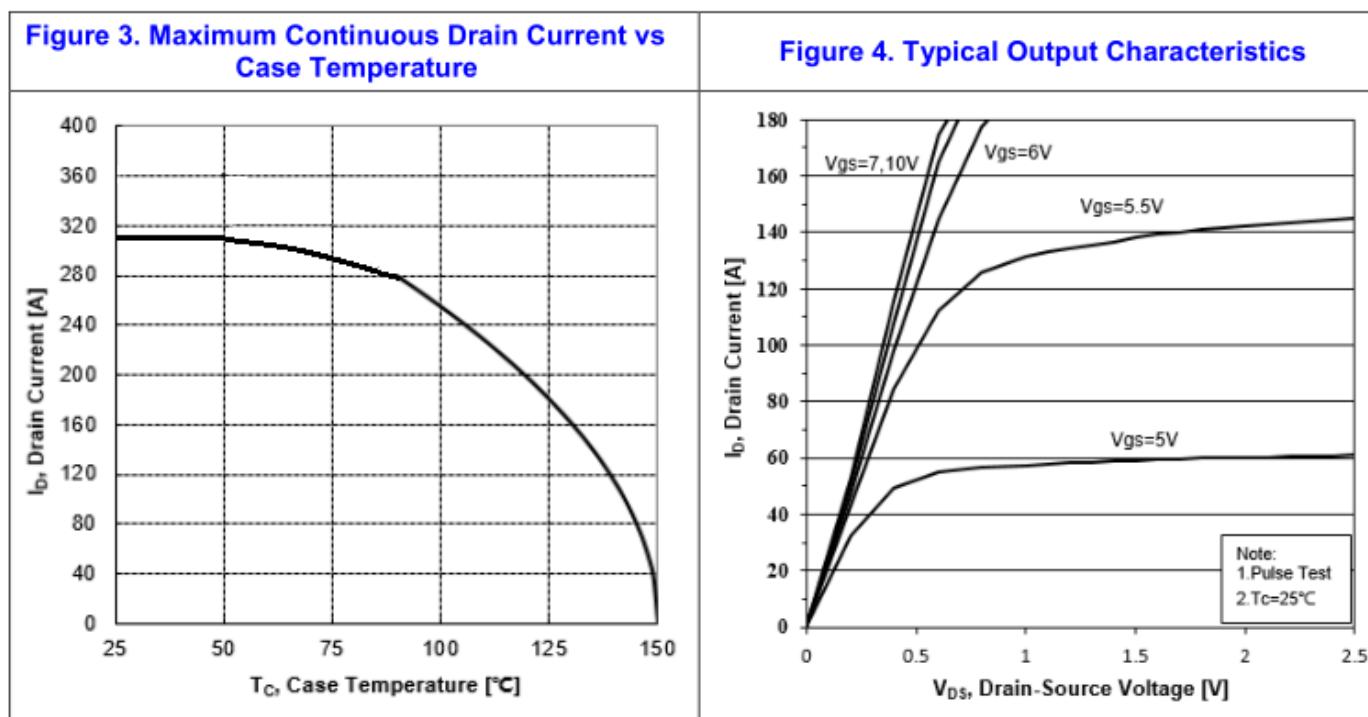
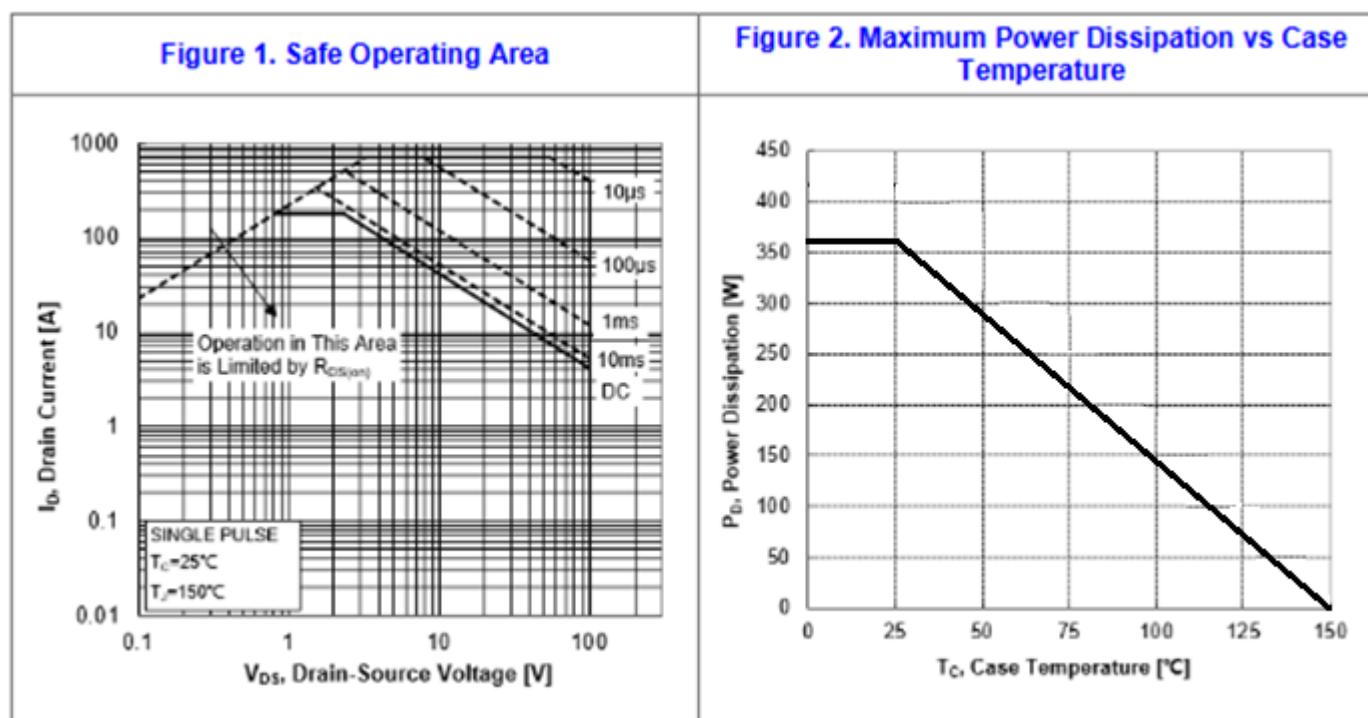
注释:

- 1: 脉冲宽度由最高结温限制
- 2: L=1mH, IAS=40A, VDD=50V, RG=25 Ω, 起始结温 TJ=25°C
- 3: ISD ≤310A, di/dt ≤200A/μs, VDD≤BV_{DSS}, 起始结温 TJ=25°C
- 4: 脉冲测试: 脉冲宽度 ≤300μs, 占空比≤2%
- 5: 基本与工作温度无关

Notes:

- 1: Pulse width limited by maximum junction temperature
- 2: L=1mH, IAS=40A, VDD=50V, RG=25 Ω ,Start TJ=25°C;
- 3: ISD ≤310A, di/dt ≤200A/μs, VDD≤BV_{DSS}, Starting TJ=25°C
- 4: Pulse Test: Pulse Width ≤300μs, Duty Cycle≤2%
- 5: Essentially independent of operating temperature

特性曲线 (ELECTRICAL CHARACTERISTICS (curves))



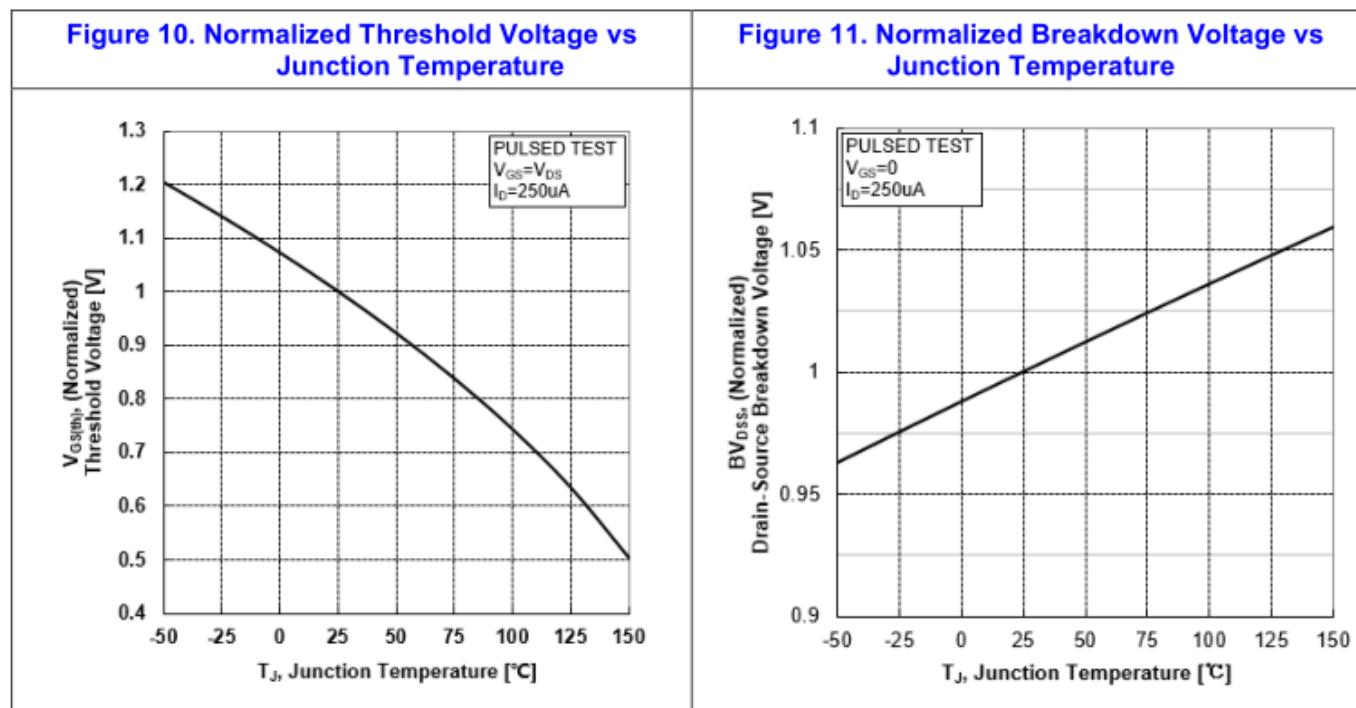
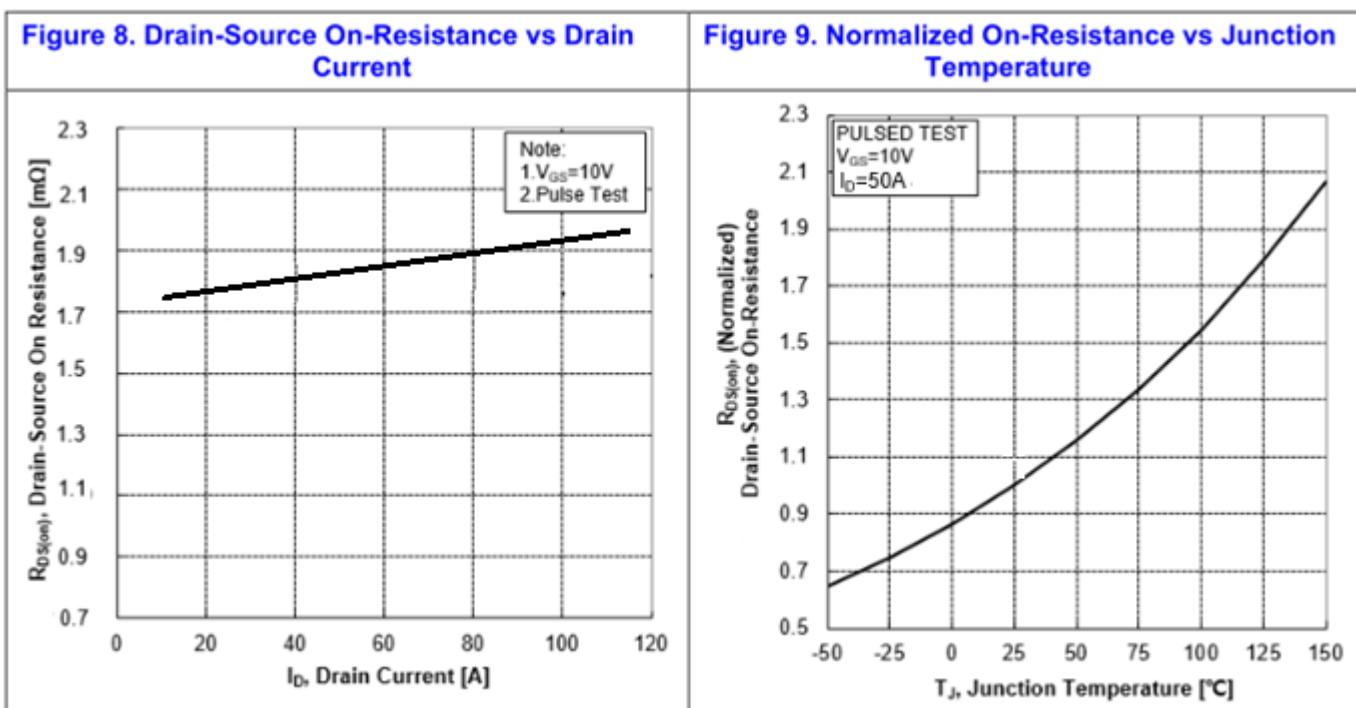
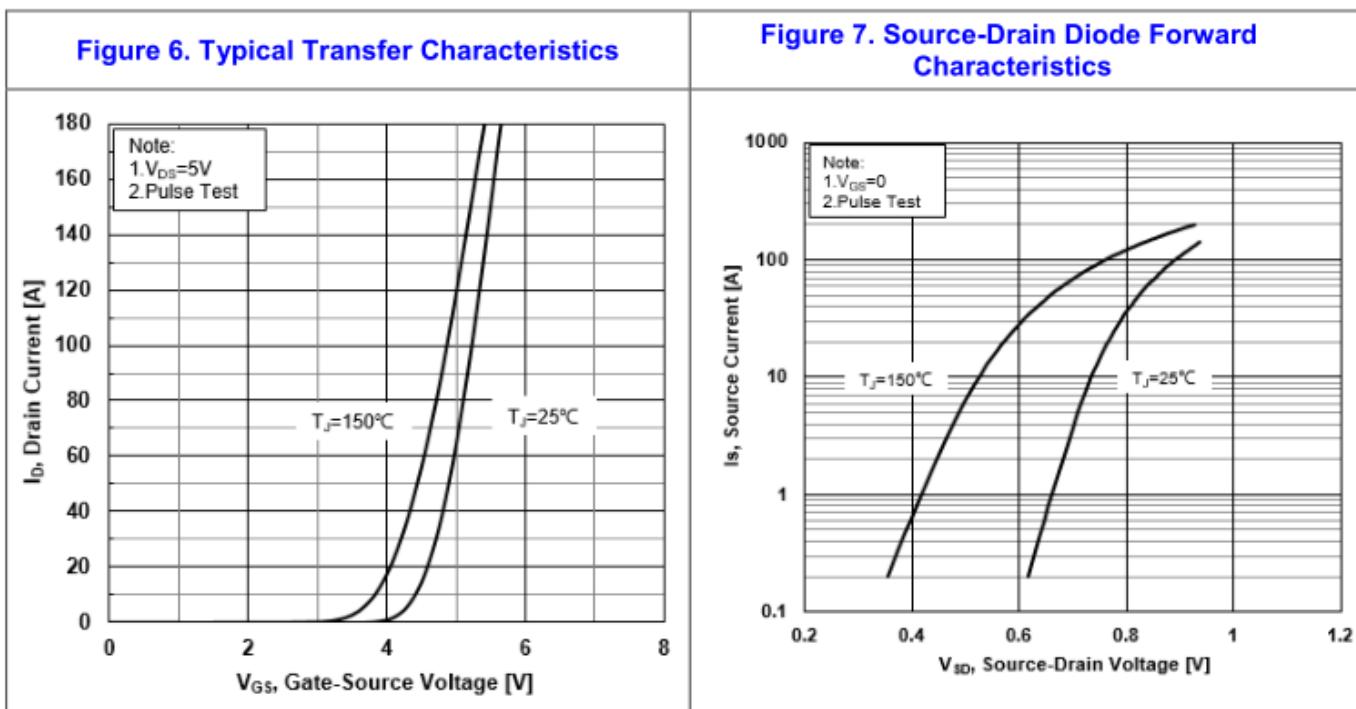


Figure 12. Capacitance Characteristics

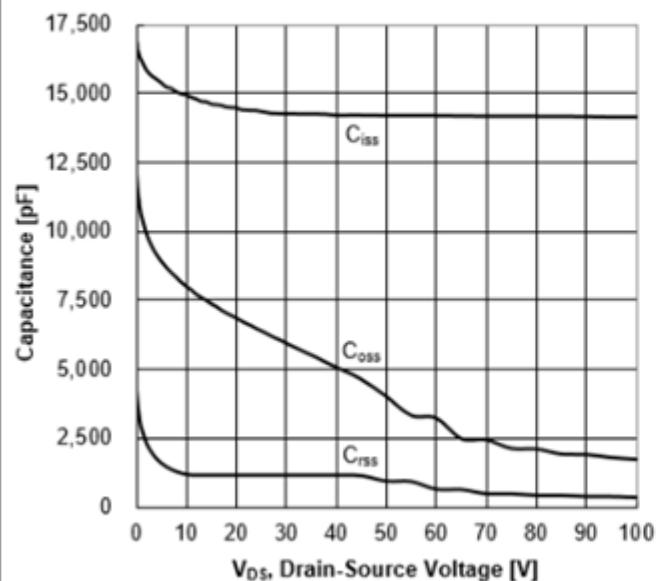
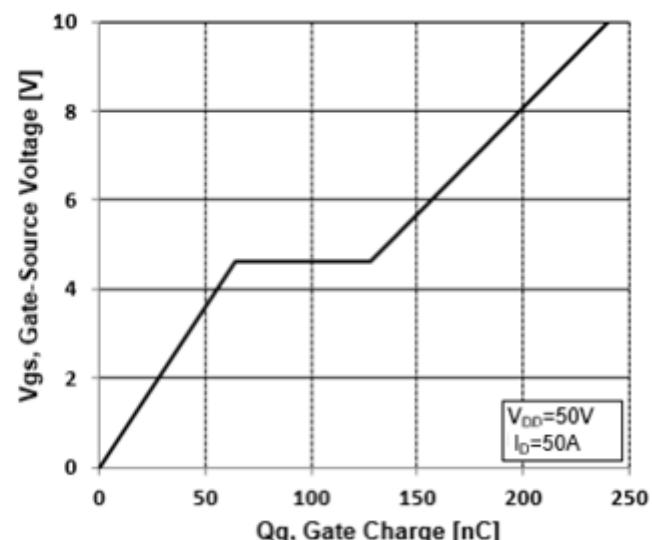
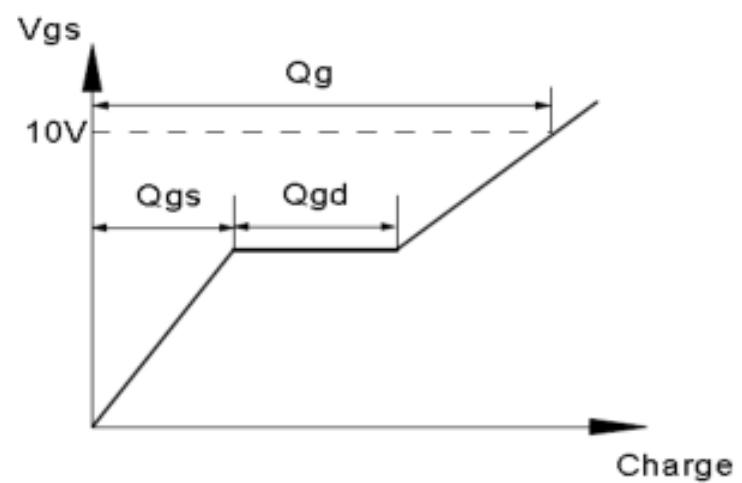
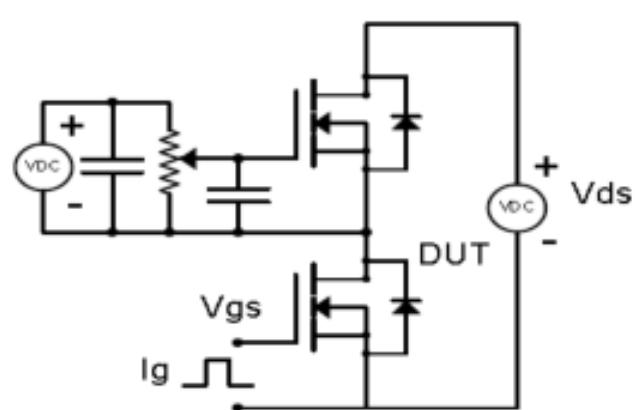


Figure 13. Typical Gate Charge vs Gate-Source Voltage

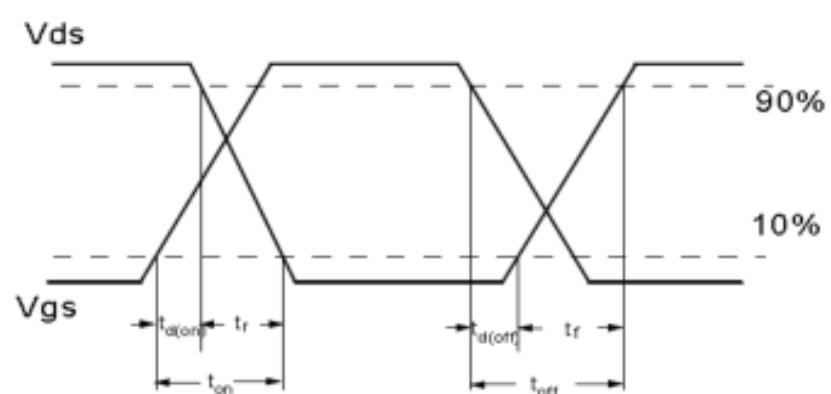
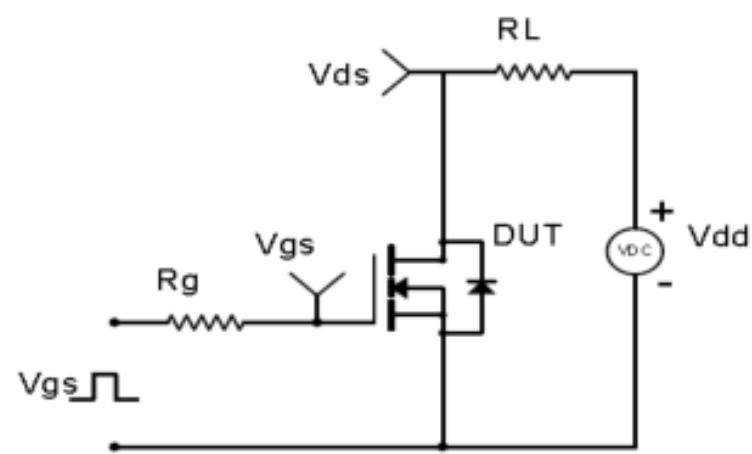


Test Circuit & Waveform

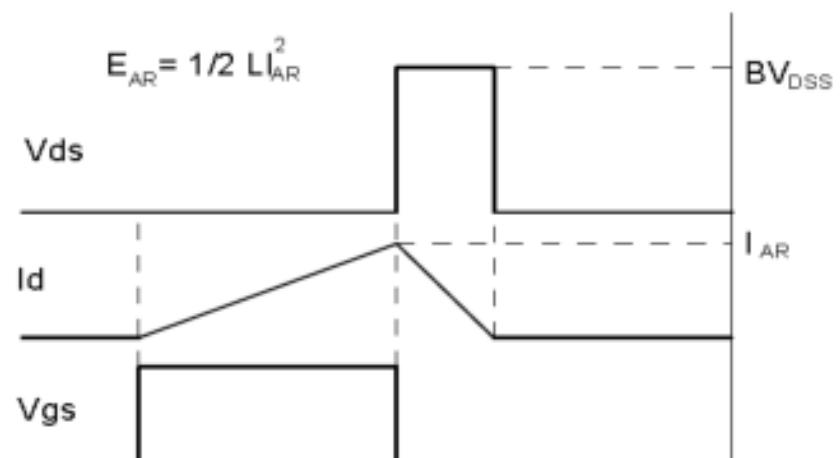
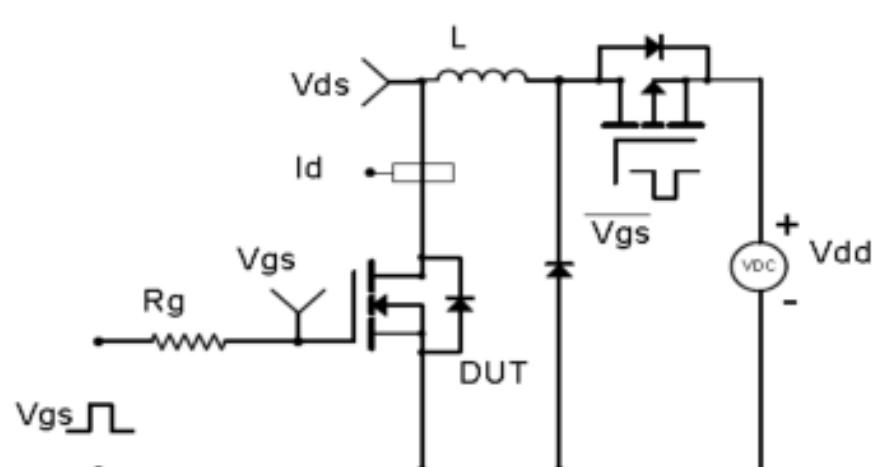
Gate Charge Test Circuit & Waveform



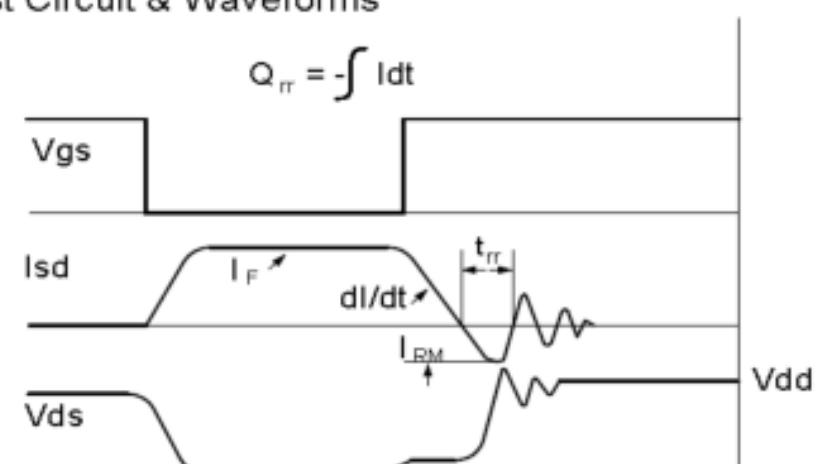
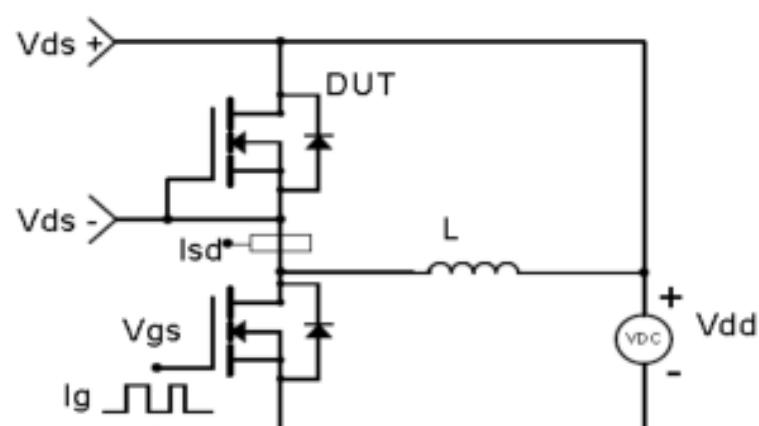
Resistive Switching Test Circuit & Waveforms



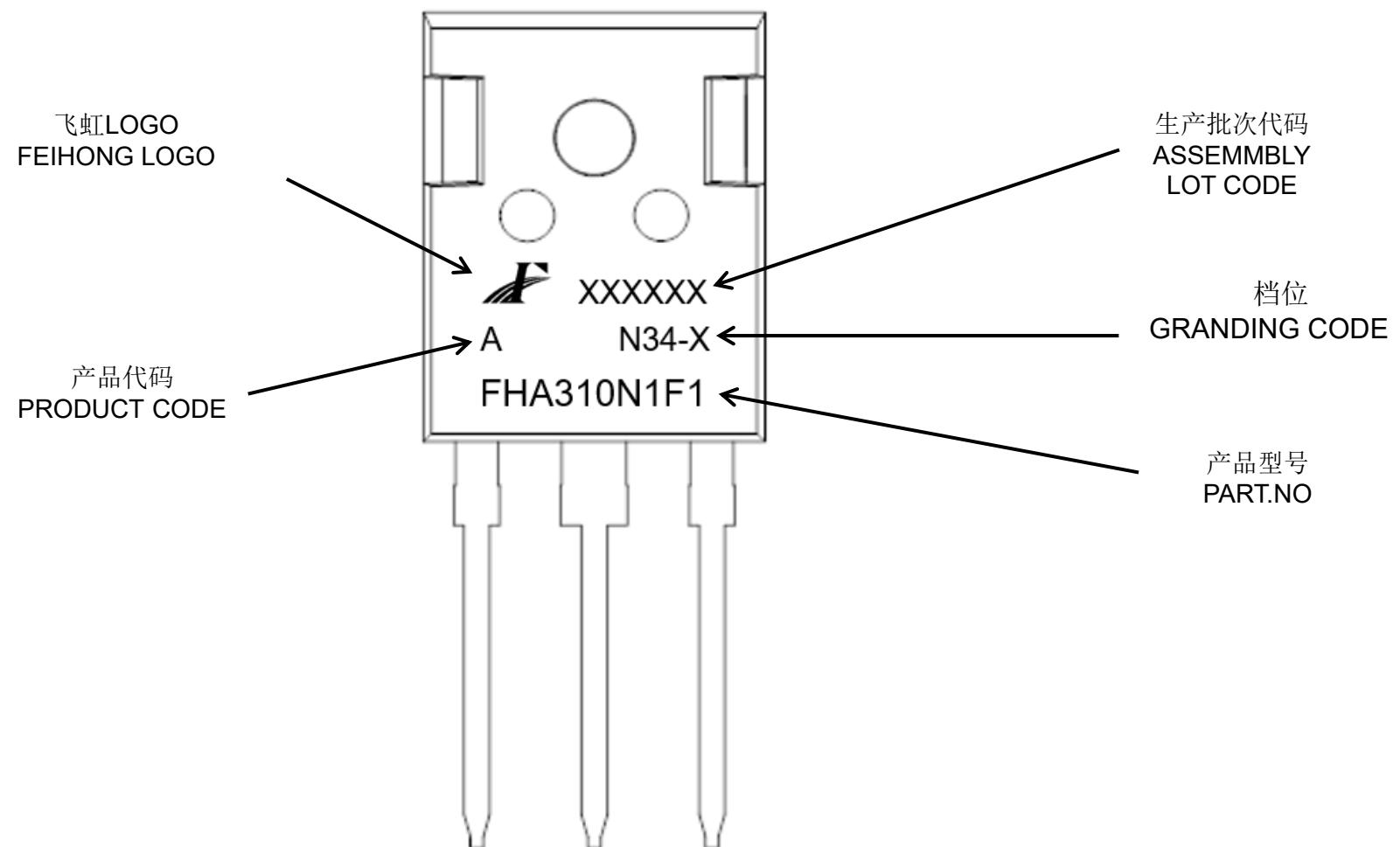
Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



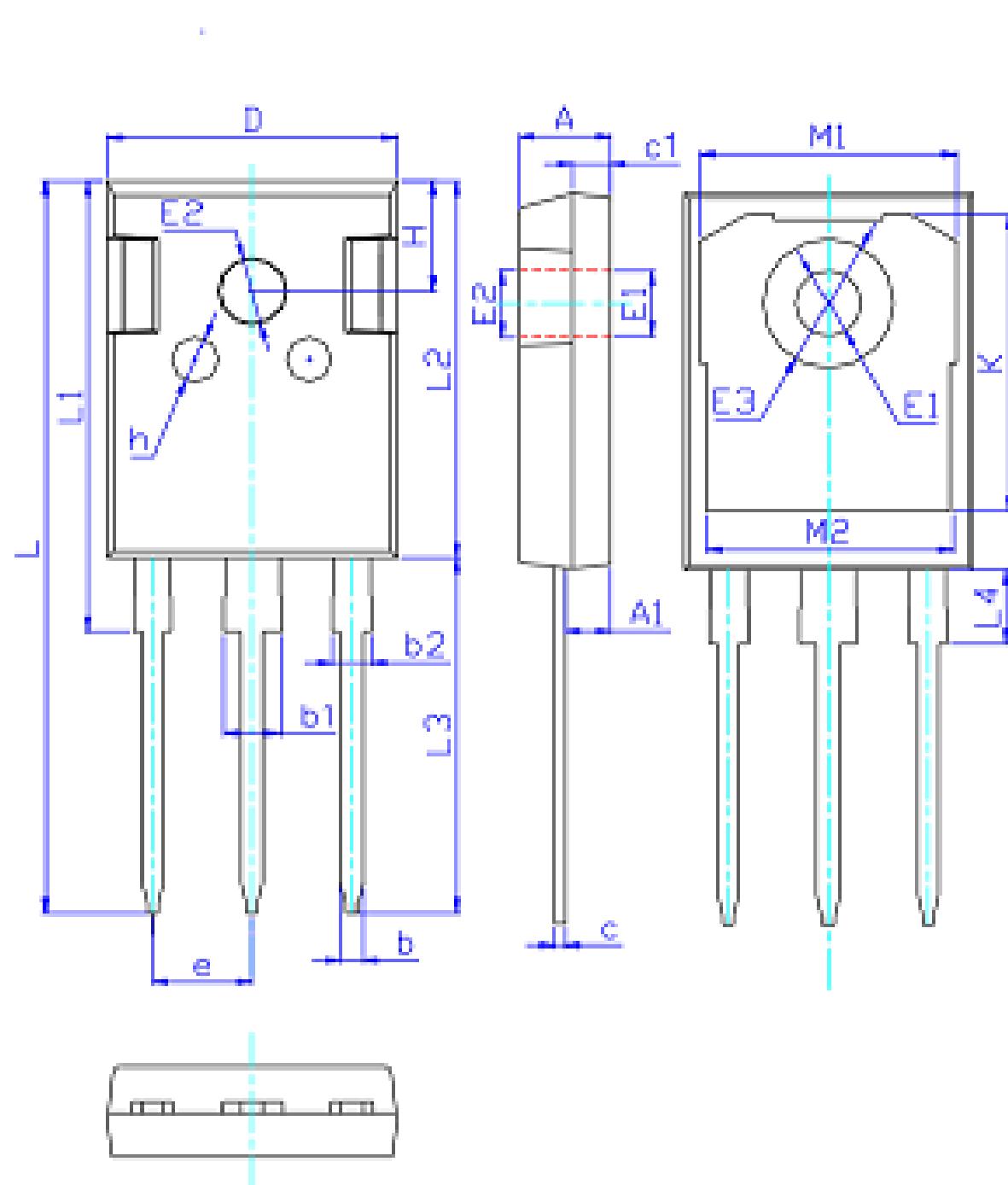
印记 Marking:



外形尺寸:

Package Dimension:

TO-247



标注	尺寸(mm)
A	5.00±0.05
A1	2.41±0.05
b	1.2±0.05
b1	3.05±0.05
b2	2.05±0.05
c	0.60±0.05
c1	2.00±0.05
D	15.80±0.10
E1	3.60±0.05
E2	3.70±0.05
E3	7.19±0.05
L	40.92±0.10
L1	24.95±0.10
L2	21.00±0.10
L3	19.92±0.10
L4	4.10±0.05
e	5.44±0.05
H	6.15±0.05
h	2.50±0.05
K	16.45±0.10
M1	14.00±0.10
M2	13.30±0.10