



N 沟道增强型场效应晶体管 N-CHANNEL MOSFET FHP8N60B/FHF8N60B

主要参数 MAIN CHARACTERISTICS

ID	8A
VDSS	600V
Rdson-typ (@Vgs=10V)	1.0 Ω
Qg-typ	26nC

用途 APPLICATIONS

高频开关电源	High efficiency switch mode power supplies
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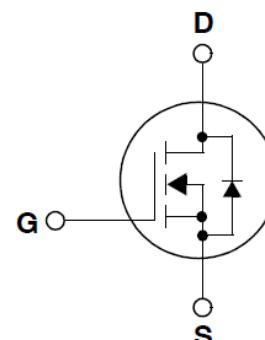
产品特性 FEATURES

低栅极电荷	Low gate charge
低 Crss (典型值 3.1pF)	Low Crss (typical 3.1pF)
开关速度快	Fast switching
100% 经过雪崩测试	100% avalanche tested
100% 经过热阻测试	100% DVDS tested
100% 经过 RG 测试	100% Rg tested
高抗 dv/dt 能力	Improved dv/dt capability
RoHS 产品	RoHS product

封装形式 Package



等效电路 Equivalent Circuit



绝对最大额定值 ABSOLUTE RATINGS (Tc=25°C)

项目 Parameter	符号 Symbol	数值 Value		单位 Unit
		FHP8N60B	FHF8N60B	
最高漏极—源极直流电压 Drain-Source Voltage	VDS	600		V
连续漏极电流* Drain Current -continuous *	Id (Tc=25°C)	8		A
连续漏极电流* Drain Current -continuous *	Id (Tc=100°C)	5.1		A
最大脉冲漏极电流 (注 1) Drain Current – pulse (note 1)	Idm	32		A
最高栅源电压 Gate-Source Voltage	VGS	±30		V
单脉冲雪崩能量 (注 2) Single Pulsed Avalanche Energy (note 2)	EAS	180		mJ
雪崩电流 (注 1) Avalanche Current (note 1)	IAR	6		A
重复雪崩能量 (注 1) Repetitive Avalanche Current (note 1)	EAR	108		mJ
二极管反向恢复最大电压变化速率 (注 3) Peak Diode Recovery dv/dt (note 3)	dv/dt	5.0		V/ns
耗散功率 Power Dissipation	PD (TC=25°C)	198	50	W
	-Derate above 25°C	1.6	0.4	W/°C
最高结温及存储温度 Operating and Storage Temperature Range	TJ, Tstg	150, -55 to 150		°C
引线最高焊接温度 Maximum Lead Temperature for Soldering Purposes	T _L	300		°C

*漏极电流由最高结温限制

*Drain current limited by maximum junction temperature

电特性 ELECTRICAL CHARACTERISTICS

项目 Parameter	符号 Symbol	测试条件 Tests conditions	最小 Min	典型 Typ	最大 Max	单位 Units	
关态特性 Off -Characteristics							
漏一源击穿电压 Drain-Source Voltage	BV_{DSS}	$I_D=250\mu A, V_{GS}=0V$	600	-	-	V	
击穿电压温度特性 Breakdown Voltage Temperature Coefficient	$\Delta BV_{DSS}/\Delta T_J$	$I_D=250\mu A$, referenced to 25°C	-	0.6	-	V/°C	
零栅压下漏极漏电流 Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=600V, V_{GS}=0V, T_c=25^{\circ}C$	-	-	10	μA	
		$V_{DS}=480V, T_c=125^{\circ}C$	-	-	100	μA	
栅极体漏电流 Gate-body leakage current	$I_{GSS} (F/R)$	$V_{DS}=0V, V_{GS}=\pm 30V$	-	-	± 100	nA	
通态特性 On-Characteristics							
阈值电压 Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D=250\mu A$	2.0	-	4.0	V	
静态导通电阻 Static Drain-Source On-Resistance	$R_{DS(ON)}$	$V_{GS} = 10V, I_D=4A$	-	1.0	1.15	Ω	
正向跨导 Forward Transconductance	g_{fs}	$V_{DS} = 20V, I_D=4A$ (note 4)	-	12	-	S	
动态特性 Dynamic Characteristics							
栅电阻 Gate Resistance	R_g	$f=1.0MHz, V_{DS} OPEN$	-	2.1	-	Ω	
输入电容 Input capacitance	C_{iss}	$V_{DS}=25V, V_{GS}=0V, f=1.0MHz$	-	1130	-	pF	
输出电容 Output capacitance	C_{oss}		-	95	-		
反向传输电容 Reverse transfer capacitance	C_{rss}		-	3.1	-		
开关特性 Switching Characteristics							
延迟时间 Turn-On delay time	$t_{d(on)}$	$V_{DS}=300V, I_D=8A, R_G=5\Omega, V_{GS}=10V$ (note 4, 5)	-	33	-	ns	
上升时间 Turn-On rise time	t_r		-	31	-	ns	
延迟时间 Turn-Off delay time	$t_{d(off)}$		-	81	-	ns	
下降时间 Turn-Off Fall time	t_f		-	36	-	ns	
栅极电荷总量 Total Gate Charge	Q_g	$V_{DS}=480V, I_D=8A, V_{GS}=10V$ (note 4, 5)	-	26	-	nC	
栅一源电荷 Gate-Source charge	Q_{gs}		-	8	-	nC	
栅一漏电荷 Gate-Drain charge	Q_{gd}		-	15	-	nC	
漏一源二极管特性及最大额定值 Drain-Source Diode Characteristics and Maximum Ratings							
正向最大连续电流 Maximum Continuous Drain-Source Diode Forward Current	I_s		-	-	8	A	
正向最大脉冲电流 Maximum Pulsed Drain-Source Diode Forward Current	I_{SM}		-	-	32	A	
正向压降 Drain-Source Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_s=8A$	-	-	1.2	V	
反向恢复时间 Reverse recovery time	t_{rr}	$V_{GS}=0V, I_s=8A, dI/dt=100A/\mu s$ (note 4)	-	327	-	ns	
反向恢复电荷 Reverse recovery charge	Q_{rr}		-	1837	-	nC	

热特性 THERMAL CHARACTERISTIC

项目 Parameter	符号 Symbol	FHP8N60B	FHF8N60B	单位 Unit
结到管壳的热阻 Thermal Resistance, Junction to Case	R _{th(j-c)}	0.63	2.5	°C/W
结到环境的热阻 Thermal Resistance, Junction to Ambient	R _{th(j-A)}	62.5	62.5	°C/W

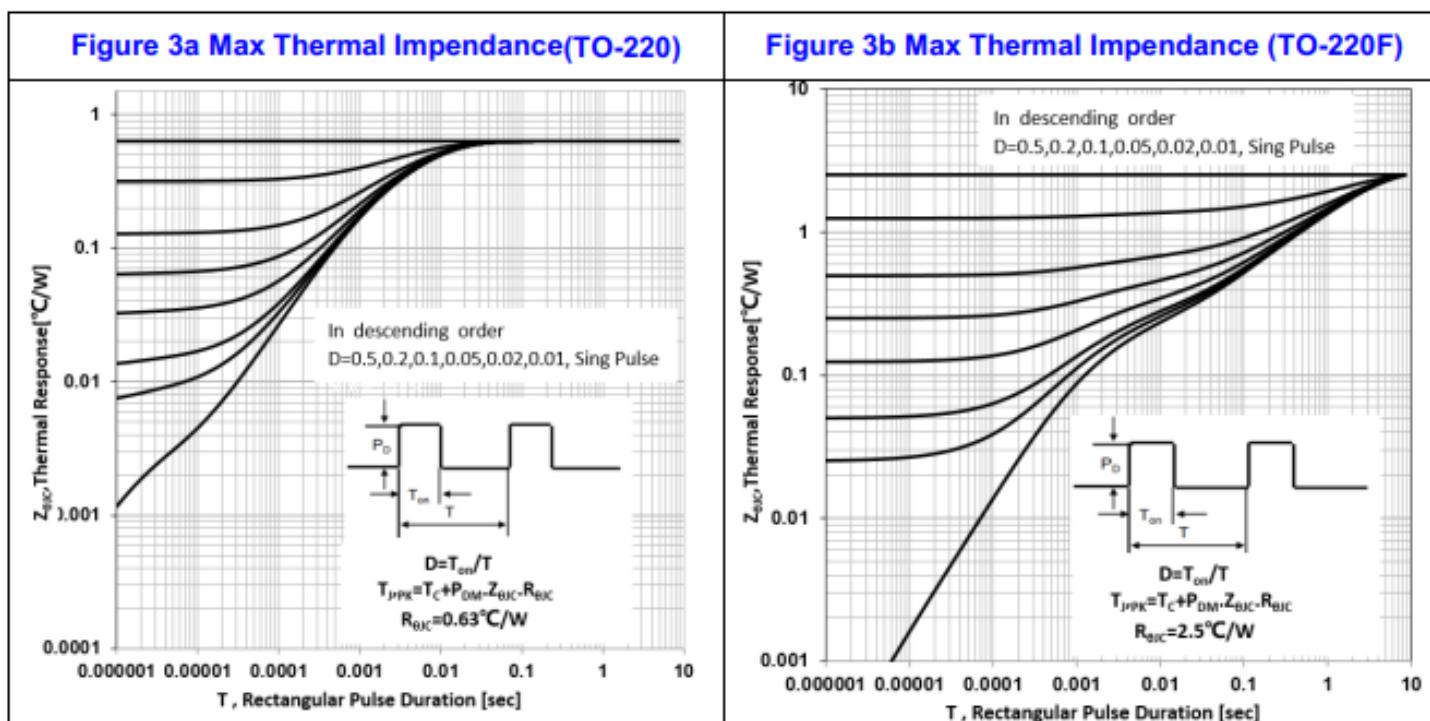
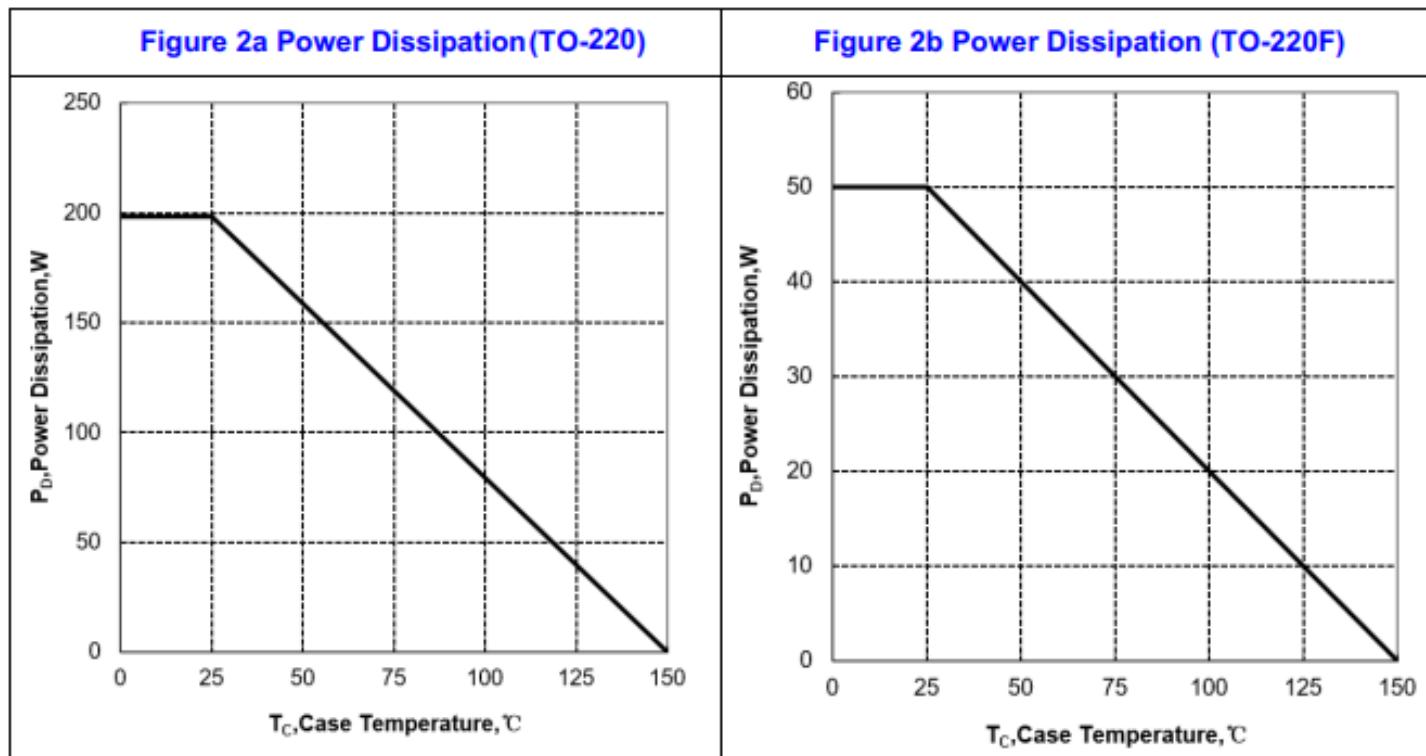
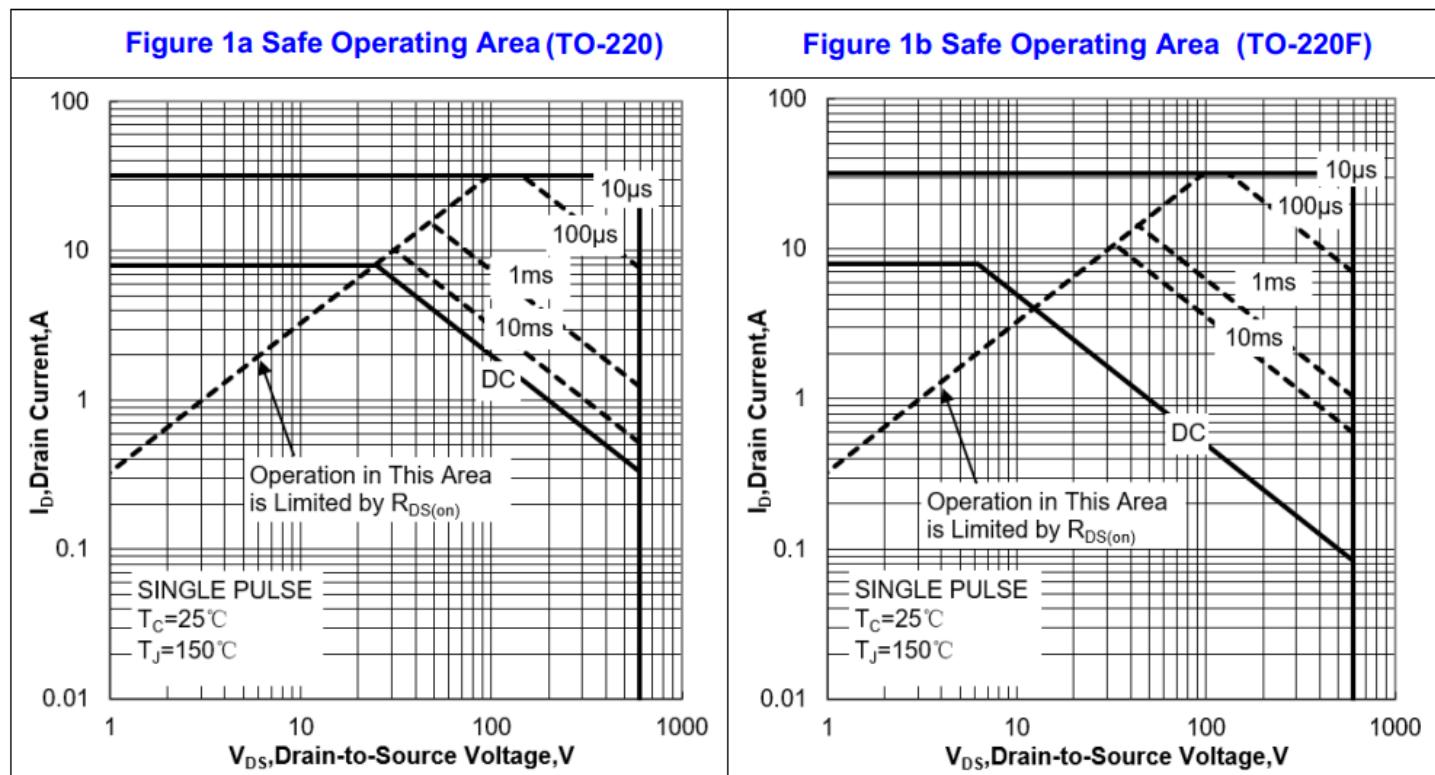
注释：

- 1: 脉冲宽度由最高结温限制
- 2: L=10mH, IAS=6A, VDS=50V, RG=25 Ω, 起始结温 TJ=25°C
- 3: ISD ≤8A, di/dt ≤100A/μs, VDD≤BV_{DSS}, 起始结温 TJ=25°C
- 4: 脉冲测试：脉冲宽度 ≤300μs, 占空比≤2%
- 5: 基本与工作温度无关

Notes:

- 1: Pulse width limited by maximum junction temperature
- 2: L=10mH, IAS=6A, VDS=50V, RG=25 Ω ,Start TJ=25°C;
- 3: ISD ≤8A, di/dt ≤100A/μs, VDD≤BV_{DSS}, Starting TJ=25°C
- 4: Pulse Test: Pulse Width ≤300μs, Duty Cycle≤2%
- 5: Essentially independent of operating temperature

特性曲线 (ELECTRICAL CHARACTERISTICS (curves))



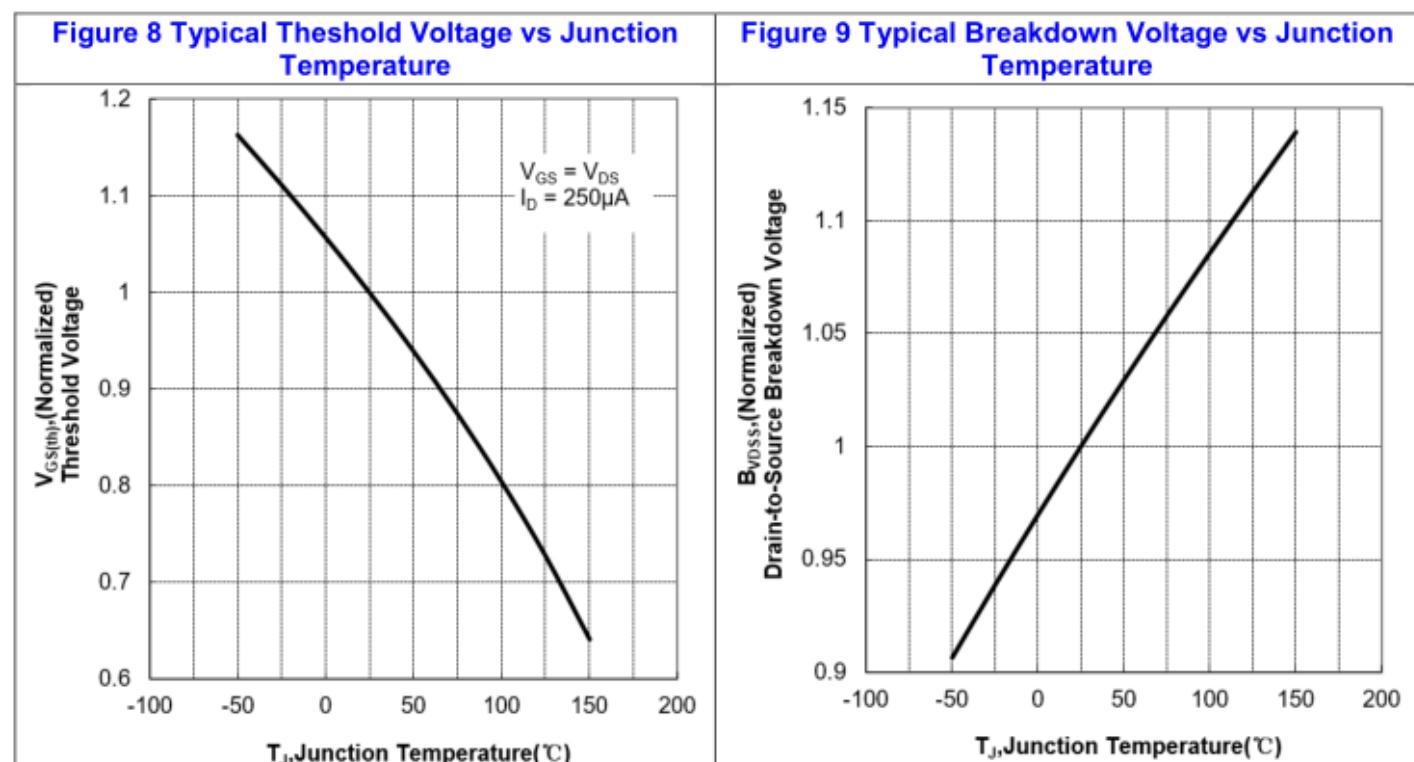
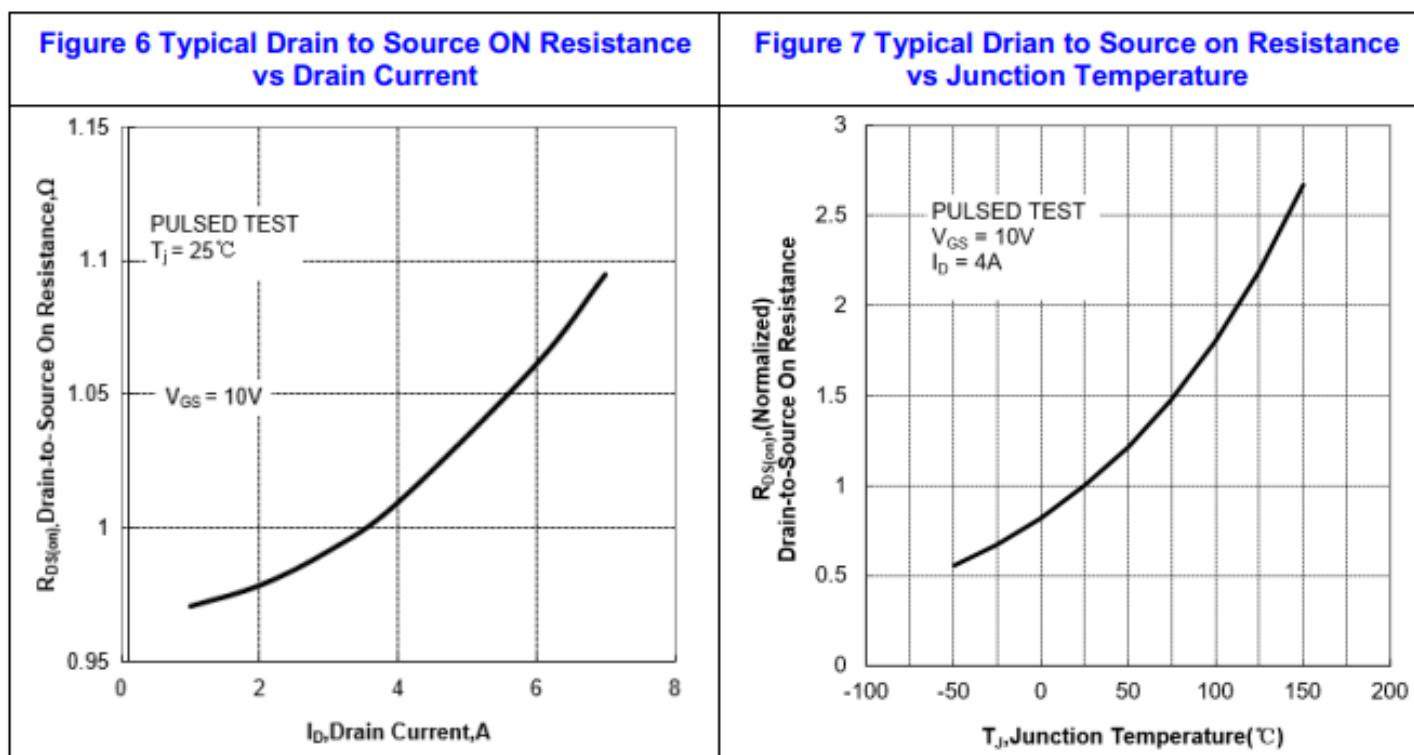
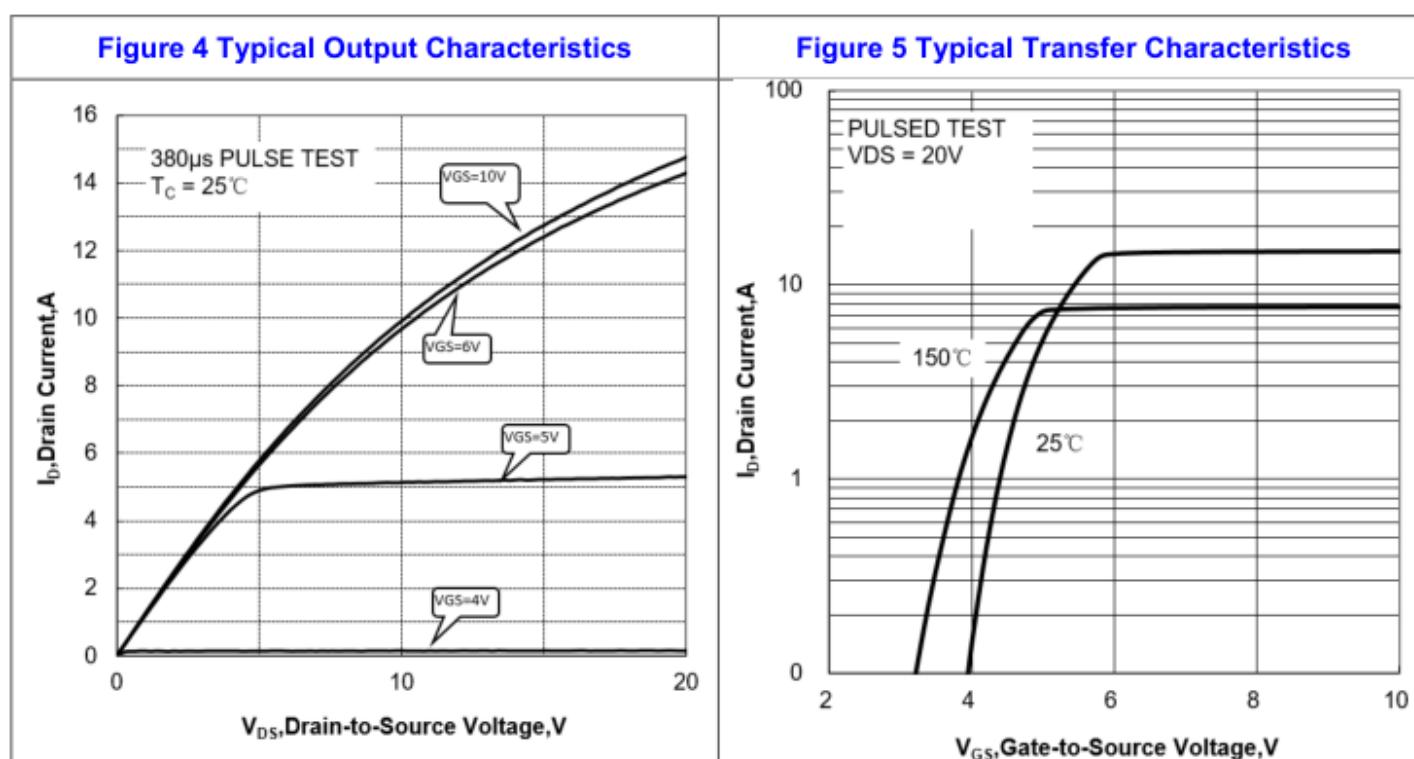


Figure 10 Typical Capacitance vs Drain to Source Voltage

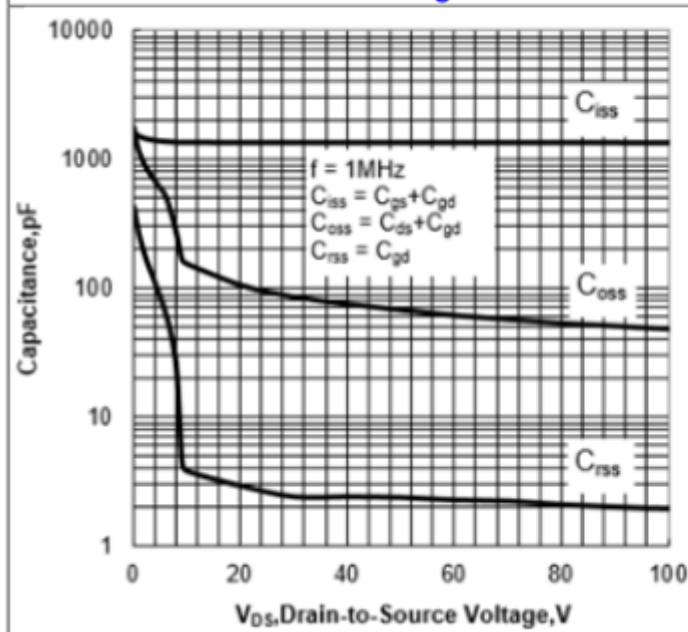
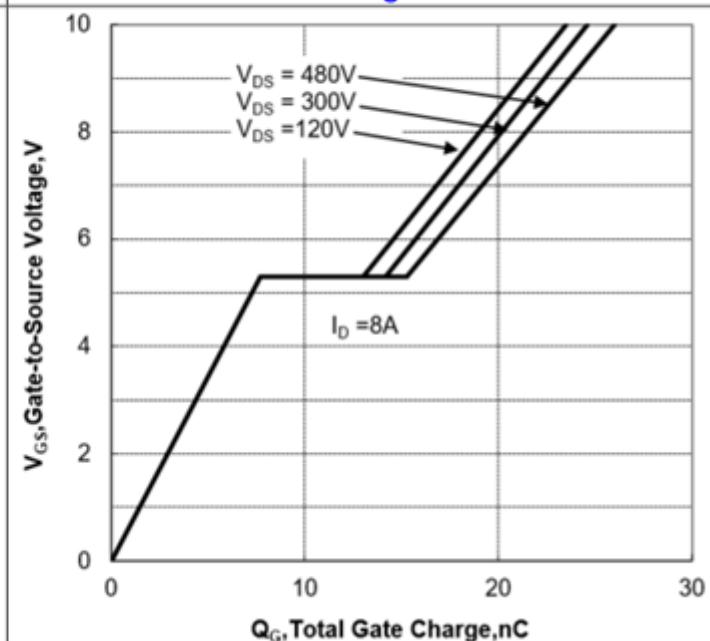


Figure 11 Typical Gate Charge vs Gate to Source Voltage



Test Circuit and Waveform

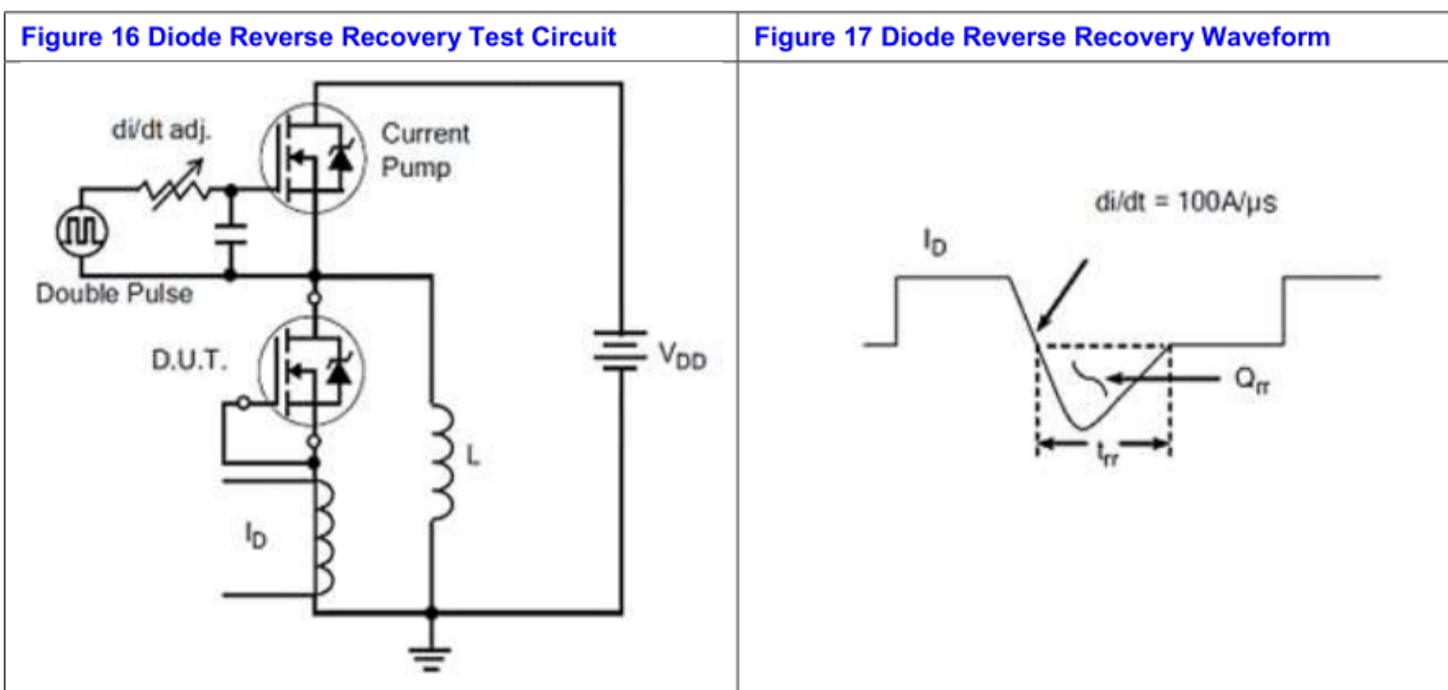
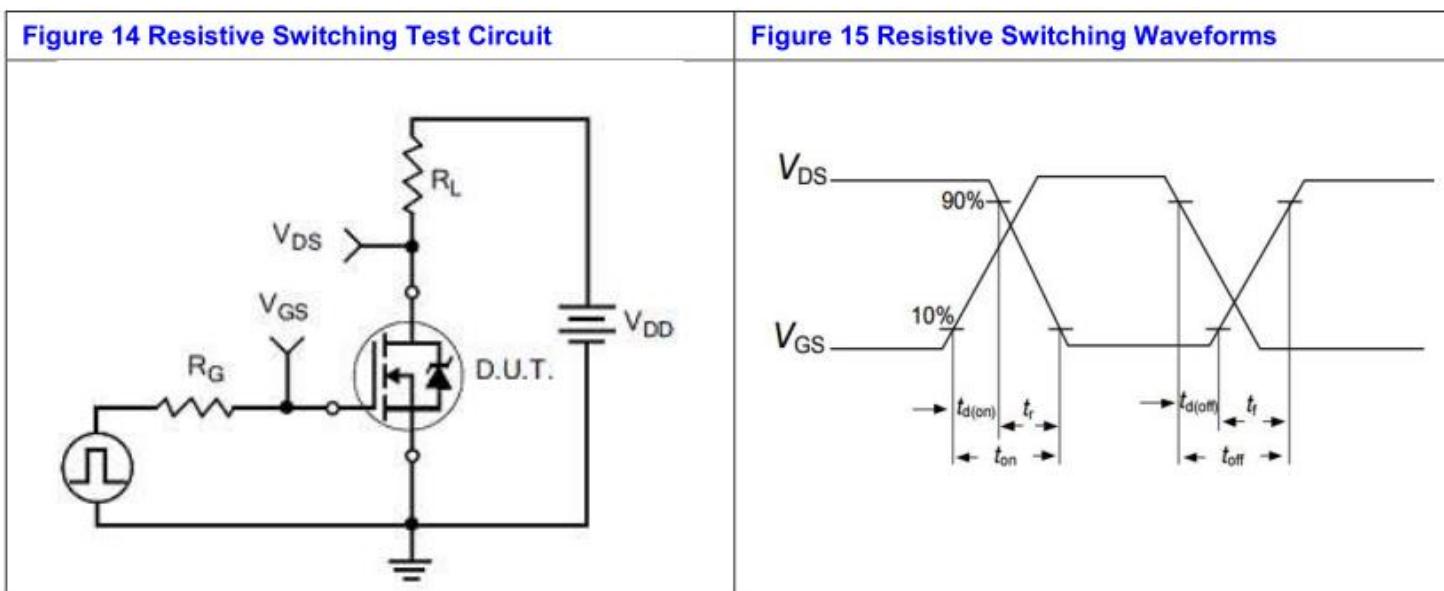
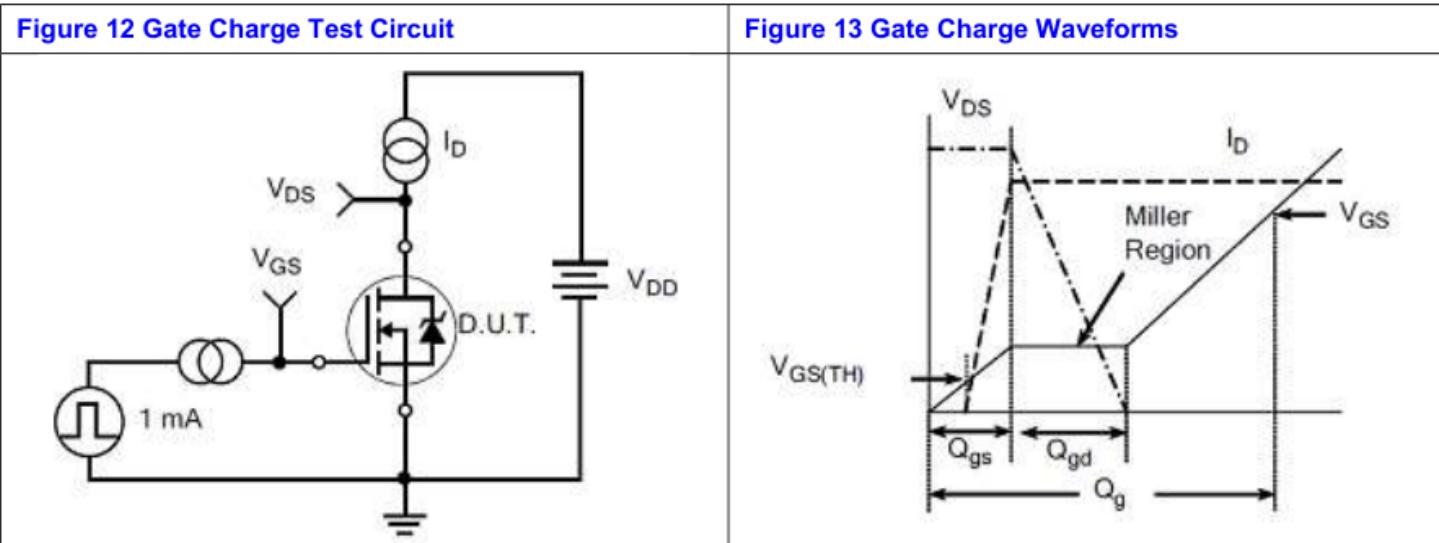


Figure 18 Unclamped Inductive Switching Test Circuit

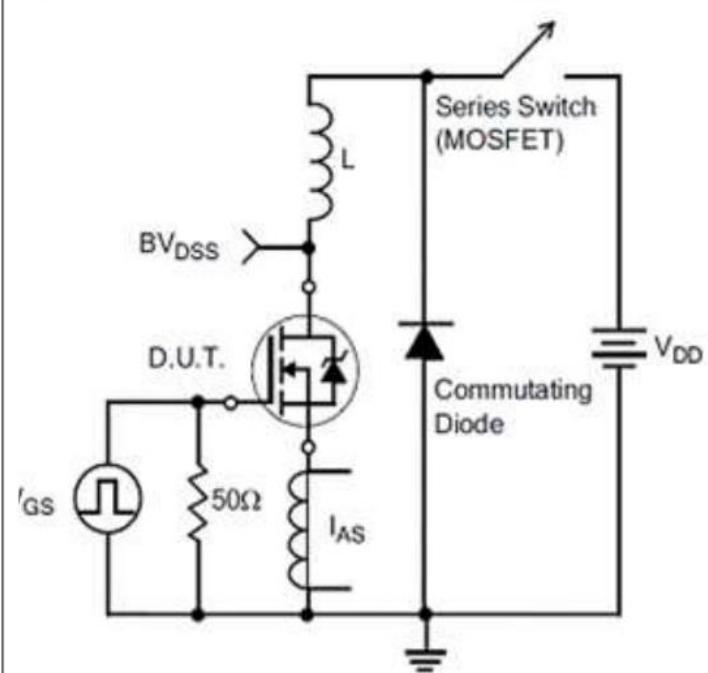
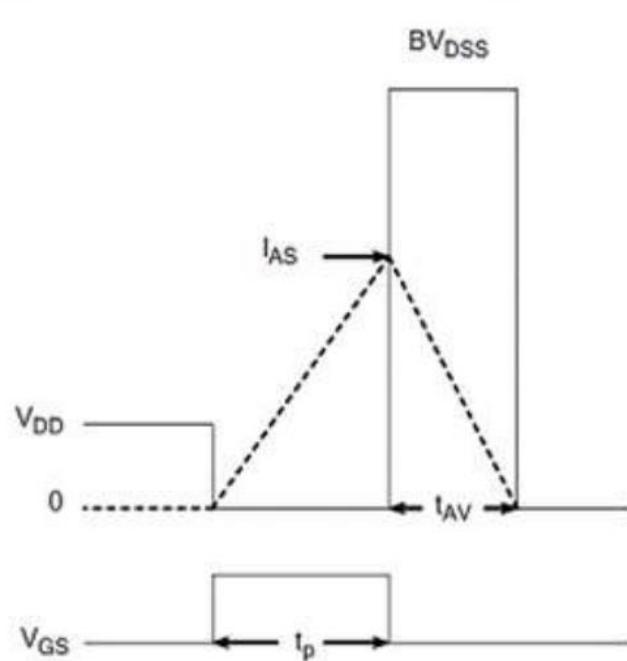
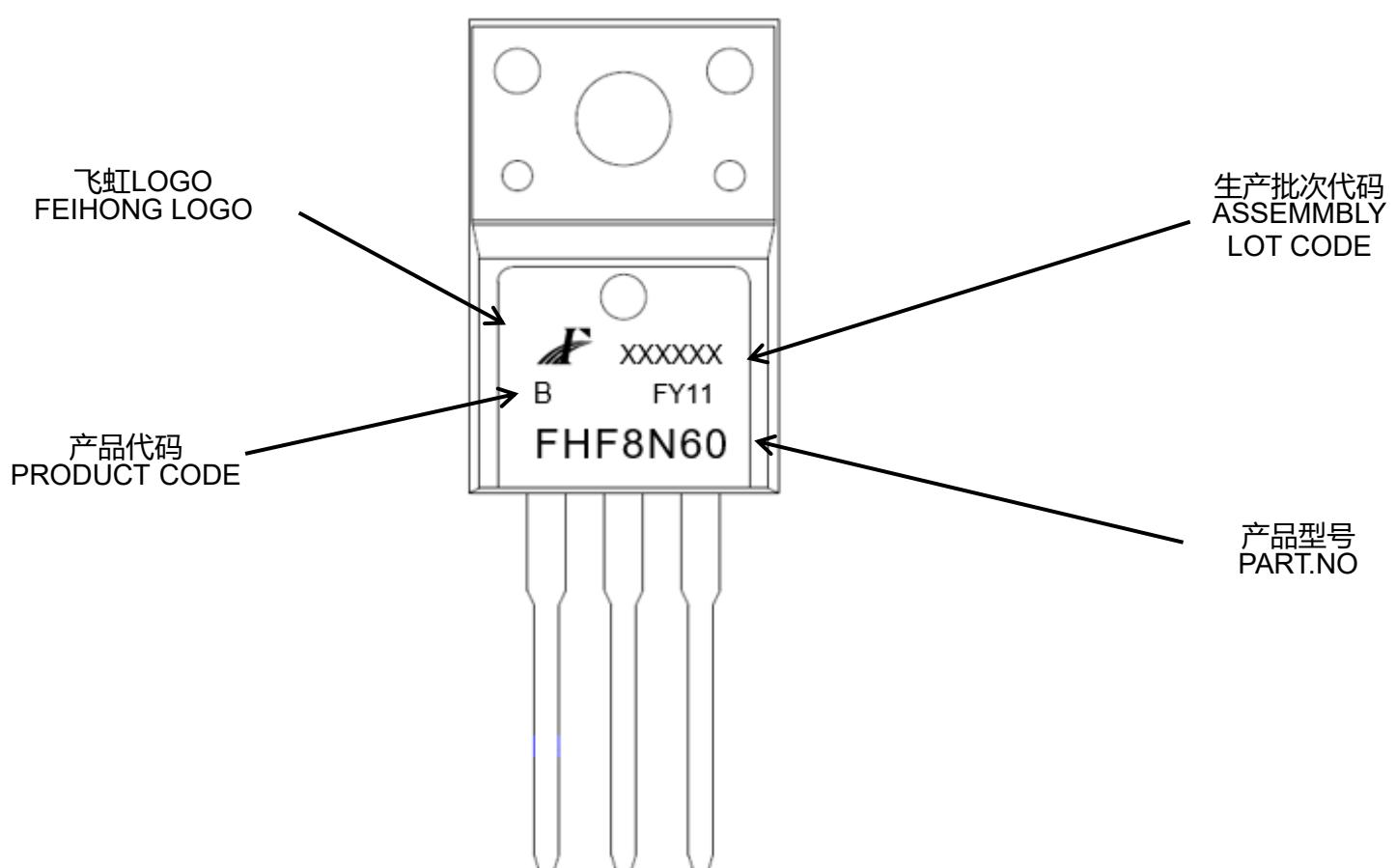
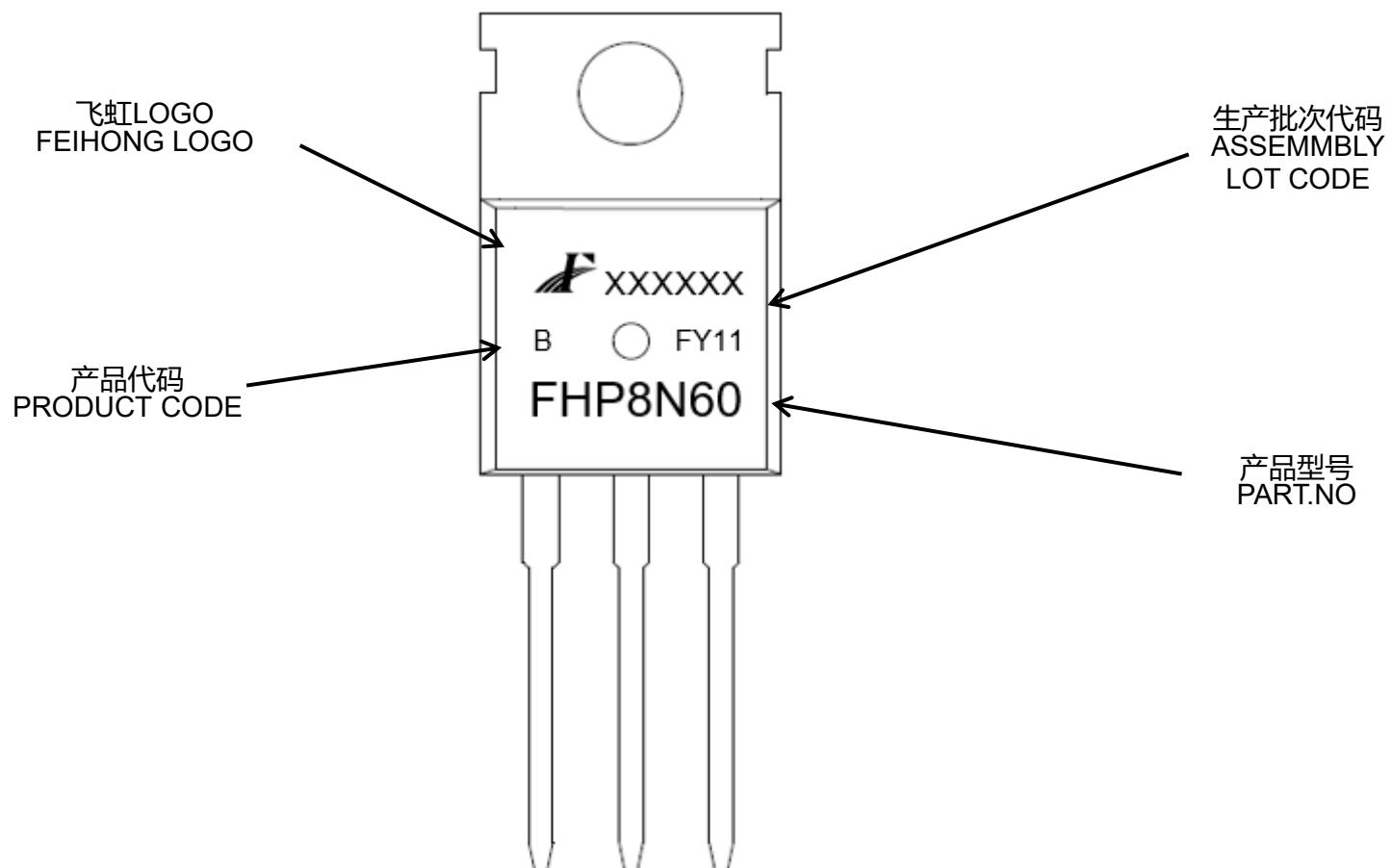


Figure 19 Unclamped Inductive Switching Waveform



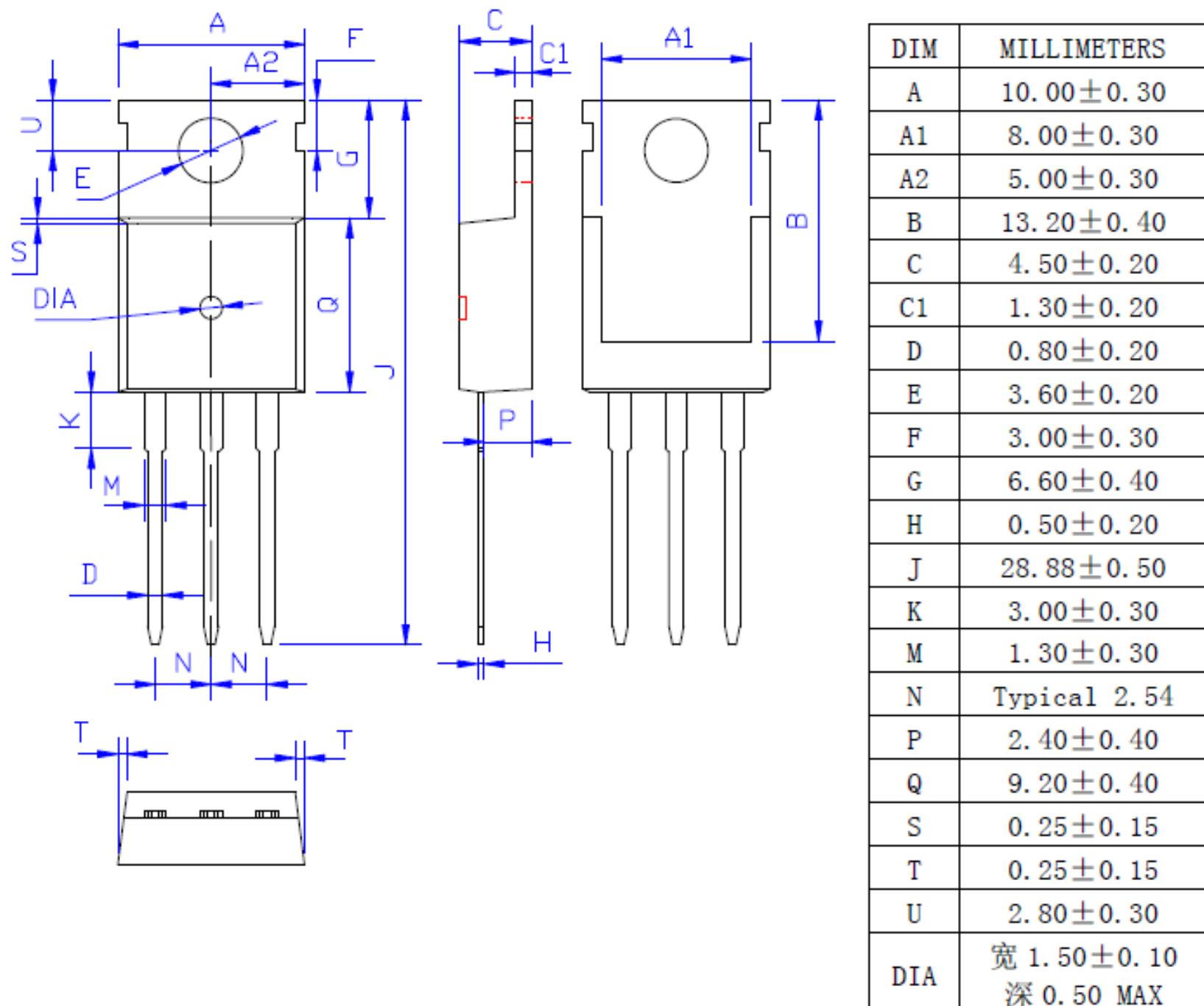
印记 Marking:



外形尺寸:

Package Dimension:

TO-220

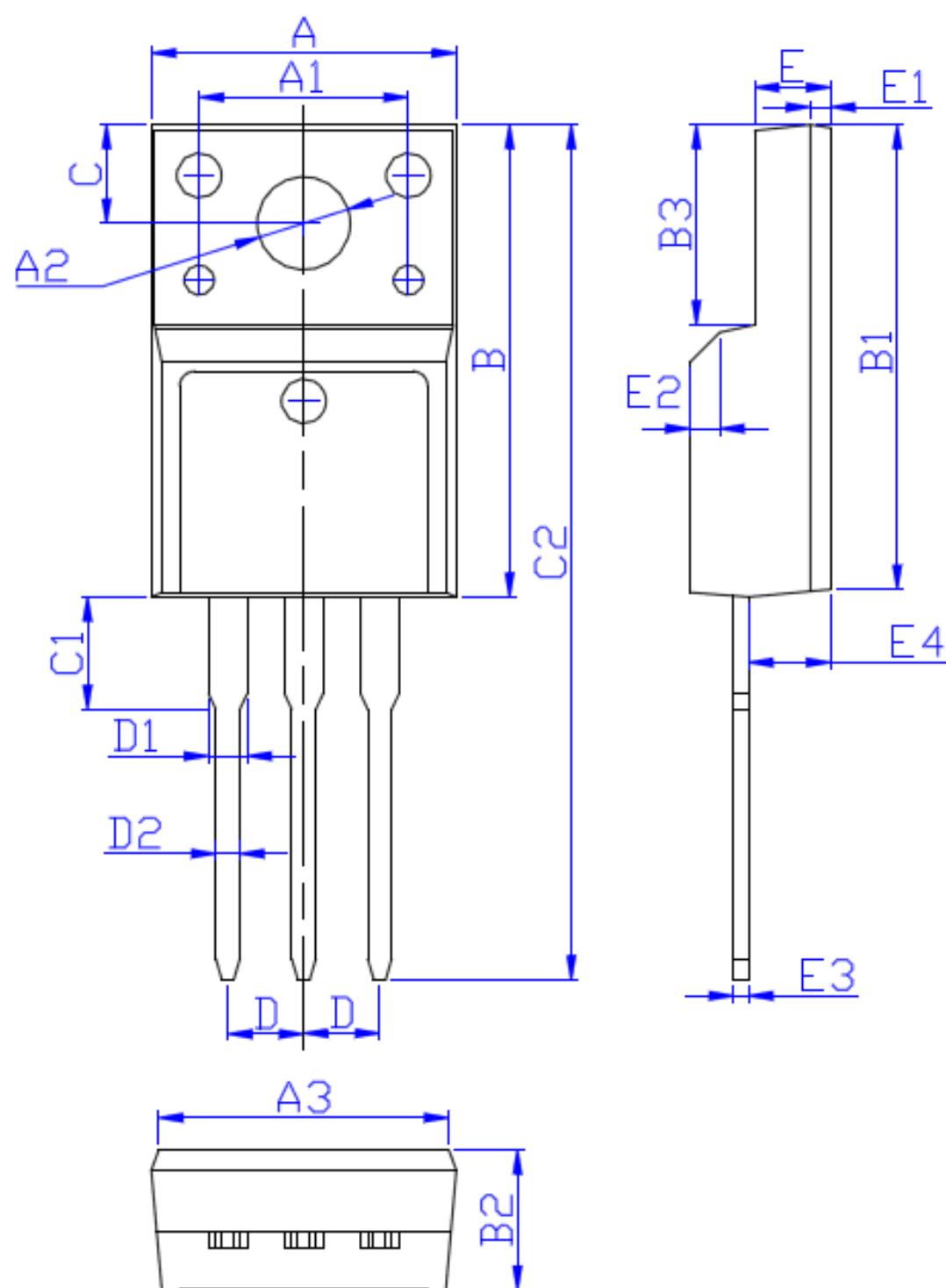


(Unit: mm)

外形尺寸:

Package Dimension:

TO-220F



DIM	MILLIMETERS
A	10.16±0.30
A1	7.00±0.20
A2	3.12±0.20
A3	9.70±0.30
B	15.90±0.50
B1	15.60±0.50
B2	4.70±0.30
B3	6.70±0.30
C	3.30±0.25
C1	3.25±0.30
C2	28.70±0.50
D	Typical 2.54
D1	1.47 (MAX)
D2	0.80±0.20
E	2.55±0.25
E1	0.70±0.25
E2	1.0×45°
E3	0.50±0.20
E4	2.75±0.30

(Unit:mm)