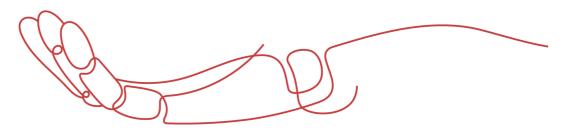


## **PRODUCT DATA SHEET**



To learn more about JGSEMI, please visit our website at



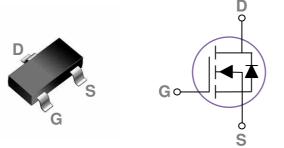
Please note: Please check the JINGAO Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.jg-semi.cn. Please email any questions regarding the system integration to JINGAO\_questions@jgsemi.com.

## JG Techology

#### **General Description**

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

#### **SOT23-3Pin Configuration**



# BVDSS RDSON ID 60V 70mΩ 4A

#### **Features**

- 60V,4A,  $RDS(ON) = 70m\Omega@VGS = 10V$
- Improved dv/dt capability
- Fast switching
- Green Device Available

#### **Applications**

- Motor Drive
- Power Tools
- LED Lighting

#### Absolute Maximum Ratings Tc=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
Vds	Drain-Source Voltage	60	V
Vgs	Gate-Source Voltage	±20	V
lo	Drain Current – Continuous (T <sub>C</sub> =25°C)	4.0	А
	Drain Current – Continuous (Tc=100°C)	2	А
Ідм	Drain Current – Pulsed <sup>1</sup>	12.8	А
Po	Power Dissipation (Tc=25°C)	1.56	W
	Power Dissipation – Derate above 25℃	0.012	W/°C
T <sub>STG</sub>	Storage Temperature Range	-50 to 150	°C
TJ	Operating Junction Temperature Range	-50 to 125	°C

#### **Thermal Characteristics**

Symbol	Symbol Parameter		Max.	Unit
R <sub>BJA</sub> Thermal Resistance Junction to ambient			80	°C/W

Ver.1.0

## 2SK3065



#### **Electrical Characteristics** (T<sub>J</sub>=25 °C, unless otherwise noted)

#### **Off Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	60			V
$\triangle BV_{DSS} / \triangle T_J$	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25℃ , I <sub>D</sub> =1mA		0.05		V/°C
IDSS	Drain-Source Leakage Current	V <sub>DS</sub> =60V,V <sub>GS</sub> =0V,TJ=25°C			1	uA
		V <sub>DS</sub> =48V , V <sub>GS</sub> =0V , TJ=125℃			10	uA
Igss	Gate-Source Leakage Current	$V_{GS}=\pm20V$ , $V_{DS}=0V$			±100	nA

#### **On Characteristics**

Rds(on)	Static Drain-Source On-Resistance	V <sub>GS</sub> =10V , I <sub>D</sub> =4A		70	90	mΩ
		$V_{GS}$ =4.5V , $I_{D}$ =1.5A		80	100	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	-V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	1.0	1.6	2.5	V
$ riangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient	VGS=VDS, $ID=2500A$		-5		mV/°C
gfs	Forward Transconductance	V <sub>DS</sub> =10V , I <sub>D</sub> =4A		7		S

#### **Dynamic and switching Characteristics**

Qg	Total Gate Charge <sup>2,3</sup>		 9.3	
Qgs	Gate-Source Charge <sup>2,3</sup>	$V_{DS}=48V$ , $V_{GS}=10V$ , $I_{D}=4A$	 2.1	 nC
$Q_{gd}$	Gate-Drain Charge <sup>2,3</sup>		 1.8	
Td(on)	Turn-On Delay Time <sup>2 , 3</sup>		 2.9	
Tr	$\label{eq:relation} \mbox{Rise Time}^{2,3} \qquad \qquad \mbox{V}_{\mbox{DD}}\mbox{=}30\mbox{V},\mbox{V}_{\mbox{GS}}\mbox{=}10\mbox{V},\mbox{R}_{\mbox{GS}}\mbox{=}3.3\Omega$		 9.5	 20
T <sub>d(off)</sub>	Turn-Off Delay Time <sup>2,3</sup>	I <sub>D</sub> =1A	 18.4	 ns
Tf	Fall Time <sup>2,3</sup>		 5.3	
Ciss	Input Capacitance		 500	
Coss	Output Capacitance	$V_{DS}$ =15V , $V_{GS}$ =0V , F=1MHz	 45	 рF
Crss	Reverse Transfer Capacitance		 16	
Rg	Gate resistance	$V_{GS}$ =0V, $V_{DS}$ =0V, F=1MHz	 2	 Ω

#### **Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
ls	Continuous Source Current	-V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			4.0	А
lsм	Pulsed Source Current	VG=VD=OV, FOICe Current			6.0	А
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V,Is=1A,TJ=25℃			1.2	V
t <sub>rr</sub>	Reverse Recovery Time <sup>2</sup>	Vgs=30V,Is=1A , dI/dt=100A/µs		23.2		ns
Qrr	Reverse Recovery Charge <sup>2</sup>	TJ=25℃		14.3		nC

Note :

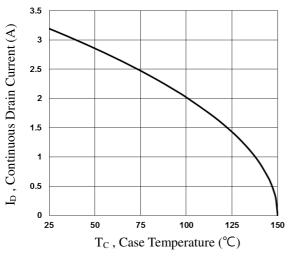
1. Repetitive Rating : Pulsed width limited by maximum junction temperature.

2. The data tested by pulsed , pulse width  $\leq$  300us , duty cycle  $\leq$  2%.

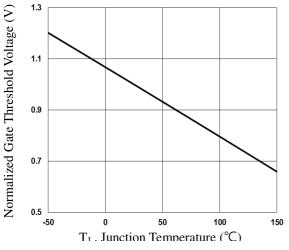
3. Essentially independent of operating temperature.



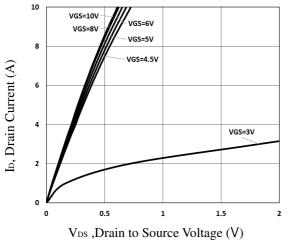
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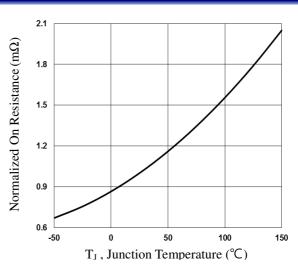














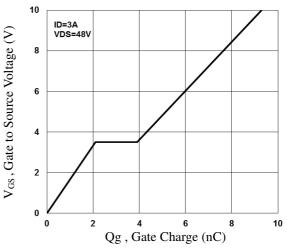
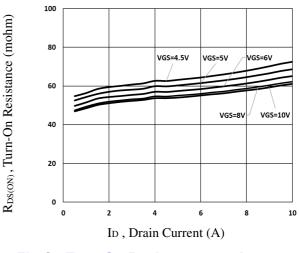


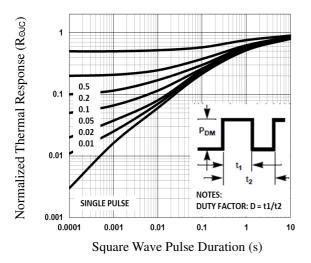
Fig.4 Gate Charge Waveform







## 2SK3065



#### Fig.7 Normalized Transient Response

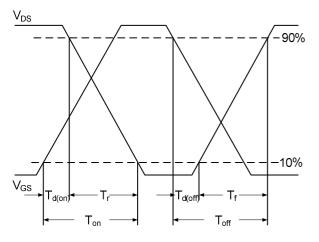
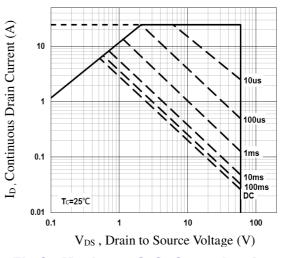


Fig.9 Switching Time Waveform

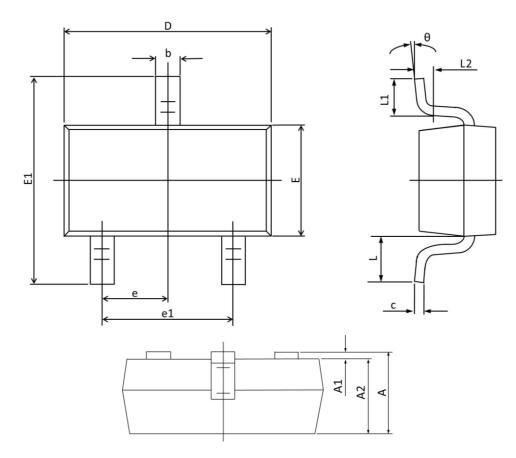






## 2SK3065

## SOT23-3L PACKAGE INFORMATION



Symbol	Dimensions	In Millimeters	Dimensior	ns In Inches
	MAX	MIN	MAX	MIN
Α	1.150	0.900	0.045	0.035
A1	0.100	0.000	0.004	0.000
A2	1.050	0.900	0.041	0.035
b	0.500	0.300	0.020	0.012
С	0.150	0.080	0.006	0.003
D	3.000	2.800	0.118	0.110
E	1.700	1.500	0.067	0.059
E1	2.550	2.250	0.100	0.089
е	0.95 TYP.		0.037 TYP.	
e1	2.000	1.800	0.079	0.071
L	0.5	5 REF.	0.022 REF.	
L1	0.500	0.300	0.020	0.012
L2	0.2	5 TYP.	0.0	1 TYP.
θ	<b>8</b> °	<b>0°</b>	<b>8</b> °	<b>0</b> °





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