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0.8A, Gate Driver Photo Coupler

Description

The TDL314 series Photocoupler is ideally suited for driving power IGBTs and MOSFETs used in motor control inverter applications

and inverters in power supply system. It contains an AlGaAs LED optically coupled to an integrated circuit with a power output stage.

The Photocoupler operational parameters are guaranteed over the temperature range from -40° C ~ +110°C.

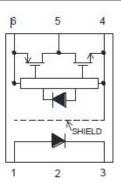
Features

- 1.5 A maximum peak output current
- 0.8 A minimum peak output current
- Rail-to-rail output voltage
- 110 ns maximum propagation delay
- Under Voltage Lock-Out protection (UVLO)
 with hysteresis
- Wide operating range: 10 to 30 Volts (V_{CC})
- Guaranteed performance over temperature
 -40°C ~ +110°C.

Applications

- Isolated IGBT/Power MOSFET gate drive
- Industrial Inverter
- AC brushless and DC motor drives
- Induction Heating

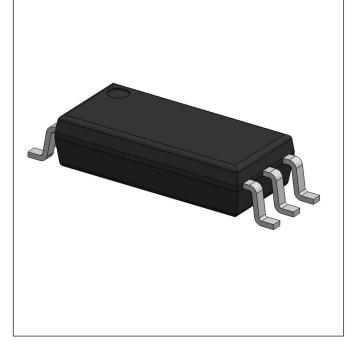
SCHEMATIC



PIN DEFINITION

1.Anode	6.V _{cc}
2.None	5.VO
3.Cathode	4.V _{SS}

PACKAGE





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TRUTH TABLE								
LED	V _{CC} -V _{SS} (Turn-ON, +ve going)	V_{CC} - V_{SS} (Turn-OFF, -ve going)	Vo					
Off	0V to 30V	0V to 30V	Low					
On	0V to 6.9V	0V to 5.9V	Low					
On	6.9V to 8.7V	5.9V to 7.5V	Transition					
On	8.7V to 30V	7.5V to 30V	High					

Note: A $0.1\mu F$ bypass capacitor must be connected between Pin 4 and 6.

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	Min	Max	UNIT	Note				
Storage Temperature	Tstg	-55	125	°C	-				
Operating Temperature	Topr	-40	110	°C	-				
Output IC Junction Temperature	TJ	-	125	°C	-				
Total Output Supply Voltage	(Vcc –Vss)	0	35	V	-				
Average Forward Input Current	lf	-	20	mA	-				
Reverse Input Voltage	VR	-	5	V	-				
"High" Peak Output Current	IOH(PEAK)	0.8	1.5	А	1				
"Low" Peak Output Current	IOL(PEAK)	0.8	1.5	А	1				
Output Voltage	VO(PEAK)	-0.5	Vcc	V	-				
Power Dissipation	Pi	-	45	mW	-				
Output IC Power Dissipation	Po	-	250	mW	-				
Lead Solder Temperature	Tsol	-	260	°C	-				

Note: Ambient temperature = 25°C, unless otherwise specified. Stresses exceeding the absolute maximum ratings can cause permanent damage to the device. Exposure to absolute maximum ratings for long periods of time can adversely affect reliability.

Note 1: Exponential waveform. Pulse width ≤ 10 µs, f ≤ 15 kH**RECOMMENDED OPERATION**

CONDITIONS				
PARAMETER	SYMBOL	MIN.	MAX.	UNIT
Operating Temperature	TA	-40	110	°C
Supply Voltage	V _{CC}	10	30	V
Input Current (ON)	I _{F(ON)}	7	16	mA
Input Voltage (OFF)	V _{F(OFF)}	-3.0	0.8	V

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EL	ECTRI	CAL (OPTICA		RAC	TERISTICS	
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITION	NOTE
		INF	PUT CHAR	ACTERIS	STICS		
Forward Voltage	VF	-	1.38	1.8	V	I _F = 10 mA	-
Reverse Current	I _R	-	-	10	μA	V _R =5V	-
Input Threshold Current	IFLH		0.6		V ₀ > 5V, I ₀ = 0A		
(Low to High)	IFLH	-	0.0	2	mA	$v_0 > 5v, v_0 = 0A$	-
Input Threshold Voltage	Vfhl	0.8		_	- V	Vcc = 30 V, Vo < 5V	_
(High to Low)	VFHL	0.0	-	-	v	VCC = 30 V, VC < 5V	-
Input Capacitance	CIN	-	60	-	pF	V⊧ = 0, f = 1MHz	-
		OUT	PUT CHAI	RACTER	ISTICS		
High Level Supply Current	I _{ССН}	_	1.50	3	mA	I_F = 10 mA, V_{CC} = 30 V,	
Thigh Level Supply Current		-				V _o = Open, Rg = 30Ω, Cg = 3 nF	
Low Level Supply Current	l	_	1.50	3	mA	$I_F = 0 \text{ mA}, V_{CC} = 30 \text{ V},$	
Low Level Supply Current	ICCL	-	1.50	5	ША	V ₀ = Open, Rg = 30Ω, Cg = 3 nF	
High Level Output Voltage	V _{OH}	29.4	29.69	-	V	I _F = 10 mA, I _O = -100 mA	2,3
Low Level Output Voltage	Vol	-	0.17	0.34	V	I _F = 0 mA, I _O = 100 mA	
Lligh Lovel Output Current		0.0				I _F = 10 mA, V _{CC} = 30V	
High Level Output Current	I _{OH}	-0.8	-	-	A	$V_{\rm O}$ = $V_{\rm CC}$ - 4	1
Low Lovel Output Current		0.0			A	I _F = 0 mA, V _{CC} = 30V	
Low Level Output Current	I _{OL}	0.8	-	-	A	$V_{O} = V_{SS} + 4$	1
Under Voltage Lockout	VUVLO+	6.9	7.8	8.7	V	V _O > 5V, I _F = 10 mA	
Threshold	VUVLO-	5.9	6.9	7.5	V	V _O < 5V, I _F = 10 mA	

All Typical values at $T_A = 25^{\circ}$ C and $V_{CC} - V_{SS} = 30$ V, unless otherwise specified; all minimum and maximum specifications are at recommended operating condition.

Note 1: Maximum pulse width = 10 μ s.

Note 2: In this test VOH is measured with a dc load current. When driving capacitive loads, VOH will approach VCC as IOH approaches zero amps.

Note 3: Maximum pulse width = 1 ms.



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	SW	TCH	ING	SPE		CATION				
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITION	NOTE			
SWITCHING CHARACTERISTICS										
Propagation Delay Time	t _{РНL}	_	54	500	ns					
to Output Low Level	L PHL	_		500	115	_	-			
Propagation Delay Time	t	_	69	500	ns	Rg = 30Ω,				
to Output High Level	t _{PLH}	-	09	500	115	Cg = 3 nF,	-			
Pulse Width Distortion	PWD	_	22	200	ns	f = 10kHz,	_			
	1 110			200		Duty Cycle = 50%				
Propagation Delay Difference	PDD	-200	_	+200	ns	I _F = 10mA,	_			
Between Any Two Parts	(t _{PHL} - t _{PLH})			200		V _{CC} = 30V				
Rise Time	tr	-	13	-	ns		-			
Fall Time	t _f	-	13	-	ns		-			
Common Mode Transient						I _F =7 to 16mA V _{CC} = 30V,				
Immunity at Logic High	CМн	-20	-	-	kV/µs	T _A = 25 °C,	1,2			
						V _{CM} = 1kV				
Common Mode Transient						I⊧=0mA V _{CC} = 30V,				
Immunity at Logic Low	CM∟	20	-	-	kV/µs	T _A = 25 °C,	1,3			
						V _{CM} = 1kV				

All Typical values at $T_A = 25^{\circ}$ C and $V_{CC} - V_{SS} = 30$ V, unless otherwise specified; all minimum and maximum specifications are at recommended operating condition.

Note 1:Pin 2 needs to be connected to LED common.

Note 2: Common mode transient immunity in the high state is the maximum tolerable dVCM/dt of the

common mode pulse, VCM, to assure that the output will remain

in the high state (meaning VO > 10.0V).

Note 3: Common mode transient immunity in a low state is the maximum tolerable dVCM/dt of the common mode pulse, VCM, to assure that the output will remain in

a low state (meaning VO < 1.0V).



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ISOLATION CHARACTERISTIC									
Parameter	Symbo	Device	Min.	Тур.	Max.	Unit	Test Condition	Note	
Withstand Insulation	VISO		5000			V	RH ≤ 40%-60%,	10	
Test Voltage	VI50	-	5000	-	-	v	t = 1min, T _A = 25 °C	1,2	
Input-Output	D			10 ¹²		0		1	
Resistance	R _{I-0}	-	-	10'-	-	Ω	V _{I-O} = 500V DC		

All Typical values at $T_A = 25^{\circ}$ C and $V_{CC} - V_{SS} = 30$ V, unless otherwise specified; all minimum and maximum specifications are at recommended operating condition.

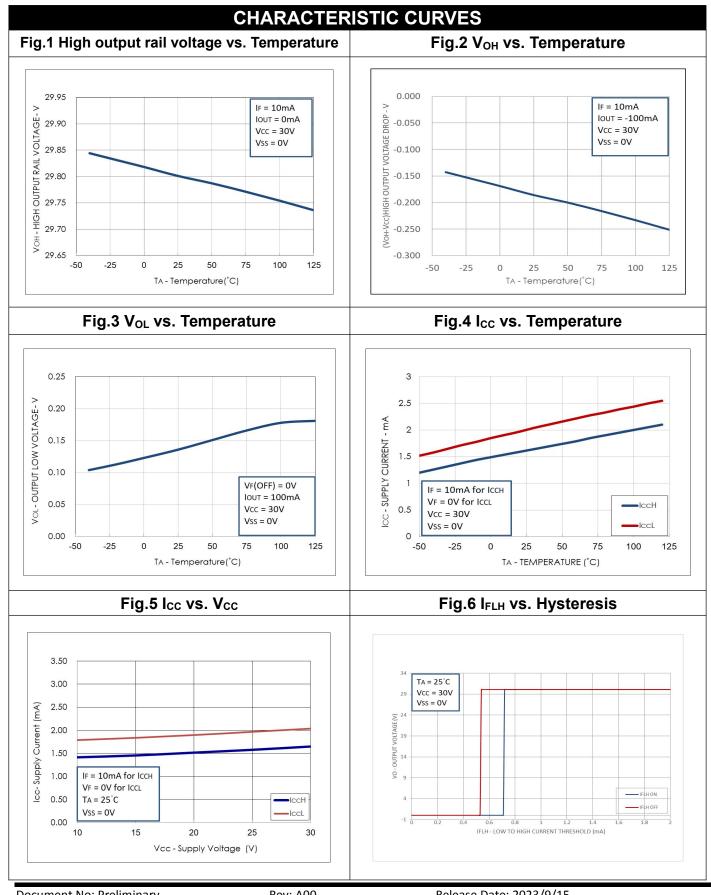
Note 1: Device is considered a two terminal device: pins 1, 2, 3 are shorted together and pins 4, 5, 6 are shorted together.

Note 2: According to UL1577, each photocoupler is tested by applying an insulation test voltage 6000VRMS for one second (leakage current less than 10uA). This test is performed before the 100% production test for partial discharge.

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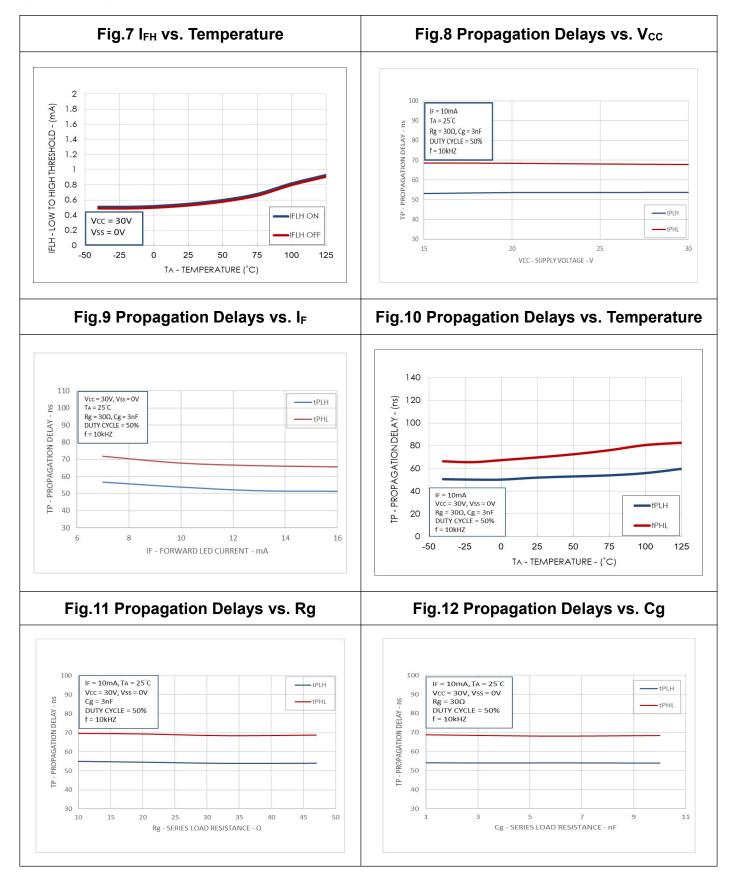
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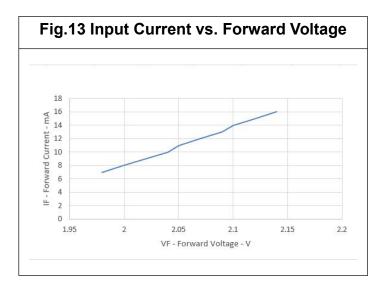


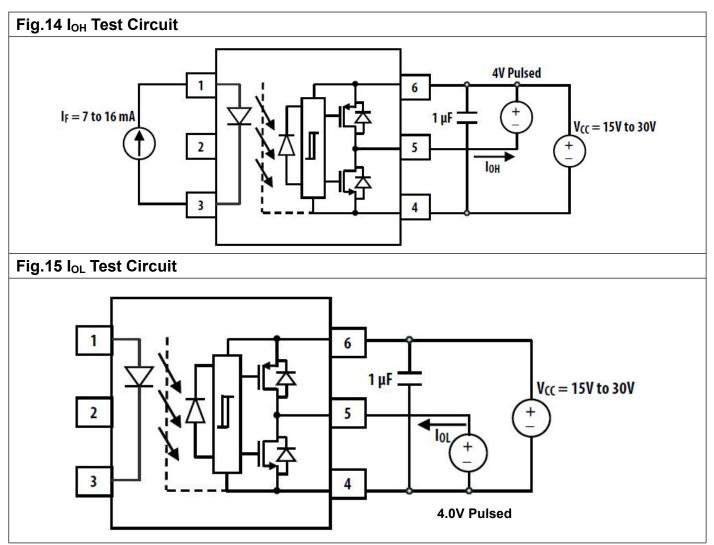
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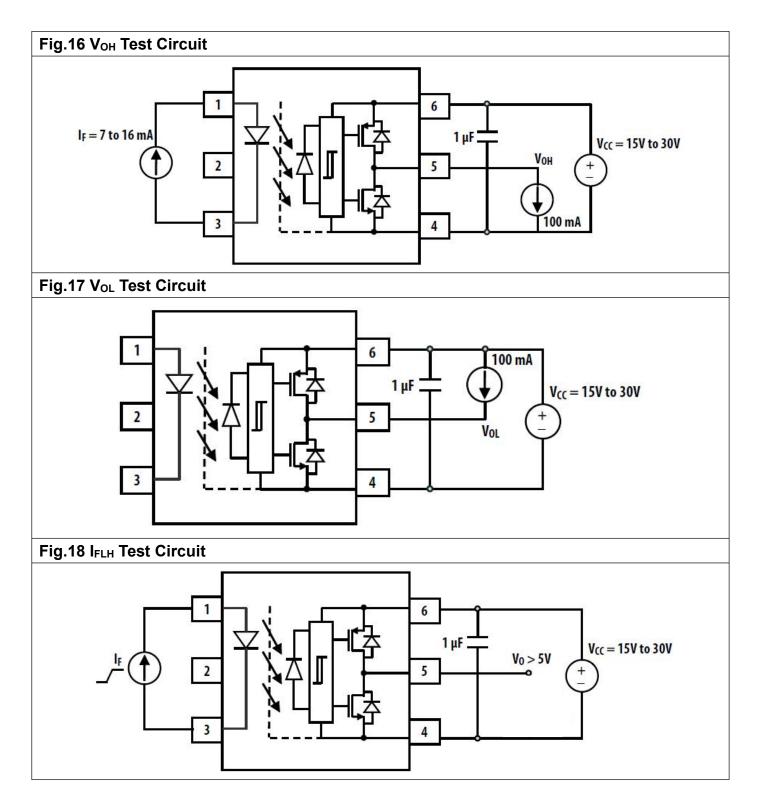
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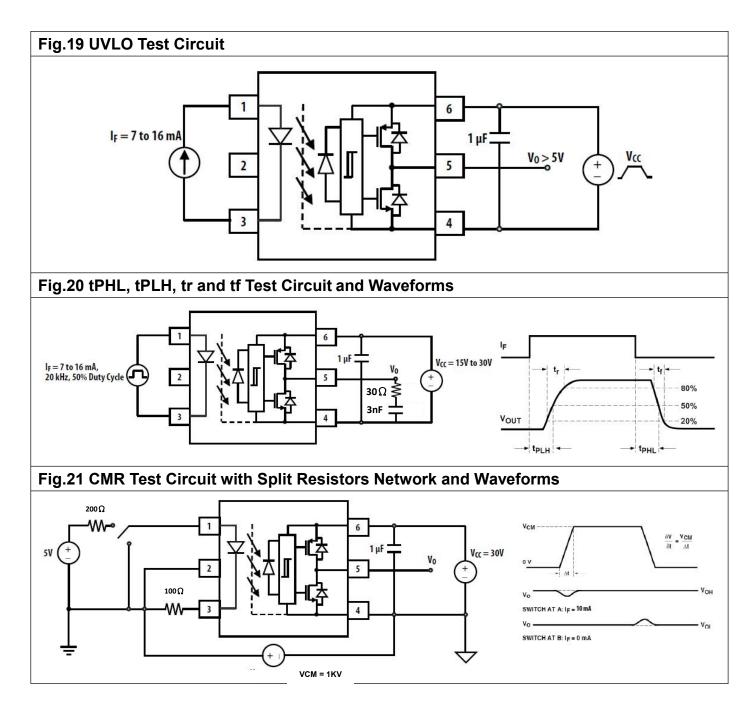






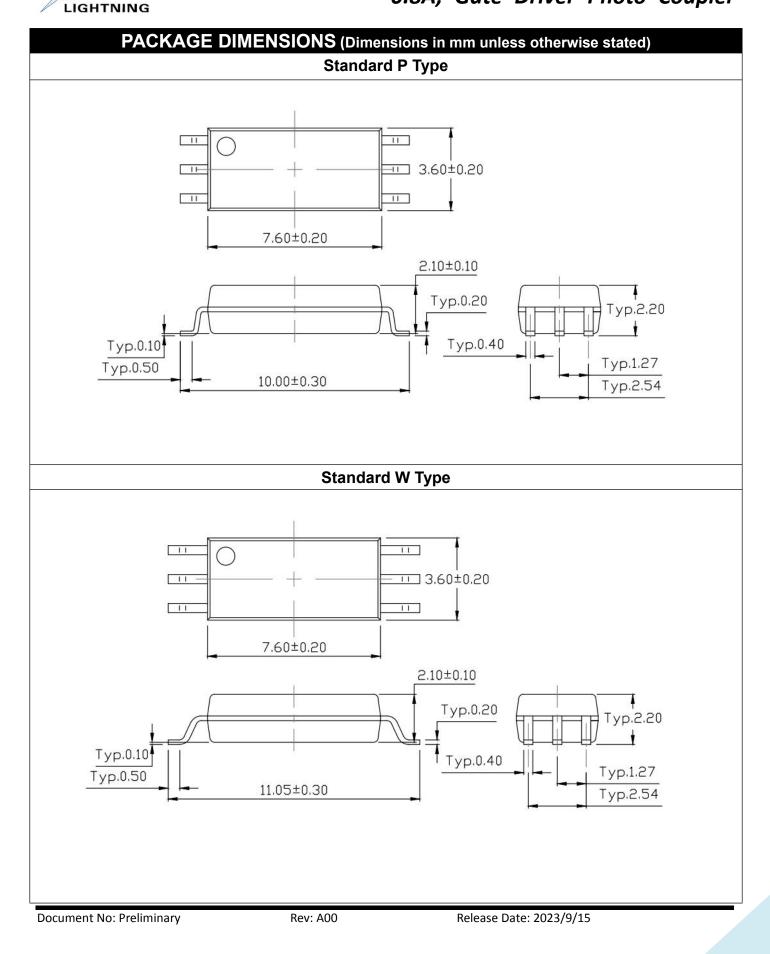






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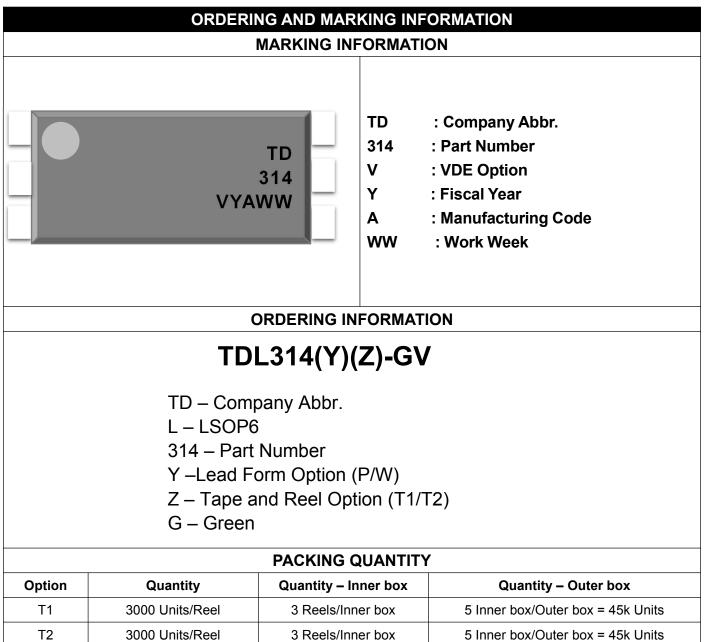
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