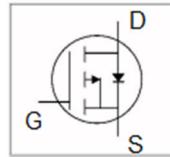
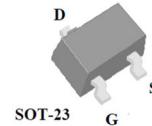


- Simple Drive Requirement
- Small Package Outline
- Surface Mount Device
- RoHS Compliant & Halogen-Free



BVDSS	-20V
RDS(ON)typ	95mΩ
ID	-2.5A



## Description

KE2301 is from Kingeavy innovated design and silicon process technology to achieve the lowest possible on- resistance and fast switching performance. It provides the designer with an extreme efficient device for use in a wide range of power applications.

## Absolute Maximum Ratings@ $T_j=25^\circ\text{C}$ (unless otherwise specified)

Symbol	Parameter	Rating	Units
VDS	Drain-Source Voltage	-20	V
VGS	Gate-Source Voltage	$\pm 8$	V
I <sub>D</sub> @ $T_A=25^\circ\text{C}$	Drain Current, V <sub>GS</sub> @ 4.5V	-2.5	A
I <sub>D</sub> @ $T_A=70^\circ\text{C}$	Drain Current, V <sub>GS</sub> @ 4.5V	-2	A
P <sub>D</sub> @ $T_A=25^\circ\text{C}$	Total Power Dissipation <sup>3</sup>	0.6	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
T <sub>J</sub>	Operating Junction Temperature Range	150	°C

## Thermal Data

Symbol	Parameter	Value	Unit
R <sub>thj-a</sub>	Maximum Thermal Resistance, Junction-ambient <sup>3</sup>	150	°C/W

**Electrical Characteristics@ $T_j=25\text{ }^\circ\text{C}$ (unless otherwise specified)**

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$\text{VGS}=0\text{V}, \text{ID}=250\mu\text{A}$	-20	-	-	V
$\text{RDS}(\text{ON})$	Static Drain-Source On-Resistance	$\text{VGS}=4.5\text{V}, \text{ID}=-2\text{A}$	-	95	130	$\text{m}\Omega$
		$\text{VGS}=-2.5\text{V}, \text{ID}=-2\text{A}$	-	130	175	$\text{m}\Omega$
$\text{V}_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$\text{VDS}=\text{VGS}, \text{ID}=250\mu\text{A}$	-0.5	-	-1	V
$\text{g}_{\text{fs}}$	Forward Transconductance	$\text{VDS}=5\text{V}, \text{ID}=-2\text{A}$	-	4	-	S
$\text{I}_{\text{DSS}}$	Drain-Source Leakage Current	$\text{VDS}=-20\text{V}, \text{VGS}=0\text{V}$	-	-	-1	$\mu\text{A}$
$\text{I}_{\text{GSS}}$	Gate-Source Leakage	$\text{VGS}=\pm 8\text{V}, \text{VDS}=0\text{V}$	-	-	$\pm 100$	nA
$\text{Q}_{\text{g}}$	Total Gate Charge	$\text{ID}=-2\text{A}$ $\text{VDS}=-10\text{V}$ $\text{VGS}=-2.5\text{V}$	-	5.5	-	nC
$\text{Q}_{\text{gs}}$	Gate-Source Charge		-	3.3	-	nC
$\text{Q}_{\text{gd}}$	Gate-Drain ("Miller") Charge		-	1.3	-	nC
$t_{\text{d(on)}}$	Turn-on Delay Time	$\text{VDS}=-10\text{V}$ $\text{ID}=1\text{A}$ $\text{RG}=1\Omega$ $\text{VGS}=-4.5\text{V}, \text{RL}=10\Omega$	-	11	20	ns
$t_r$	Rise Time		-	35	60	ns
$t_{\text{d(off)}}$	Turn-off Delay Time		-	30	50	ns
$t_f$	Fall Time		-	10	20	ns
$\text{C}_{\text{iss}}$	Input Capacitance	$\text{VGS}=0\text{V}$ $\text{VDS}=-10\text{V}$ $f=1.0\text{MHz}$	-	405	-	pF
$\text{C}_{\text{oss}}$	Output Capacitance		-	75	-	pF
$\text{Crss}$	Reverse Transfer Capacitance		-	55	-	pF

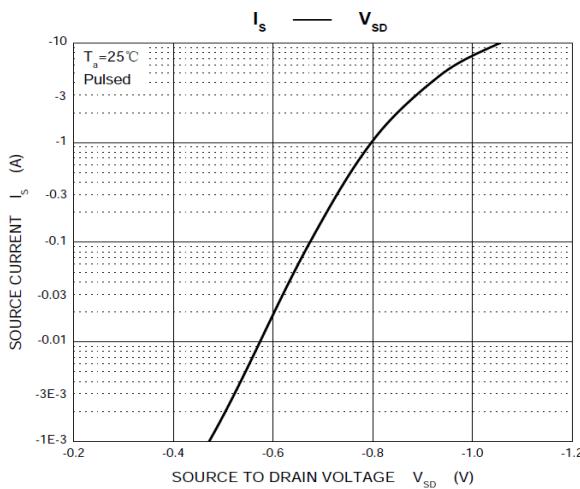
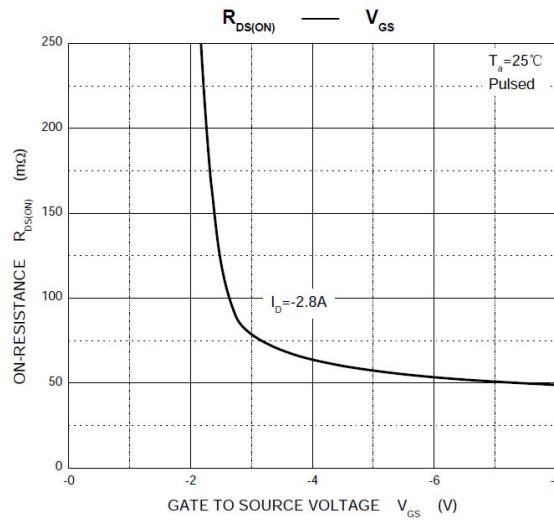
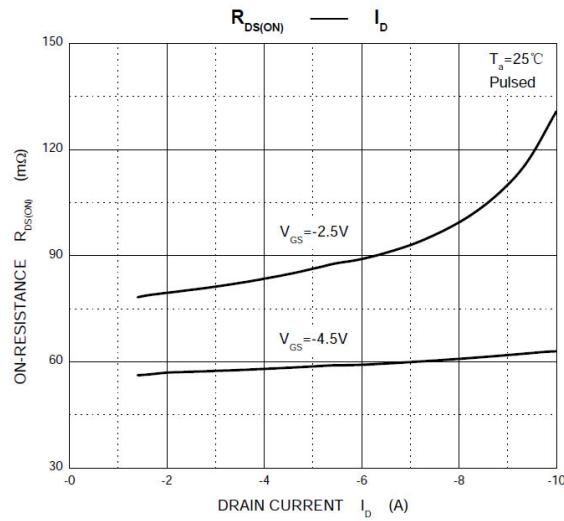
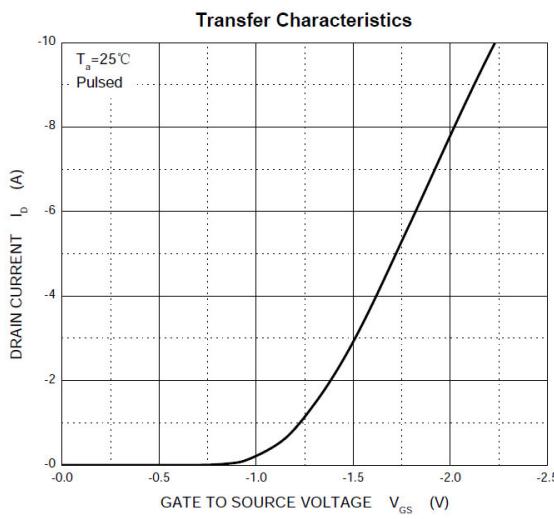
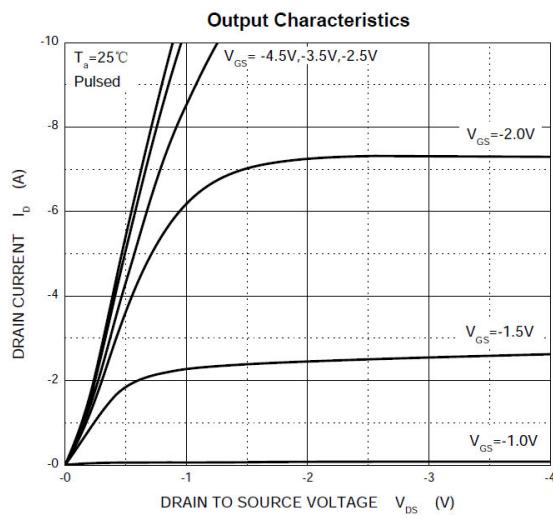
**Source-Drain Diode**

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$\text{V}_{\text{SD}}$	Forward On Voltage <sup>2</sup>	$\text{Is}=0.7\text{A}, \text{VGS}=0\text{V}$	-	-0.8	-1.2	V

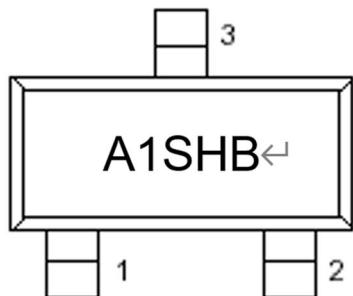
**Notes:**

- 1.Pulse width limited by Max. junction temperature.
- 2.Pulse test
- 3.Surface mounted on 1 in<sup>2</sup> 2oz copper pad of FR4 board,  $t \leq 10\text{sec}$ ; 300°C/W when mounted on min. copper pad.

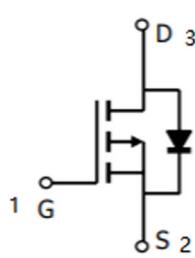
**Typical Performance Characteristics**



## Marking Information



Top view



印字 marking : A1SHB

## Package Outline : SOT-23

