

-20V P-Channel Enhancement Mode MOSFET

Description

The AP2301DI uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

General Features

V_{DS} = -20V I_D =-2.1A

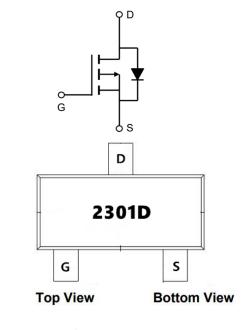
 $R_{DS(ON)} < 150 m\Omega @ V_{GS} = -4.5 V (Type: 118 m\Omega)$

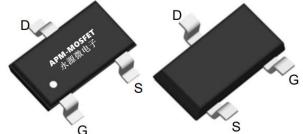
Application

Battery protection

Load switch

Uninterruptible power supply





Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
AP2301DI	SOT23L	2301D	3000

Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	-20	V
Vgs	Gate-Source Voltage	±12	V
I _D @T _A =25℃	Continuous Drain Current, V _{GS} @ -4.5V ¹	-2.1	А
I _D @T _A =70°C	Continuous Drain Current, V _{GS} @ -4.5V ¹	-0.8	А
Ідм	Pulsed Drain Current ²	-7.2	А
PD@TA=25°C	Total Power Dissipation ³	1.0	W
P _D @T _A =70°C	Total Power Dissipation ³	0.5	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C
R₀JA	Thermal Resistance Junction-Ambient ¹	125	°C/W
RθJC	Thermal resistance, junction-case	32	°C/W



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Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
V(BR)DSS	Drain-Source Breakdown Voltage	V _{GS} =0V,I _D = -250µA	-20	-24	-	V
IDSS	Zero Gate Voltage Drain Current	V _{DS} = -20V, V _{GS} = 0V,	-	-	-1	μA
IGSS	Gate to Body Leakage Current	V_{DS} =0V, V_{GS} = ±12V	-	-	±100	nA
VGS(th)	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = -250µA	-0.4	-0.7	-1.0	V
	Static Drain-Source on-Resistance	V _{GS} =-4.5V, I _D =-2A	-	118	150	mΩ
RDS(on)		V _{GS} =-2.5V, I _D =-1A	-	165	190	
Ciss	Input Capacitance		-	34	-	pF
Coss	Output Capacitance	V _{DS} = -10V, V _{GS} = 0V, f = 1.0MHz	-	24	-	pF
Crss	Reverse Transfer Capacitance		-	23	-	pF
Qg	Total Gate Charge		-	2.4	-	nC
Qgs	Gate-Source Charge	V _{DS} = -10V, I _D = -2A, V _{GS} = -4.5V	-	0.6	-	nC
Qgd	Gate-Drain("Miller") Charge		-	0.4	-	nC
td(on)	Turn-on Delay Time		-	5	-	ns
tr	Turn-on Rise Time	V _{DD} = -10V, R∟=5Ω,	-	18	-	ns
td(off)	Turn-off Delay Time	$R_{GEN}=3\Omega, V_{GS}=-4.5V,$	-	79	-	ns
t _f	Turn-off Fall Time		-	42	-	ns
IS	Maximum Continuous Drain to Source Diode Forward Current		-	-	-2.1	А
ISM	Maximum Pulsed Drain to Source Diode Forward Current		-	-	-7.2	А
VSD	Drain to Source Diode Forward Voltage	V _{GS} = 0V, I _S = -2A	-	-	-1.2	V

Electrical Characteristics (TJ=25°C, unless otherwise noted)

Note :

1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.

2、The data tested by pulsed , pulse width \bigtriangleup 300us , duty cycle \bigtriangleup 2%

3. The power dissipation is limited by 150° C junction temperature

4. The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.



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Typical Characteristics

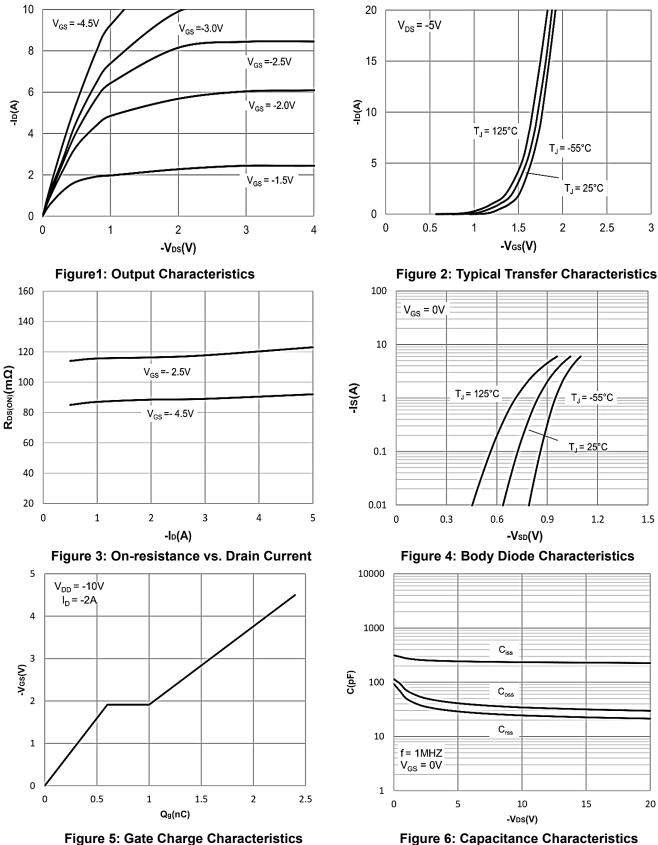


Figure 5: Gate Charge Characteristics

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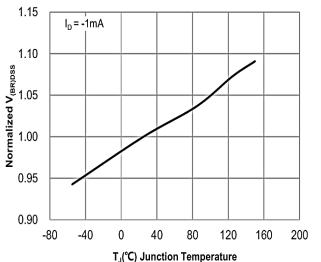


Figure 7:Normalized Breakdown voltage vs. Junction Temperature

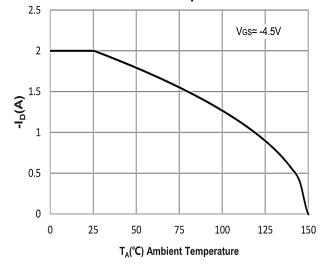
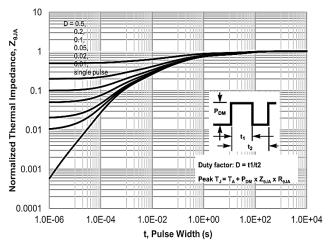


Figure 9: Maximum Safe Operating Area





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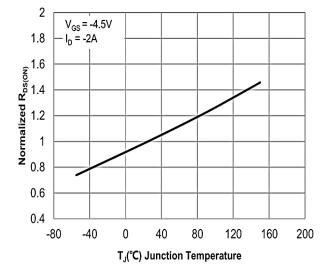


Figure 8: Normalized on Resistance vs. Junction Temperature

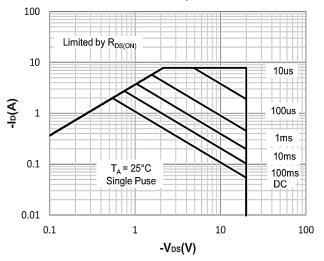
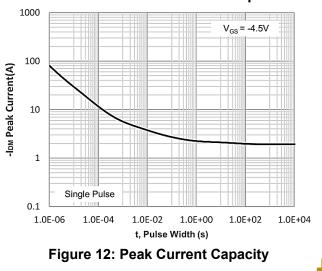


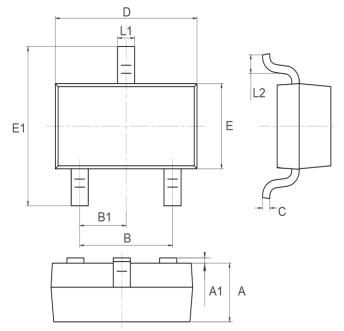
Figure 10: Maximum Continuous Drian Current vs. Ambient Temperature





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Package Mechanical Data-SOT23L



Sumbol	Dim in mm			
Symbol	Min	Тур	Мах	
A	0.9	1	1.1	
A1	0	0.05	0.1	
В	1.8	1.9	2	
B1	0.95TYP			
С	0.08	0.115	0.15	
D	2.8	2.9	3	
E	1.2	1.3	1.4	
E1	2.25	2.4	2.55	
L1	0.3	0.4	0.5	
L2	0.2	0.35	0.5	



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AP2301DI

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Edition	Date	Change
REV1.0	2021/4/31	Initial release

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