鑫沃科技
XIN WO TECHNOLOGY

产品规格手册

PRODUCT SPECIFICATION MANUAL

XW4882

Dual N-Ch 40V Fast Switching MOSFETs
SOP8/40V/8A

深圳东为电子科技有限公司
DONGWEI ELECTRONIC TECHNOLOGY CO.,LTD

★ Green Device Available
★ Super Low Gate Charge
★ Excellent CdV/dt effect decline
★ Advanced high cell density Trench technology

Product Summary

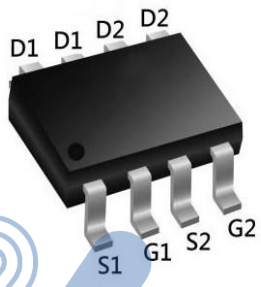
BVDSS	RDSON	ID
40V	17mΩ	8A

Description

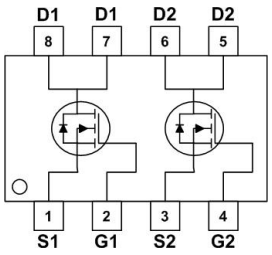
The XW4882 is the high cell density trenched N-ch MOSFETs, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

The XW4882 meet the RoHS and Green Product

SOP8 Pin Configuration



pin assignment



Schematic diagram

Package Marking and Ordering Information

Product	Package	Marking	Packing	Min Unit Quantity
XW4882	SOP8	*	3000PCS/Reel	3000PCS

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Absolute Maximum Ratings (T_A=25℃ unless otherwise specified)

Symbol	Parameter		Max.	Units
V_{DSS}	Drain-Source Voltage		40	V
V_{GSS}	Gate-Source Voltage		±20	V
I_D	Continuous Drain Current	T_A= 25℃	8.0	A
		T_A= 100℃	6	A
I_{DM}	Pulsed Drain Current note1		32.8	A
EAS	Single Pulsed Avalanche Energy note2		13	mJ
P_D	Power Dissipation	T_A= 25℃	2.9	W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient		63	℃/W
T_J, T_{STG}	Operating and Storage Temperature Range		-55 to +150	℃

N-Channel Electrical Characteristics (T_J=25℃ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	V_{GS}=0V, I_D=250μA	40	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}=40V, V_{GS}=0V	-	-	1.0	μA
I_{GSS}	Gate to Body Leakage Current	V_{DS}=0V, V_{GS}=±20V	-	-	±100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_D=250μA	1.0	1.5	2.5	V
$R_{DS(on)}$	Static Drain-Source on-Resistance note3	V_{GS}=10V, I_D=8A	-	17	22	mΩ
		V_{GS}=4.5V, I_D=5A	-	25	35	mΩ
Dynamic Characteristics						
C_{iss}	Input Capacitance	V_{DS}=20V, V_{GS}=0V, f=1.0MHz	-	633	-	pF
C_{oss}	Output Capacitance		-	67	-	pF
C_{rss}	Reverse Transfer Capacitance		-	58	-	pF
Q_g	Total Gate Charge	V_{DS}=20V, I_D=8A, V_{GS}=10V	-	12	-	nC
Q_{gs}	Gate-Source Charge		-	3.2	-	nC
Q_{gd}	Gate-Drain("Miller") Charge		-	3.1	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-on Delay Time	V_{DD}= 20V, R_L =2.5Ω V_{GS}=10V,R_{REN} =3Ω	-	4	-	ns
t_r	Turn-on Rise Time		-	3	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	15	-	ns
t_f	Turn-off Fall Time		-	2	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	8	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	32	A
V_{SD}	Drain to Source Diode Forward Voltage	V_{GS}=0V, I_S= 8A	-	-	1.2	V

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition : T_J=25℃,V_{DD}=20V,V_G=10V,L=0.5mH,Rg=25Ω,I_{AS}=7.2A

T_J=25℃,V_{DD}=-20V,V_G= -10V,L=0.5mH,Rg=25Ω,I_{AS}=-8.4A

3. Pulse Test: Pulse Width≤300μs, Duty Cycle≤2%

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Typical Performance Characteristics-N

Figure1: Output Characteristics

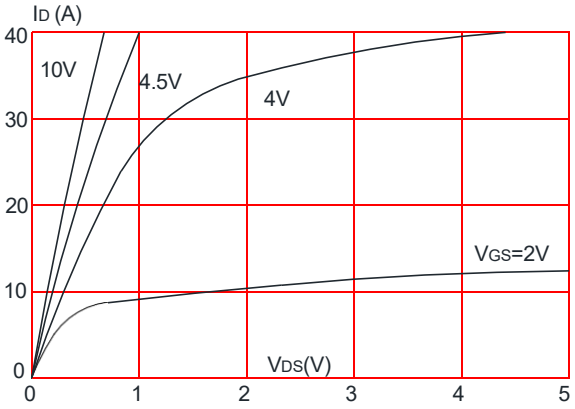

Figure 2: Typical Transfer Characteristics

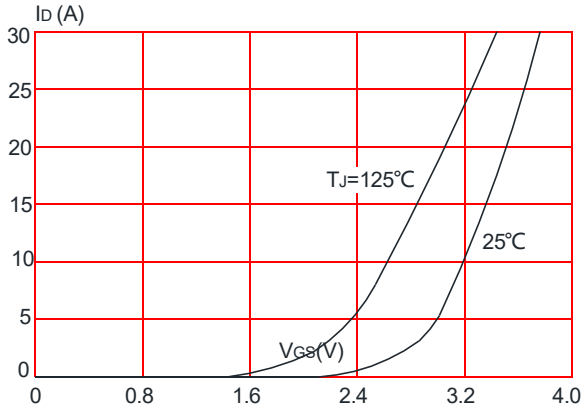

Figure 3: On-resistance vs. Drain Current

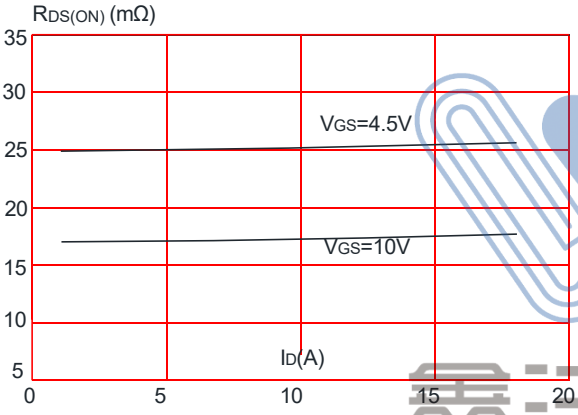

Figure 4: Body Diode Characteristics

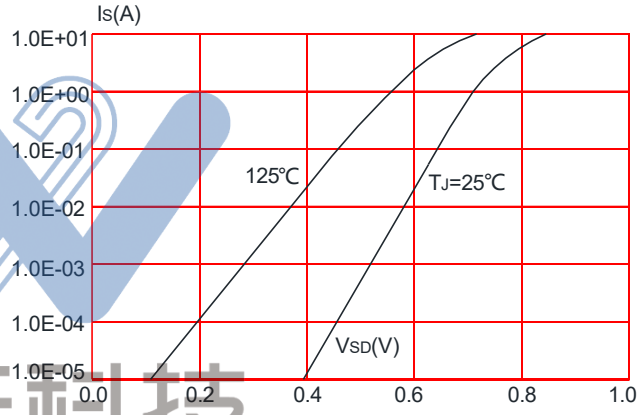

Figure 5: Gate Charge Characteristics

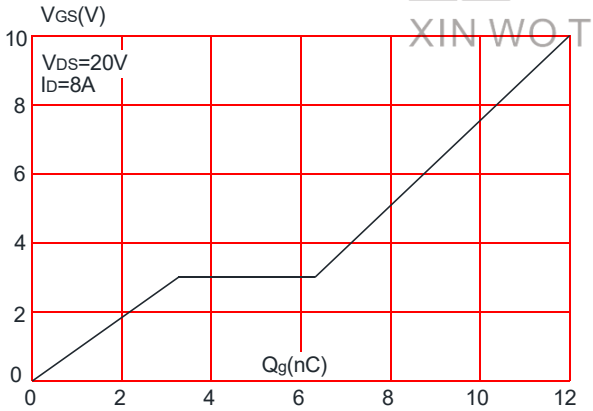

Figure 6: Capacitance Characteristics

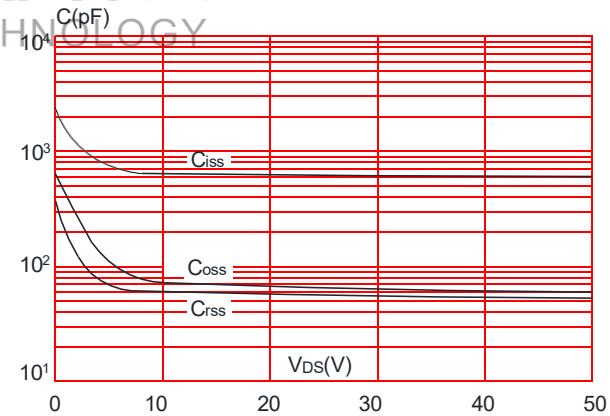

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Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

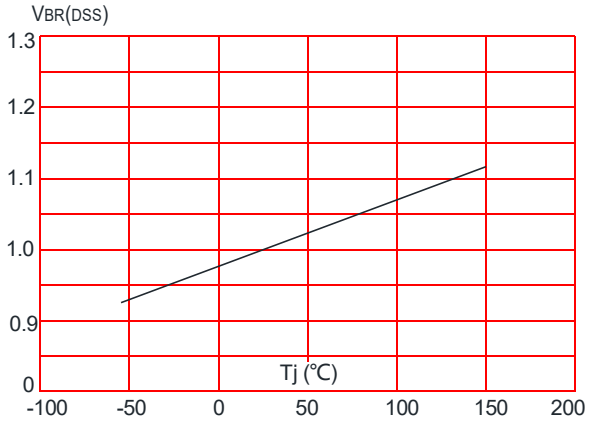

Figure 8: Normalized on Resistance vs. Junction Temperature

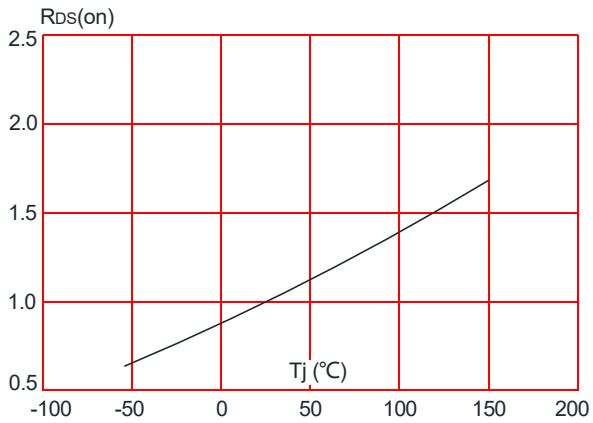

Figure 9: Maximum Safe Operating Area

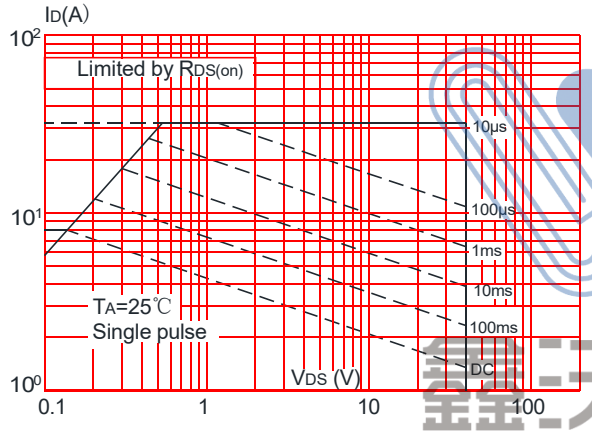

Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature

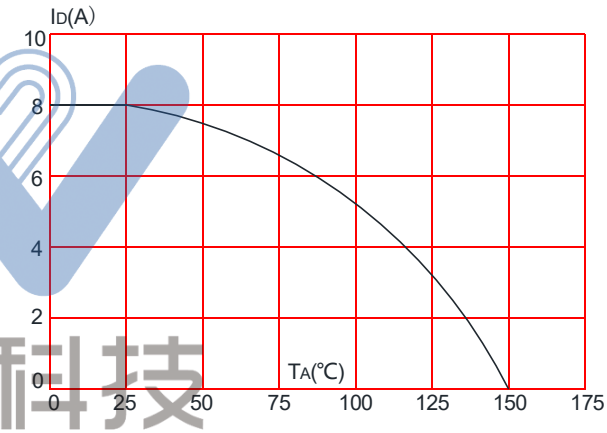

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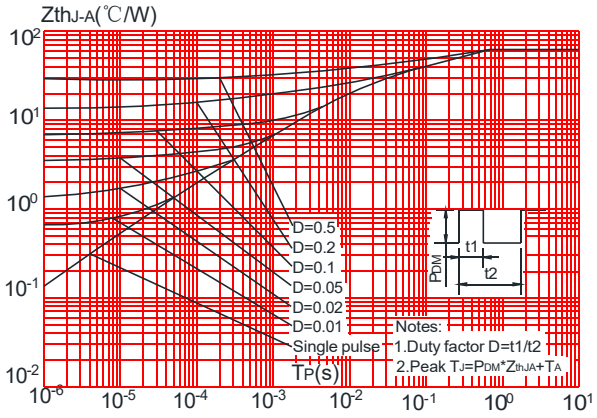
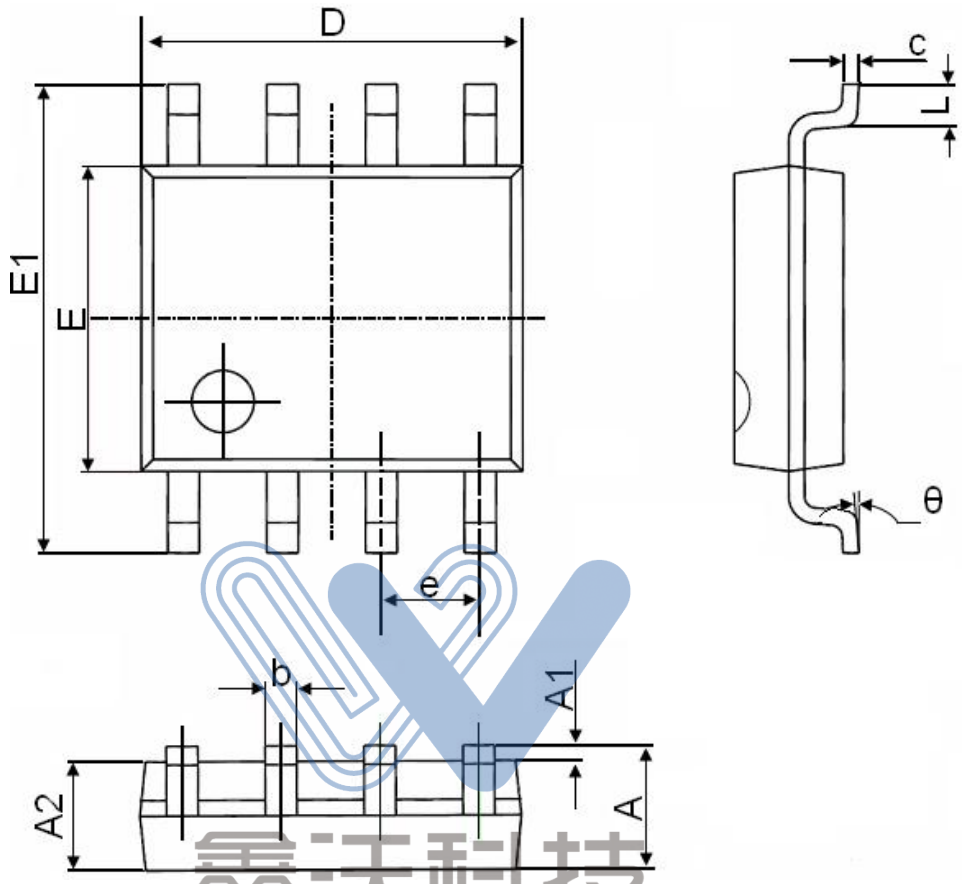
Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient



SOP-8 Package Information



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Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270(BSC)		0.050(BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°