

# SQP90P06-07L\_GE3-VB Datasheet P-Channel 60 V (D-S) MOSFET

PRODUCT SUMMARY								
V <sub>DS</sub> (V)	$R_{DS(on)}\left(\Omega\right)$	I <sub>D</sub> (A) <sup>c</sup>						
- 60	0.0030 at V <sub>GS</sub> = - 10 V	- 120						
	0.0040 at V <sub>GS</sub> = - 4.5 V	- 105						

### **FEATURES**

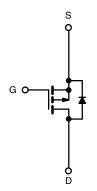
- Trench Power MOSFET
- Compliant to RoHS Directive 2002/95/EC



## **APPLICATIONS**

• DC/DC Primary Switch





P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ( $T_C = 2$	25 °C, unless othe	rwise noted)			
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V <sub>DS</sub>	- 60	V		
Gate-Source Voltage	V <sub>GS</sub>	± 20			
Continuous Drain Current /T 175 °C\C	T <sub>C</sub> = 25 °C	I-	- 120		
Continuous Drain Current (T <sub>J</sub> = 175 °C) <sup>c</sup>	T <sub>C</sub> = 125 °C	I <sub>D</sub>	- 75	٨	
Pulsed Drain Current	I <sub>DM</sub>	- 360	A		
Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	- 90		
Single Pulse Avalanche Energy <sup>a</sup>	L = 0.1 IIII	E <sub>AS</sub>	211	mJ	
Power Dissipation	T <sub>C</sub> = 25 °C	P <sub>D</sub>	250 <sup>b</sup>		
rowei Dissipation	T <sub>A</sub> = 25 °C	l 'D	2.4	W	
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C		

THERMAL RESISTANCE RATINGS								
Parameter	Symbol	Symbol Limit						
Junction-to-Ambient Free Air	Free Air R <sub>thJA</sub> 62							
Junction-to-Case	R <sub>thJC</sub>	0.6	°C/W					

#### Notes:

- a. Duty cycle  $\leq$  1 %.
- b. See SOA curve for voltage derating.
- c. Limited by package.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit			
Static	•								
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 60			V			
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$		-3		V			
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA			
		V <sub>DS</sub> = - 60 V, V <sub>GS</sub> = 0 V			- 1				
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = - 60 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C			- 50	μΑ			
		$V_{DS} = -60 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 175 ^{\circ}\text{C}$			- 250				
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = - 5 V, V <sub>GS</sub> = - 10 V	- 120			Α			
		V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 30 A		0.0030		1			
	<sub>D</sub>	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 30 A, T <sub>J</sub> = 125 °C		0.0040		0			
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 30 A, T <sub>J</sub> = 175 °C		0.0060		Ω			
		V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 20 A		0.0040					
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 30 A	20			S			
Dynamic <sup>b</sup>	•								
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = - 25 V, f = 1 MHz		18500		pF			
Output Capacitance	C <sub>oss</sub>			975					
Reverse Transfer Capacitance	C <sub>rss</sub>	]		760					
Total Gate Charge <sup>c</sup>	$Q_g$			160	240	nC			
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	V <sub>DS</sub> = - 30 V, V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 90 A		40					
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$	]		36					
Gate Resistance	$R_{g}$	f = 1.0 MHz		3		Ω			
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			20	30				
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = -30 \text{ V}, R_{L} = 0.33 \Omega$		190	250	ns			
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$I_D \cong$ - 90 A, $V_{GEN}$ = - 10 V, $R_g$ = 2.5 $\Omega$		140	210				
Fall Time <sup>c</sup>	t <sub>f</sub>	]		290	350				
Source-Drain Diode Ratings and Ch	aracteristics	(T <sub>C</sub> = 25 °C) <sup>b</sup>							
Continuous Current	I <sub>S</sub>				- 120	٨			
Pulsed Current	I <sub>SM</sub>				- 360	Α			
Forward Voltage <sup>a</sup>	$V_{SD}$	I <sub>F</sub> = - 50 A, V <sub>GS</sub> = 0 V		- 1.0	- 1.5	V			
Reverse Recovery Time	t <sub>rr</sub>			60	90	ns			
Peak Reverse Recovery Current	I <sub>RM(REC)</sub>	I <sub>F</sub> = - 50 A, dl/dt = 100 A/μs		- 3	- 4.5	Α			
Reverse Recovery Charge	Q <sub>rr</sub>	1		0.09	0.2	μС			

### Notes:

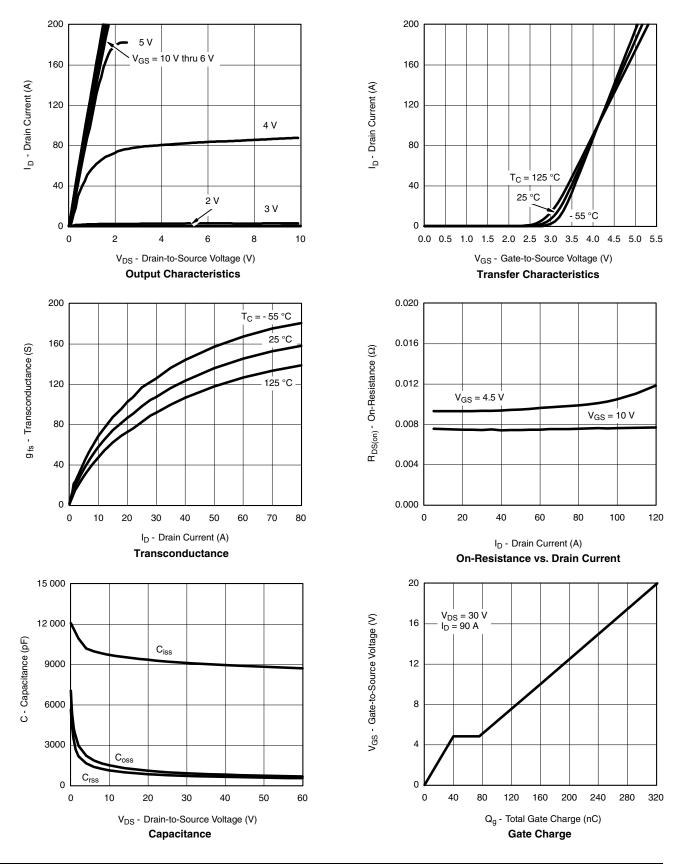
- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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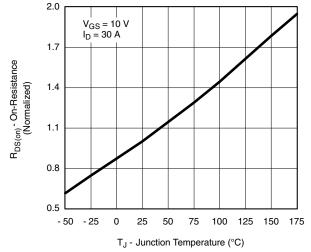


## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

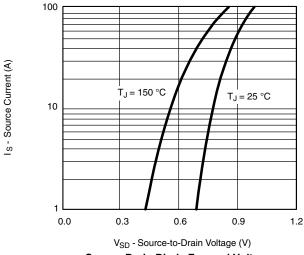




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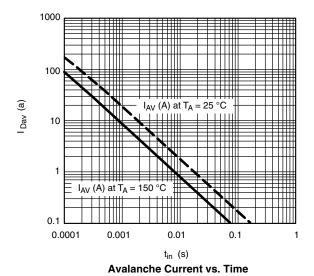


On-Resistance vs. Junction Temperature



Source-Drain Diode Forward Voltage

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72 68 64 60 -50 -25 0 25 50 75 100 125 150 175 T<sub>J</sub> - Junction Temperature (°C)

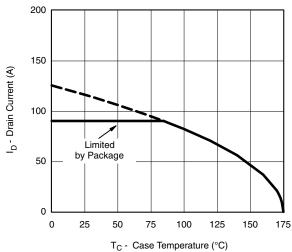
 $I_D = 10 \text{ mA}$ 

Drain Source Breakdown vs.
Junction Temperature

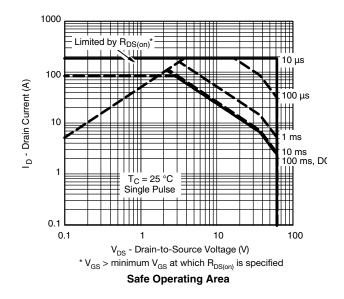
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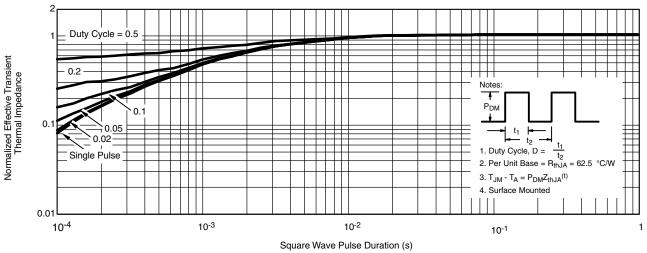


## THERMAL RATINGS



Maximum Avalanche and Drain Current vs. Case Temperature



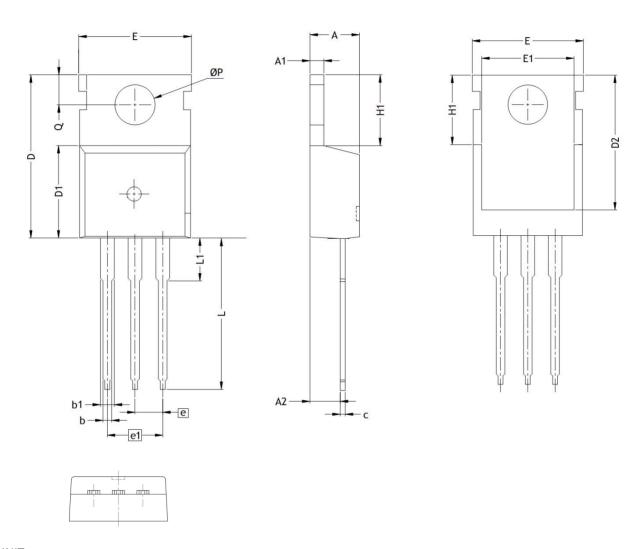


Normalized Thermal Transient Impedance, Junction-to-Case

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# TO-220 Package Outline Drawing



UNIT: mm

SYMBOLS	Α	A1	A2	b	b1	С	D	D1	D2	E	E1	e
MIN	4.25	1.25	2.35	0.7	1.15	0.45	14.35	8.80	13.05	9.90	7.85	2.540
MAX	4.65	1.35	2.55	0.9	1.75	0.60	15.95	9.50	13.65	10.35	8.85	BSC
SYMBOLS	e1	H1	L	L1	Q	фР						
MIN	5.080	6.30	12.85	2.85	2.70	3.50						
MAX	BSC	6.65	13.50	3.25	2.90	3.70						

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