PRODUCT DATA SHEET

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Datasheet Resources Samples

Please note: Please check the JINGAO Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.jg-semi.cn. Please email any questions regarding the system integration to JINGAO_questions@jgsemi.com.

General Description

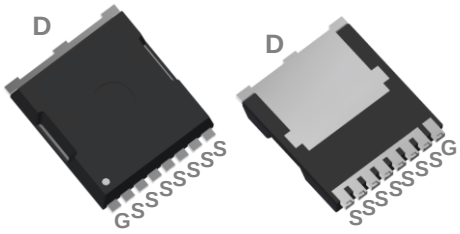
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

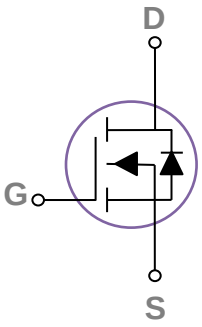
BVDSS	RDSON	ID
100V	1.7mΩ	230A

Features

- *100V,230A, RDS(ON) =1.7mΩ @VGS = 10V*
- *Improved dv/dt capability*
- *Fast switching*
- *Green Device Available*

TOLL Pin Configuration





Applications

- *Networking*
- *Load Switch*
- *LED applications*
- *Quick Charger*

Absolute Maximum Ratings Tc=25℃ unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	100	V
V_{GS}	Gate-Source Voltage	±20	V
I_D	Drain Current – Continuous (T_C=25℃)	230	A
	Drain Current – Continuous (T_C=100℃)	147	A
I_{DM}	Drain Current – Pulsed[1]	920	A
EAS	Single Pulse Avalanche Energy[2]	952	mJ
IAS	Single Pulse Avalanche Current[2]	138	A
P_D	Power Dissipation (T_C=25℃)	298	W
	Power Dissipation – Derate above 25℃	2.38	W/℃
T_{STG}	Storage Temperature Range	-55 to 150	℃
T_J	Operating Junction Temperature Range	-55 to 150	℃

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	℃/W
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	0.42	℃/W

Electrical Characteristics (T_J=25 ℃, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V , I_D=250μA	100	---	---	V
I_{DSS}	Drain-Source Leakage Current	V_{DS}=80V , V_{GS}=0V , T_J=25℃	---	---	1	μA
		V_{DS}=80V , V_{GS}=0V , T_J=85℃	---	---	10	μA
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±20V , V_{DS}=0V	---	---	±100	nA

On Characteristics

$R_{DS(ON)}$	Static Drain-Source On-Resistance	V_{GS}=10V , I_D=30A	---	1.7	2.1	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS}=V_{DS} , I_D =250μA	2	2.6	4	V

Dynamic and switching Characteristics[3]

Q_g	Total Gate Charge	V_{DS}=50V , V_{GS}=10V , I_D=80A	---	122	185	nC
Q_{gs}	Gate-Source Charge		---	27	40	
Q_{gd}	Gate-Drain Charge		---	33	50	
$T_{d(on)}$	Turn-On Delay Time	V_{DD}=50V , V_{GS}=10V , R_G=6Ω I_D=80A	---	20	30	ns
T_r	Rise Time		---	32	50	
$T_{d(off)}$	Turn-Off Delay Time		---	98	150	
T_f	Fall Time		---	32	50	
C_{iss}	Input Capacitance	V_{DS}=50V , V_{GS}=0V , F=1MHz	---	7700	11550	pF
C_{oss}	Output Capacitance		---	1500	2250	
C_{rss}	Reverse Transfer Capacitance		---	30	45	
R_g	Gate resistance	V_{GS}=0V, V_{DS}=0V, F=1MHz	---	1	---	Ω

Guaranteed Avalanche Energy

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
EAS	Single Pulse Avalanche Energy	V_{DD}=50V, L=0.1mH, I_{AS}=70A	245	---	---	mJ

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	V_G=V_D=0V , Force Current	---	---	230	A
I_{SM}	Pulsed Source Current		---	---	460	A
V_{SD}	Diode Forward Voltage	V_{GS}=0V , I_S=1A , T_J=25℃	---	---	1	V
t_{rr}	Reverse Recovery Time	V_R=100V, I_S=10A di/dt=100A/μs , T_J=25℃	---	250	---	ns
Q_{rr}	Reverse Recovery Charge		---	820	---	nC

Note :
1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. V_{DD}=50V,V_{GS}=10V,L=0.1mH,I_{AS}=138A.,R_G=25Ω,Starting T_J=25℃.
3. Essentially independent of operating temperature.

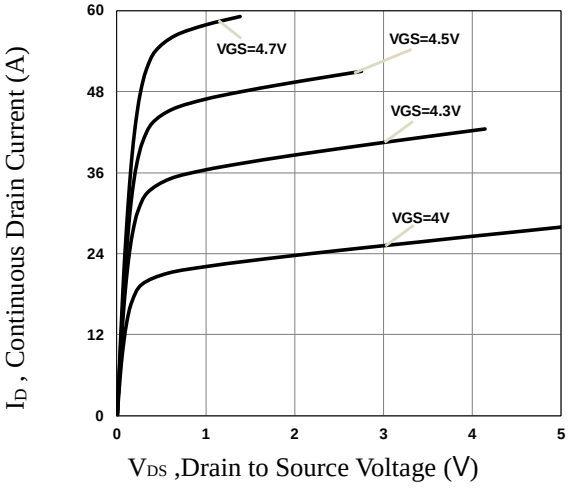


Fig.1 Typical Output Characteristics

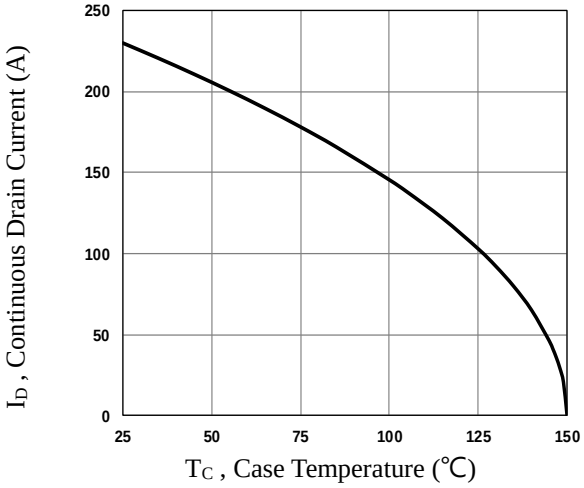


Fig.2 Continuous Drain Current vs. T_C

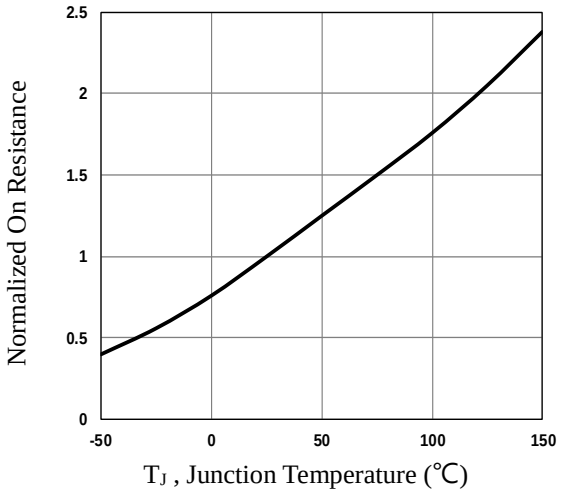


Fig.3 Normalized RDSON vs. T_J

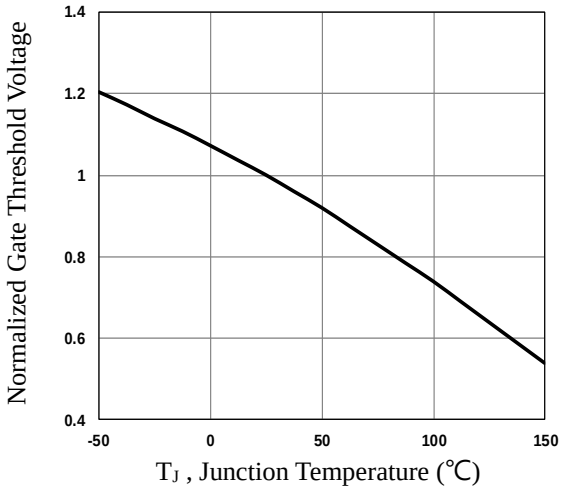


Fig.4 Normalized V_{th} vs. T_J

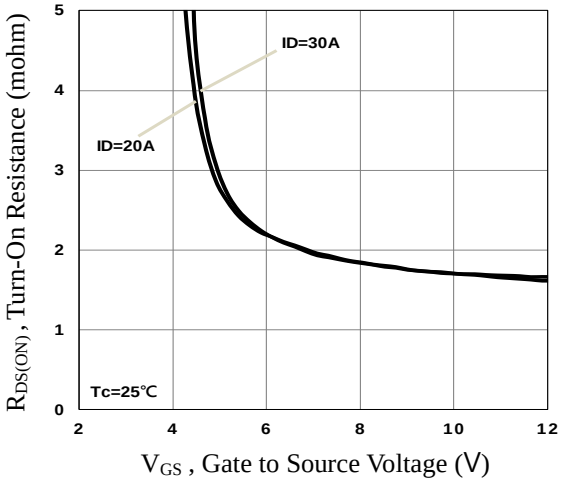


Fig.5 Turn-On Resistance vs. V_{GS}

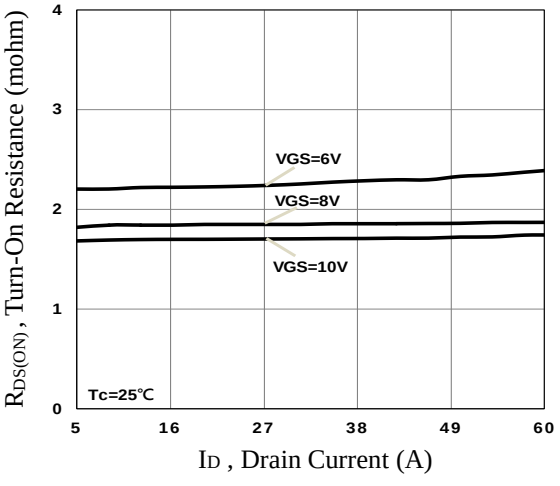


Fig.6 Turn-On Resistance vs. ID

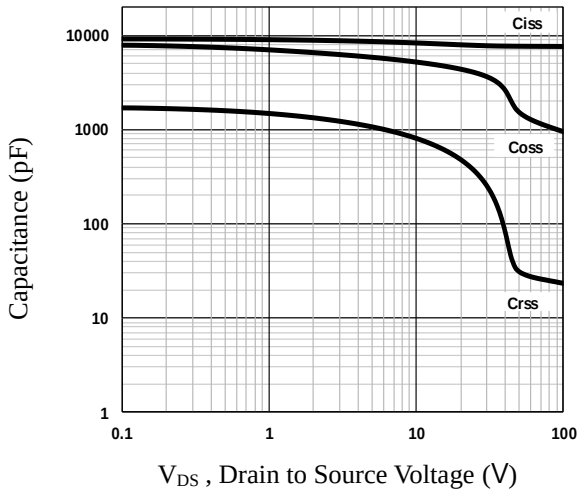


Fig.7 Capacitance Characteristics

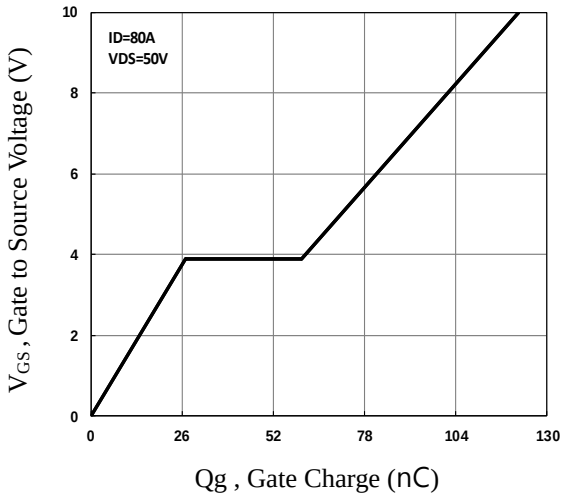


Fig.8 Gate Charge Characteristics

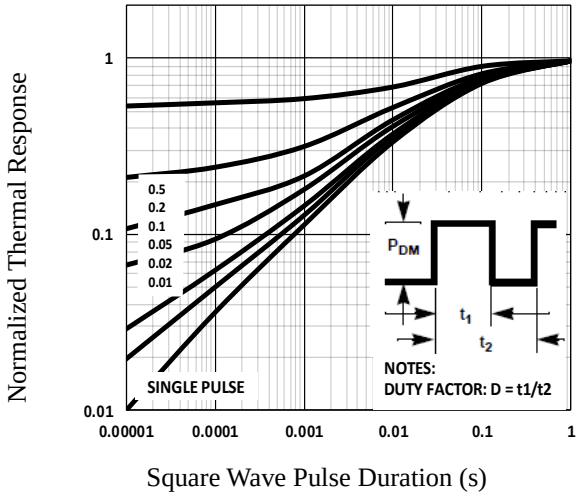


Fig.9 Normalized Transient Impedance

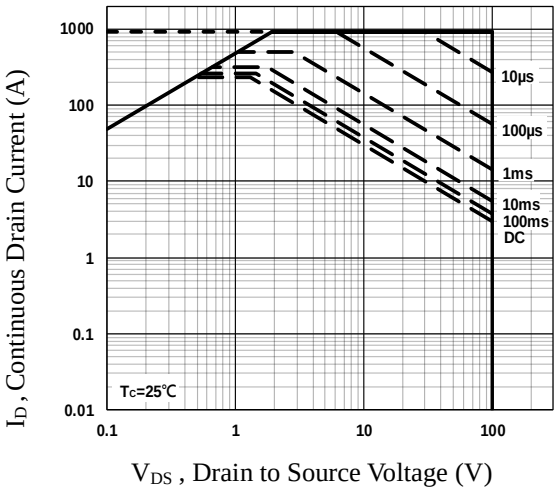


Fig.10 Maximum Safe Operation Area

$$EAS = \frac{1}{2} L \times I_{AS}^2 \times \frac{BV_{DSS}}{BV_{DSS}-V_{DD}}$$

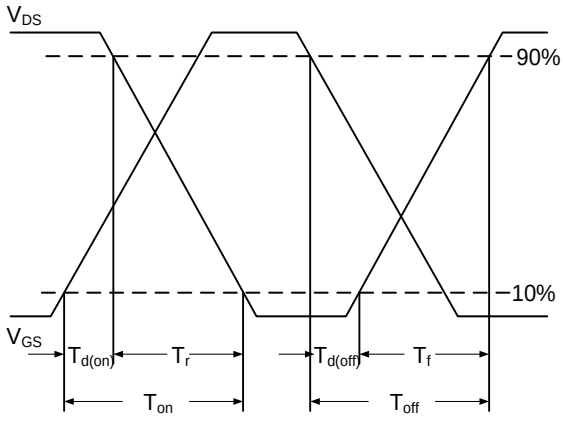


Fig.11 Switching Time Waveform

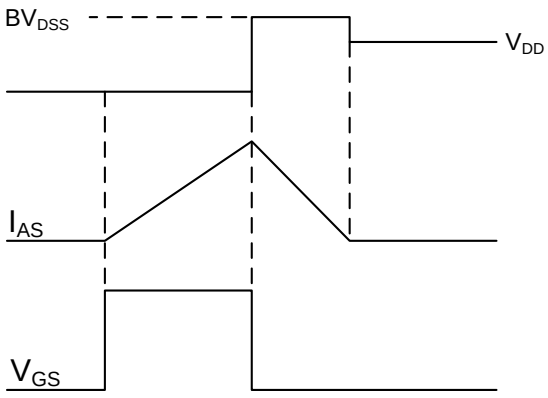
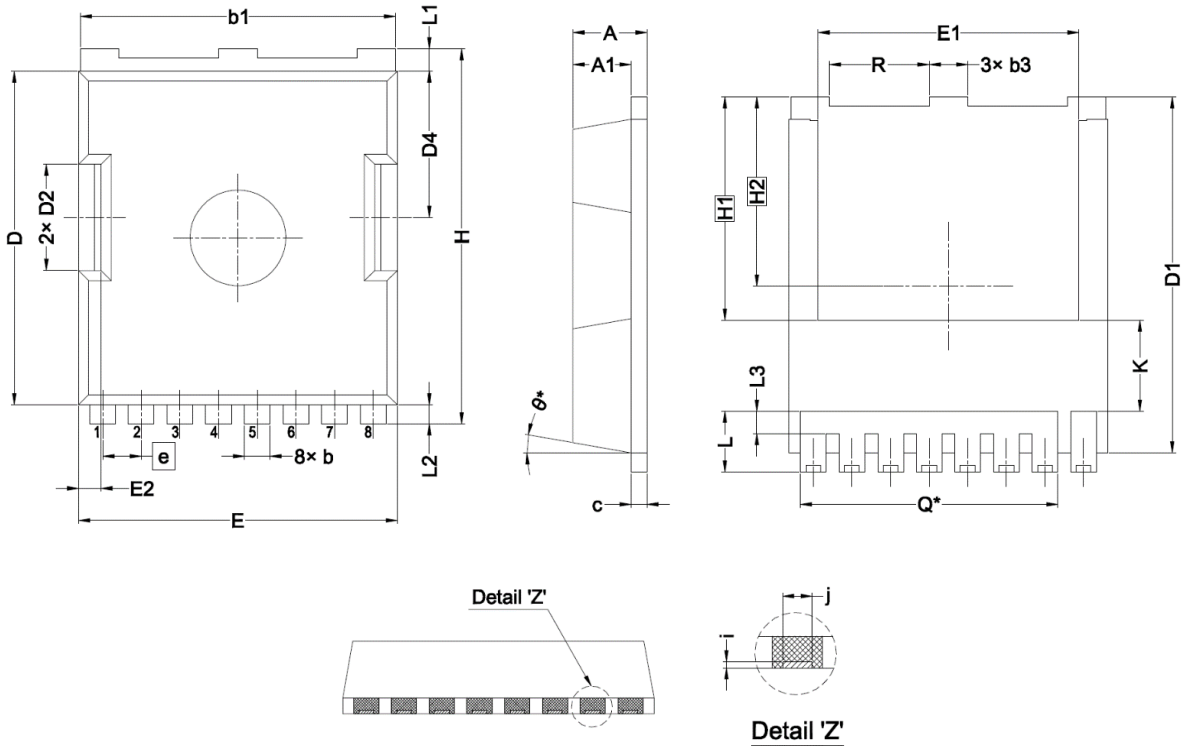


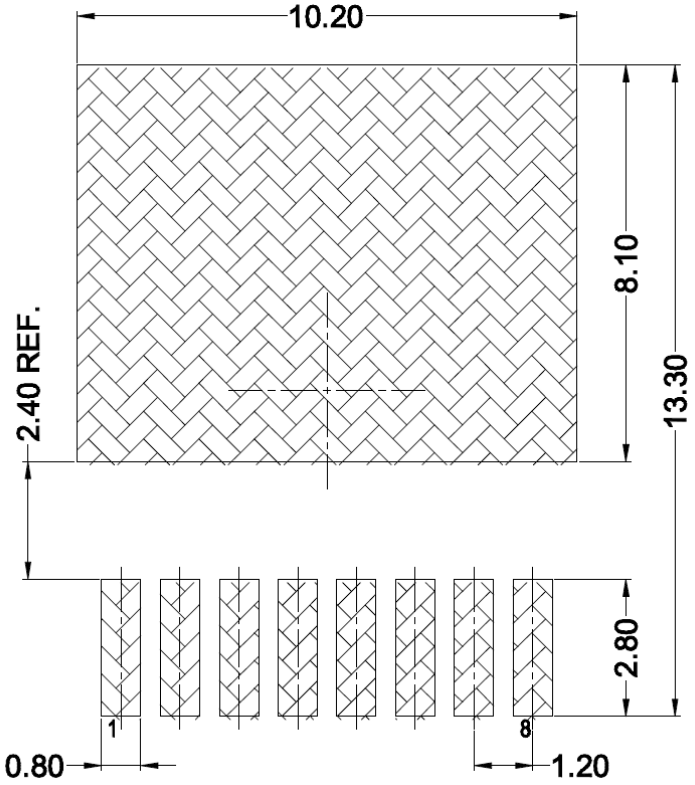
Fig.12 EAS Waveform

TOLL PACKAGE INFORMATION



SYMBOL	mm			SYMBOL	mm		
	MIN	NOM	MAX		MIN	NOM	MAX
A	2.20	2.30	2.40	H	11.58	11.68	11.78
A1	1.70	1.80	1.90	H1	6.95 BSC		
b	0.70	0.80	0.90	H2	5.89 BSC		
b1	9.70	9.80	9.90	i	0.10 REF		
b3	1.10	1.20	1.30	j	0.46 REF		
c	0.40	0.50	0.60	K	2.80 REF		
D	10.28	10.38	10.48	L	1.40	1.90	2.10
D1	10.98	11.08	11.18	L1	0.60	0.70	0.80
D2	3.20	3.30	3.40	L2	0.50	0.60	0.70
D4	4.45	4.55	4.65	L3	0.30	0.70	0.80
E	9.80	9.90	10.00	N	8		
E1	8.00	8.10	8.20	Q	8.00 REF		
E2	0.60	0.70	0.80	R	3.00	3.10	3.20
e	1.20 BSC			θ	10° REF		

TOLL RECOMMENDED LAND PATTERN



unit : mm

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